|  |  |  |  |  |
| --- | --- | --- | --- | --- |
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| 1 | [chip\_name](#1.0) |  | 0x0 |  |
| 1.1 | [b1](#1.1.0) |  | 0x0 |  |
| 1.1.1 | [rg1](#1.1.1.0) | 0x0000 | 0x0 |  |
| 1.2 | [b2](#1.2.0) |  | 0x2 |  |
| 1.2.1 | [rg1](#1.2.1.0) | 0x0000 | 0x2 |  |
| 1.2.2 | [ref\_sub\_blk](#1.2.2.0) |  | 0x4 |  |
| 1.2.2.1 | [rg1](#1.2.2.1.0) | 0x0000 | 0x4 |  |

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| 1.0 | | | chip\_name | | chip |  |
| offset | 0 | external | |  | size | 6 |
|  | | | | | | |
|  | | | | | | |
|  | | | | | | |

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| --- | --- | --- | --- | --- | --- | --- |
| 1.1.0 | | | b1 | | block |  |
| offset | 0 | external | |  | size | 2 |
|  | | | | | | |
|  | | | | | | |
| {max\_reg\_size=16} | | | | | | |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.1.1.0 | | | | | | | | rg1 | | | | | reg32 | |  |
| offset | | 0 | external | | |  | | | size | | 16 |  | |  | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0:7 | f1 | | | rw | rw | | 0x0 | | | NICE WORK | | | | | |
| 8:10 | f2 | | | rw | rw | | 0x0 | | |  | | | | | |
| 14:12 | f3 | | | rw | rw | | 0x0 | | |  | | | | | |

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| --- | --- | --- | --- | --- | --- | --- |
| 1.2.0 | | | b2 | | block |  |
| offset | 2 | external | |  | size | 4 |
|  | | | | | | |
|  | | | | | | |
| {max\_reg\_size=16} | | | | | | |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.2.1.0 | | | | | | | | rg1 | | | | | reg32 | |  |
| offset | | 0 | external | | |  | | | size | | 16 |  | |  | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0:7 | f1 | | | rw | rw | | 0x0 | | | NICE WORK | | | | | |
| 8:10 | f2 | | | rw | rw | | 0x0 | | |  | | | | | |
| 14:12 | f3 | | | rw | rw | | 0x0 | | |  | | | | | |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.2.2.0 | | | | ref\_sub\_blk | | | section | |  | |
| offset | 2 | external |  | | repeat |  | | size | | 2 |
|  | | | | | | | | | | |
|  | | | | | | | | | | |
| {type=subblock;refnodetype=section;max\_reg\_size=16} | | | | | | | | | | |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.2.2.1.0 | | | | | | | | rg1 | | | | | reg32 | |  |
| offset | | 0 | external | | |  | | | size | | 16 |  | |  | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0:7 | f1 | | | rw | rw | | 0x0 | | | NICE WORK | | | | | |
| 8:10 | f2 | | | rw | rw | | 0x0 | | |  | | | | | |
| 14:12 | f3 | | | rw | rw | | 0x0 | | |  | | | | | |

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| --- |
| End RegGroup |