|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Table of Content** | | | | |
| **Table of Content** | | | | |
| **Index** | **Component** | **Default** | **Address** | **Page** |
| 1 | [board\_name](#bookmark1) |  | 0x0 |  |
| 1.1 | [chip\_name](#bookmark2) |  | 0x0 - 0x2D |  |
| 1.1.1 | [block\_name](#bookmark4) |  | 0x0 - 0x2D |  |
| 1.1.1.1 | [reg\_name](#bookmark5) | 0x00000009 | 0x0 |  |
| 1.1.1.2 | [reg\_group\_name](#bookmark6) |  | 0x4 - 0x5 |  |
| 1.1.1.2.1 | [reg\_name2](#bookmark7) | 0x0001 | 0x4 |  |
| 1.1.1.3 | [memory\_name](#bookmark8) |  | 0x6 - 0x2D |  |

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| 1.1 chip\_name | | | chip\_name | | chip | 0x0 |
| offset |  | external | |  | size |  |
|  | | | | | | |
| oid=d675c0ae-e5f4-4d75-b315-19214c776872 | | | | | | |
|  | | | | | | |

|  |  |  |
| --- | --- | --- |
|  | enum\_name |  |
| ENUM | | |
| mnemonic name fsdfMnemonic | value | description |
| Enum1 | 1 | E |
| Enum2 | 2 |  |

|  |  |  |  |
| --- | --- | --- | --- |
| define name | value | description | private |
| DEFINE | | | |
| $p1 | 1 |  |  |
| D1 | 1 |  |  |

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| signals | | | signals | signal |  | |
|  | | | | | | |
| oid=b706fc1d-208a-4885-a537-6b0a404005b7 | | | | | | |
|  | | | | | | |
| name | port type | description | | | | |
| Sig1 | In |  | | | |  |

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| 1.1.1 block\_name | | | block\_name | | block | 0x0 |
| offset |  | external | |  | size |  |
|  | | | | | | |
| oid=3e6a03f8-11b2-41b2-b3cf-e149ab00b0a5 | | | | | | |
|  | | | | | | |

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.1.1.1 reg\_name | | | | | | | | reg\_name | | | | | reg32 | | 0x0 |
| offset | |  | external | | |  | | | size | | 32 | default | | 0x00000009 | |
|  | | | | | | | | | | | | | | | |
| oid=9a7def7c-9f29-4025-ad01-5c2c600bcf1f | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0:2 | F | | | rw |  | | $p1 | | | {next=Sig1}this is re\_name register  hello | | | | | |
| 3 | F2 | | | Rw | Rw | | D1 | | |  | | | | | |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.1.1.2 reg\_group\_name | | | | reg\_group\_name | | | section | | 0x4 | |
| offset |  | external |  | | repeat |  | | size | |  |
|  | | | | | | | | | | |
| oid=15a66179-c30f-462b-8173-8d8e367ba004 | | | | | | | | | | |
|  | | | | | | | | | | |

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.1.1.2.1 reg\_name2 | | | | | | | | | | | | | reg\_name2 | | | | | | | reg16 | | | | 0x4 | | |
| offset | | | |  | | | | | | external | | | | | | |  | | | default | | | | 0x0001 | | |
|  | | | | | | | | | | | | | | | | | | | | | | | | | | |
| oid=37441756-ca52-402b-aa93-ff3721083687 | | | | | | | | | | | | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 15 | | 14 | 13 | | 12 | | 11 | | 10 | | | 9 | | 8 | | 7 | | 6 | 5 | | 4 | 3 | 2 | | 1 | 0 |
| bits | name | | | | | s/w | | h/w | | | default | | | | description | | | | | | | | | | | |
| 15:0 | F | | | | | Rw | | Rw | | | Enum1 | | | | In this field F,  **Enum1** is applied | | | | | | | | | | | |

|  |
| --- |
| End RegGroup |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.1.1.3 memory\_name | | | | memory\_name | | | mem | | | 0x6, 0xa... |
| offset |  | depth | 10 | | width | 32 | | default | 0 | |
|  | | | | | | | | | | |
| oid=fadbfc70-ab36-40ed-af88-a17b6831f6cc | | | | | | | | | | |
|  | | | | | | | | | | |