

RAJIV GANDHI INSTITUTE OF TECHNOLOGY, MUMBAI

DEPARTMENT OF COMPUTER ENGINEERING

Professor Name : DR. S.Y. KET
Subject Name : DIGITAL LOGIC DESIGN & ANALYSIS
Year : 2019 Div : B

Question no	A	B	C	D	E	Total	Average %
1	2	3	9	6	2	22	57.27%
2	3	2	6	7	4	22	53.64%
3	1	10	4	4	3	22	61.82%
4	1	8	8	2	3	22	61.82%
5	0	6	8	5	3	22	55.45%
6	5	7	2	4	4	22	64.55%
7	17	3	2	0	0	22	93.64%
8	14	3	0	1	4	22	80%
9	17	1	4	0	0	22	91.82%
10	15	4	0	1	2	22	86.36%
11	13	4	1	3	1	22	82.73%
12	14	6	1	1	0	22	90%

Overall Average :73.26%

Student Reviews

Mam teaches well but due to lag in syllabus she tries to rush and all is messed up but she is awesome and due to massbunk some students didn't like them as per me she is proper

It feels that she doesn't have enough knowledge about the subject

Explanation of many topics are not up to the mark. often mistakes are made during teaching

Should find a better way of creating interests in students

Madam make the subject boring and no one feels excited to attend the lecture because of lack of teaching quality.

Need some progress

Her notes are good.

Need to improve teaching skills
