```
8085 Opcodes
*I
            Immediate
*X
            16 bit Register pair
; Movement
        r/M, r/M
                                  r/M, r/M
MOV
                     ; mov
MVI
        r/M, i
                                  r/M, i
                     ; mov
LDA
        addr
                     ; mov
                                  a, [addr]
        B/D
LDAX
                                  a, [BC/DE]
                     ; mov
        addr
STA
                                  [addr], a
                     ; mov
STAX
        B/D
                     ; mov
                                  [BC/DE], a
LHLD
        addr
                                  HL, [addr]
                     ; mov
                                  rp, i
LXI
        rp, i
                     ; mov
PUSH
        rp
                     ; push
                                  rp
POP
        rp
                     ; pop
                                  rp
;Arithmetic
;-----
                                  a, r/M
ADD
        r/M
                     ; add
                                  a, i
ADI
        i
                     ; add
ADC
        r/M
                                  a, r/M
                     ; adc
ACI
        i
                     ; adc
                                  a, i
DAD
                                  HL, rp
        rp
                     ; add
DAA
                     ; daa
INR
        r/M
                     ; inc
                                  r/M
INX
        rp
                     ; inc
                                  rp
DCR
        r/M
                     ; dec
                                  r/M
DCX
                     ; inc
        rp
                                  rp
;Bitwise
;----
        r/M
                                  a, r/M
ANA
                     ; and
        i
                                  a, i
ANI
                     ; and
ORA
        r/M
                                  a, r/M
                     ; or
ORI
                     ; or
                                  a, i
CMA
                                  а
                     ; not
RAL
                     ; rcl
                                  a, 1
RAR
                                  a, 1
                     ; rcr
RLC
                     ; rol
                                  a, 1
RRC
                                  a, 1
                     ; ror
;Flag Control
;-----
CMC
                                  CF
                     ; not
DI
                     ; cli
                     ; sti
ΕI
```

```
NOP
                       ; nop
HLT
                       ; hlt
RIM
                       ; mov
                                    a, IF
;Branching
CMP
         r/M
                                    a, r/M
                       ; cmp
JMP
         addr
                       ; jmp
                                    addr
JΖ
         addr
                                    addr
                       ; jz
JNZ
         addr
                       ; jnz
                                    addr
JC
         addr
                       ; jc
                                    addr
JNC
         addr
                                    addr
                       ; jnc
JP
         addr
                        jns
                                    addr
JM
         addr
                       ; js
                                    addr
PCHL
                       ; jmp
                                    [HL]
                                    addr
JPE
         addr
                       ; jnp
JPO
         addr
                       ; jp
                                    addr
CALL
         addr
                       ; call
                                    addr
CZ
         addr
                       ; callz
                                    addr
                       ; callnz
CNZ
         addr
                                    addr
CC
         addr
                       ; callc
                                    addr
         addr
                       ; callnc
{\tt CNC}
                                    addr
CP
         addr
                       ; callns
                                    addr
CM
         addr
                       ; calls
                                    addr
CPE
         addr
                       ; callnp
                                    addr
         addr
CPO
                       ; callp
                                    addr
RET
                       ; ret
RΖ
                       ; retz
RNZ
                       ; retnz
RC
                        retc
RNC
                       ; retnc
RP
                       ; retns
RM
                        rets
RPE
                       ; retnp
RPO
                       ; retp
PUSH
         PSW
                       ; push
                                    psw
POP
         PSW
                       ; pop
                                    psw
; I/O
;---
IN
         addr
                       ; in
                                    a, addr
OUT
         addr
                                    addr, a
                       ; out
```