



One-day-workshop on Introduction to Deep Learning using Tensorflow.

20th January 2019 (Sunday) 9:30 AM – 5: 30 PM
ECE Department, Indian Institute of Science

Introduction to TensorFlow



- Computational Graph
- Session and Variable Types
- **Hands-On-Coding** with Toy Example
- Using **Keras** for building Models
- Visualizing model via **Tensorboard**

Schedule:

09:00 - 09: 30 AM : Workshop Registration
09:30 - 10: 15 AM : Deep Learning: Motivation.
10:15 - 10: 30 AM : Tea/ Snacks.
10:30 - 11: 45 AM : Deep neural network fundamentals.
12:00 - 01: 15 PM : Various DNN architectures.
01:15 - 02: 00 PM : Lunch
02:00 - 03: 00 PM : Deep Learning and Tensorflow Part 1
03:00 - 04: 00 PM : Deep Learning and Tensorflow Part 2
04:00 - 04: 15 PM : Evening Tea/ Snacks.
04:15 - 05: 30 PM : Deep Learning and Tensorflow Part 3

Registration Fee*:

Non-IEEE Normal: Rs. 2000

Non-IEEE (Students/Faculty/Startup): Rs 1500

IEEE members: Rs. 1200

*The registration includes kit, lunch/tea etc, A participation certificate will be given

Coordinator:

Dr. T. Srinivas, Branch counselor, (tsrinu.iisc@gmail.com)

Kundan Kumar, IEEE-IISc, (kundankumar@ieee.org)

Ritesh Kumar, IEEE-IISc, (riteshkumar@iisc.ac.in)

Registration Link:

http://bit.ly/TensorFlow_IISc



Lead Speakers:



Sarthak Gupta: Sarthak Gupta is currently Graduate student at IISc, Bangalore in Electronic Systems Engineering. He has worked in Texas Instruments, Bangalore as Design Engineer for approx 2 years. His research interest include hardware implementation of machine learning algorithms. **He has also authored book “Getting Started with Tiva ARM Cortex M4 Microcontrollers”** published by Springer Publication. He is currently working on the development of chip for recurrent neural networks with on-chip online and local learning. He is experienced in taking the Deep learning workshop.

Saurabh Gupta: has worked in Bharat Electronics Ltd. Bangalore as design engineer for 3 years. His research interest include deep learning for biomedical applications, mixed mode VLSI circuit design, processor and FPGA designs and software design for communication networks. He has authored paper in VLSID 2019 conference. He is also selected among 10 international candidates to represent at Youth Workshops in computational science and technology at **RIKEN Center for Computational Science (R-CCS) , Kobe, Japan.** He is currently working on development of low cost portable Ultrasound imaging system designed specifically to address health care issues in developing countries.

