

# Appendix

## Reviewer: 1

*I do appreciate the revised manuscript and the explanations given in the cover letter. For the newly added complexity of exploring mappings, I'd suggest to support the mathematical derivation through references. (I also understand about space and references limitations w/ ESL. If needed, please consider "outsourcing" references to all the prior art you studied, mentioned as >100 papers in the cover letter, to some webpage of yours.)*

**Response:** We would like to thank the honourable reviewer for their valuable remark. We have assembled all the papers that we studied in a separate GitHub webpage- <https://github.com/nivi1501/ESL/>.

The mathematical derivation of address mapping complexity is a novel concept. Due to constraints on space, the manuscript does not detail all intermediate steps of the derivation. We present two detailed proves to support our argument:

**Lemma 1:**  $I(K; Y|T) \leq I(X; Y|T)$

**Proof:** Given the plaintext  $T$ , key  $K$ , we have a Markov chain  $\langle \text{key}(K), T \rangle \rightarrow X \rightarrow Y$  [1]. This will lead us to  $I(K; Y|T) = I(X; Y|T)$  (see Lemma 6 in [1]). This basically means that the MI between  $X$  and  $Y$  determines the amount of information that can be extracted out of  $Y$  about the key. The two assumptions are that this is a Markov process and the noise distribution  $N$  is independent of the key,  $K$ .

In the generic case, we can use the data processing inequality in information theory and write  $I(K; Y|T) \leq I(X; Y|T)$  (derived from Eqn.8 in [1]).

This means that  $I(K; Y|T)$  establishes a lower bound on  $I(X; Y|T)$  and it suits us best if we minimize  $I(K; Y|T)$ , which can be achieved by not using the key  $K$  to transform  $X$  to  $Y$ . We need to use a *separate process* to minimize  $I(X; Y|T)$  that could possibly rely on a different key  $K'$ .

Now, given that both  $X$  and  $Y$  have the same domain, the mapping of  $X$  to  $Y$  ( $g(X) = Y$ ) needs to be a bijection, specifically a 1-way permutation, where it is hard to compute  $g^{-1}$  [2] without any side information.  $g$  needs to be a bijection because there has to be a one-to-one correspondence between  $X$  and  $Y$ , which are memory addresses. A compute and storage-efficient version of this is precisely defined to be a *block cipher* !!! Now, let us examine the mapping options for the T-table.

**Lemma 2:** *An adversary needs to try out  $n^{O(\log \log n)}$  candidate mappings to find out the correct  $X - Y$  mapping.*

**Proof:** Algorithms such as AES have an interesting property -for accessing T-tables or S-boxes we need to XOR plain text bytes with key bytes. A XOR operation preserves the Hamming distance (for the same key).

Section 3.2 of Reference [3] emphasizes this point. We have exploited this dependency in our idea. The mathematical derivations are straightforward and are described in detail below. We can represent T-table entries as a hypercube. A hypercube with  $n$  vertices  $n(\log n)!$  labellings. We apply sterling approximation which is:

$$n! = \sqrt{2\pi n} \left(\frac{n}{e}\right)^n \quad (1)$$

Using this we derive  $\log n!$  as:

$$(\log n)! = \sqrt{2\pi \log n} \left(\log\left(\frac{n}{e}\right)\right)^{\log n} \quad (2)$$

Additionally:

$$(\log n)^{\log n} = n^{\log \log n} \quad (3)$$

Taking log on both sides of the above equation gives the same expression:

$$\log n \cdot \log(\log n) = \log \log n \cdot \log n \quad (4)$$

$$e^{\log_2 n} = e^{\ln n \cdot \log_e 2} = n^{\ln 2} \quad (5)$$

Putting these values in Equation 2, we get:

$$(\log n)! = \sqrt{2\pi \log n} \frac{n^{\log \log n}}{n^{\ln 2}} \quad (6)$$

As  $\sqrt{\log n} < n$  and emitting constants, we conclude that the  $(\log n)!$  grows with a complexity of  $O(n^{\log \log n})$

## References

- [1] E. de Chérisey *et al.*, “Best information is most successful,” *CHES*, 2019.
- [2] L. Batina *et al.*, “Mutual information analysis: a comprehensive study,” *J. of Cryptology*, 2011.
- [3] R. Spreitzer and T. Plos, “Cache-access pattern attack on disaligned aes t-tables,” in *International Workshop on Constructive Side-Channel Analysis and Secure Design*. Springer, 2013, pp. 200–214.