

# ATMega8

3 PWM channels

Timer

Mode of operation

Disable all channels  
16 bit Phase+Frequency correct  
8 bit Phase Correct  
9 bit Phase Correct  
10 bit Phase Correct  
16 bit Phase correct  
8 bit Fast PWM  
9 bit Fast PWM  
10 bit Fast PWM  
16 bit Fast PWM

Register  
Bit

Timer 1 - 16 bit				
TCCR1B	TCCR1B	TCCR1A	TCCR1A	Value for
WGM13	WGM12	WGM11	WGM10	TOP
0	0	0	0	65535
1	0	0	0	ICR1
0	0	0	1	255
0	0	1	0	511
0	0	1	1	1023
1	0	1	0	ICR1
0	1	0	1	255
0	1	1	0	511
0	1	1	1	1023
1	1	1	0	ICR1

Timer 2 - 8 bit

TCCR2	TCCR2	Value for
WGM21	WGM20	TOP
0	0	255
0	1	255
1	1	255

Pre-scaler

Disable all channels  
Clock divided by 1  
Clock divided by 8  
Clock divided by 32  
Clock divided by 64  
Clock divided by 128  
Clock divided by 256  
Clock divided by 1024

Register  
Bit

TCCR1B	TCCR1B	TCCR1B
CS12	CS11	CS10
0	0	0
0	0	1
0	1	0
0	1	1
1	0	0
1	0	1

TCCR2	TCCR2	TCCR2
CS22	CS21	CS20
0	0	0
0	0	1
0	1	0
0	1	1
1	0	0
1	0	1
1	1	0
1	1	1

Compare output modes

Turn this channel off  
Non-inverting  
Inverting

Register  
Bit

Channel A		Channel B	
TCCR1A	TCCR1A	TCCR1A	TCCR1A
COM1A1	COM1A0	COM1B1	COM1B0
0	0	0	0
1	0	1	0
1	1	1	1

The only channel	
TCCR2	TCCR2
COM21	COM20
0	0
1	0
1	1

Comparator Value

Register

OCR1A (16 bit)	OCR1B (16 bit)
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OCR2 (8 bit)
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I/O pin for PWM output

OC1A	OC1B
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OC2
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