

IRF730B/IRFS730B

400V N-Channel MOSFET

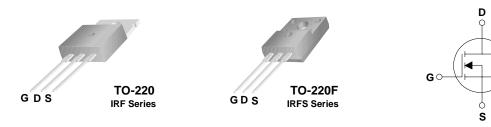
General Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supplies and electronic lamp ballasts based on half bridge.

Features

- 5.5A, 400V, $R_{DS(on)} = 1.0\Omega$ @V_{GS} = 10 V Low gate charge (typical 25 nC)
- Low Crss (typical 20 pF)
- Fast switching
- 100% avalanche tested
- · Improved dv/dt capability



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		IRF730B	IRFS730B	Units
V _{DSS}	Drain-Source Voltage		400		V
I _D	Drain Current - Continuous (T _C = 25°C)		5.5	5.5 *	Α
	- Continuous (T _C = 100°C)		3.5	3.5 *	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	22	22 *	Α
V _{GSS}	Gate-Source Voltage		± 30		V
E _{AS}	Single Pulsed Avalanche Energy (Note		330		mJ
I _{AR}	Avalanche Current	(Note 1)	5.5		Α
E _{AR}	Repetitive Avalanche Energy (Not		7.3		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		5.5		V/ns
P_D	Power Dissipation (T _C = 25°C)		73	38	W
	- Derate above 25°C		0.58	0.3	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300		°C

^{*} Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	IRF730B	IRFS730B	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case Max.	1.71	3.31	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient Max.	62.5	62.5	°C/W

Symbol	Parameter	Test Conditions	S	Min	Тур	Max	Units
Off Cha	racteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		400			V
ΔBV_{DSS} / ΔT_{J}	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced	to 25°C	1	0.4		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 400 V, V _{GS} = 0 V				10	μΑ
		V _{DS} = 320 V, T _C = 125°C)			100	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$				100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$		-		-100	nA
On Cha	racteristics						
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 2.75 \text{ A}$			0.83	1.0	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 2.75 A	(Note 4)		4.5		S
C _{iss}	Input Capacitance Output Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz			790 80	1000	pF pF
C _{oss}	Output Capacitance				80	100	pF
C _{rss}	Reverse Transfer Capacitance				20	26	pF
Switchi	ng Characteristics						
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 200 \text{ V, } I_{D} = 5.5 \text{ A,}$ $R_{G} = 25 \Omega$			15	40	ns
t _r	Turn-On Rise Time				55	120	ns
t _{d(off)}	Turn-Off Delay Time	11.6 - 20 22			85	180	ns
t _f	Turn-Off Fall Time		(Note 4, 5)	-	50	110	ns
Qg	Total Gate Charge	V _{DS} = 320 V, I _D = 5.5 A,			25	33	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V			4.3		nC
Q _{gd}	Gate-Drain Charge		(Note 4, 5)		11		nC
Drain-S	ource Diode Characteristics a	nd Maximum Rating	s				
I _S	Maximum Continuous Drain-Source Diode Forward Current					5.5	Α
I _{SM}	Maximum Pulsed Drain-Source Diode F	Forward Current				22	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = 5.5 \text{ A}$				1.5	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_S = 5.5 \text{ A,}$ $dI_F / dt = 100 \text{ A/}\mu\text{s}$ (Note 4)		1	265		ns
Q _{rr}	Reverse Recovery Charge				2.32		μC

- **Notes:**1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L = 19mH, I_{AS} = 5.5A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C 3. I_{SD} \leq 5.5A, di/dt \leq 300A/μs, V_{DD} \leq BV_{DSS}, Starting T_J = 25°C 4. Pulse Test : Pulse width \leq 300 μ s, Duty cycle \leq 2% 5. Essentially independent of operating temperature

Typical Characteristics

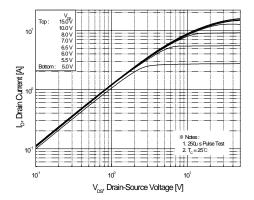


Figure 1. On-Region Characteristics

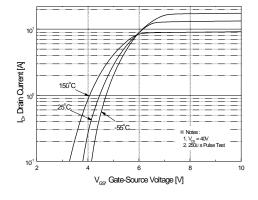


Figure 2. Transfer Characteristics

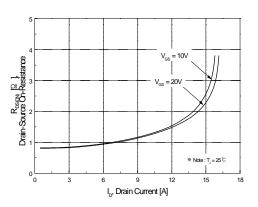


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

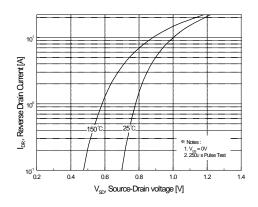


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

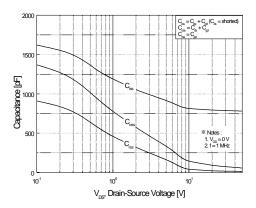


Figure 5. Capacitance Characteristics

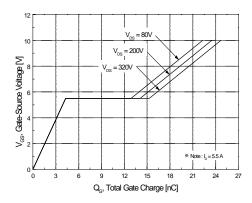


Figure 6. Gate Charge Characteristics

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Typical Characteristics (Continued)

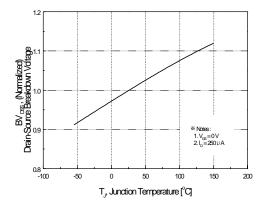


Figure 7. Breakdown Voltage Variation vs Temperature

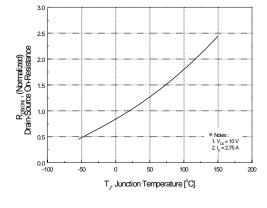


Figure 8. On-Resistance Variation vs Temperature

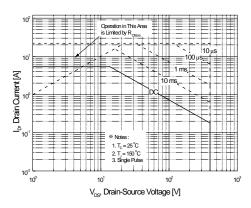


Figure 9-1. Maximum Safe Operating Area for IRF730B

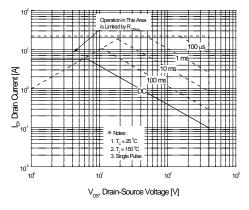


Figure 9-2. Maximum Safe Operating Area for IRFS730B

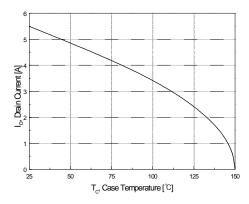


Figure 10. Maximum Drain Current vs Case Temperature

Typical Characteristics (Continued)

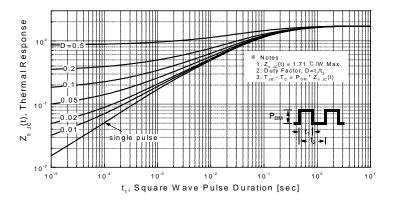


Figure 11. Transient Thermal Response Curve for IRF730B

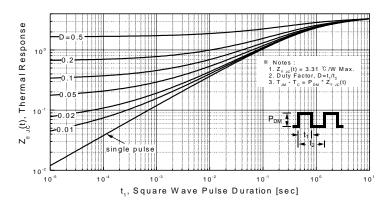
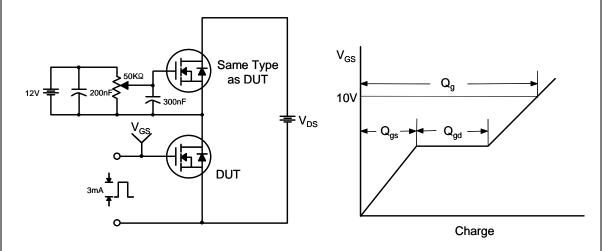
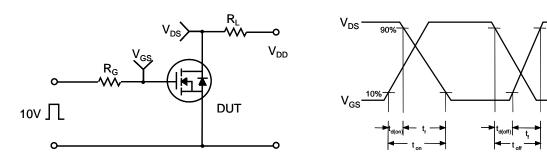


Figure 11-2. Transient Thermal Response Curve for IRFS730B

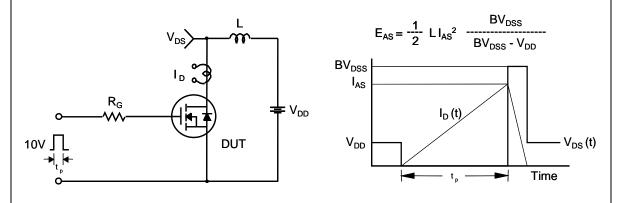
Gate Charge Test Circuit & Waveform



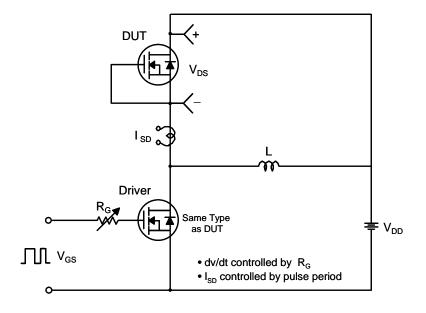
Resistive Switching Test Circuit & Waveforms



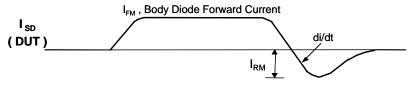
Unclamped Inductive Switching Test Circuit & Waveforms



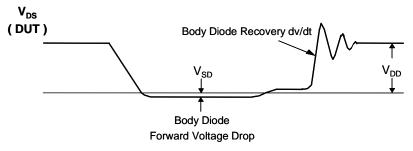
Peak Diode Recovery dv/dt Test Circuit & Waveforms

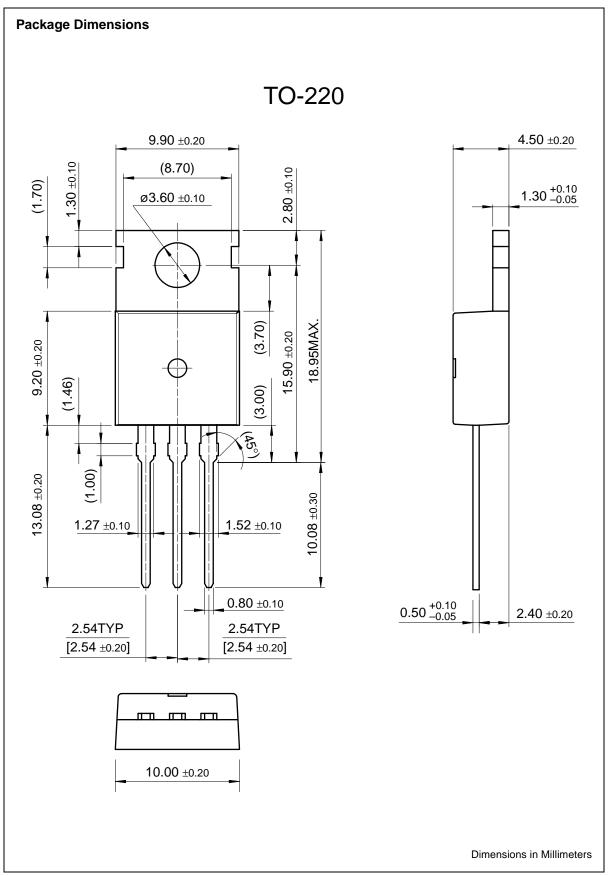


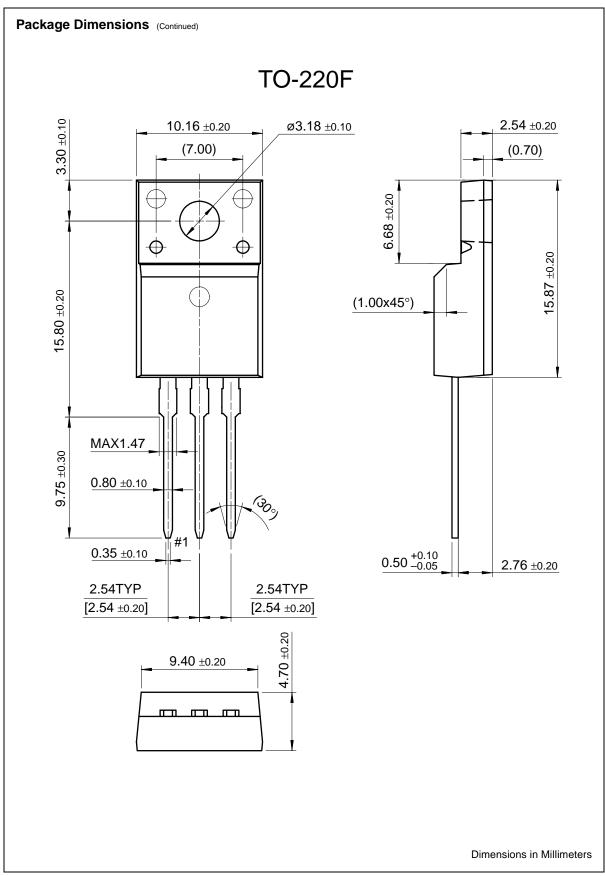




Body Diode Reverse Current







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