

# 数字电子技术第十一作业

74解：

$$\text{易得 } F_1 = \overline{A}\overline{B} + \overline{A}B + AB \quad F_2 = \overline{A}\overline{B} + A\overline{B} + AB$$

75解 将表达式分解为最小项之和

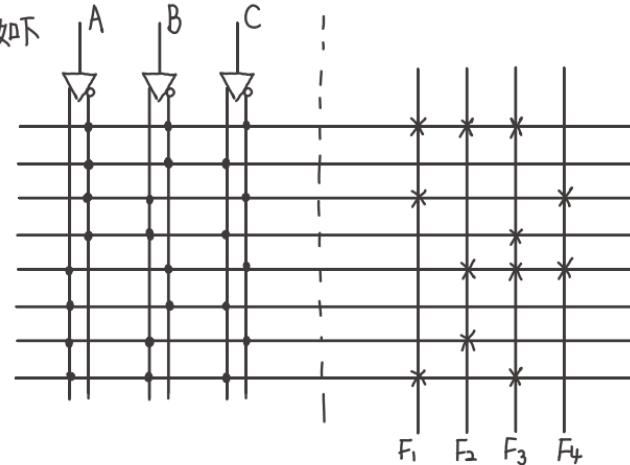
$$F_1 = \overline{A}\overline{C} + ABC = \overline{A}\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C} + ABC$$

$$F_2 = \overline{B}\overline{C} + A\overline{C} = A\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C} + A\overline{B}\overline{C}$$

$$F_3 = \overline{B}\overline{C} + BC = \overline{A}\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C} + ABC + \overline{A}BC$$

$$F_4 = \overline{A}BC + A\overline{B}\overline{C}$$

电路如下



7.8解 二进制码和格雷码转换关系如下

二进制码  $B_3 B_2 B_1 B_0$  | 格雷码  $G_3 G_2 G_1 G_0$  | 等效

0000	0000	1000	1100
0001	0001	1001	1101
0010	0011	1010	1111
0011	0010	1011	1110
0100	0110	1100	1010
0101	0111	1101	1011
0110	0101	1110	1001
0111	0100	1111	1000

卡诺图化简后可得

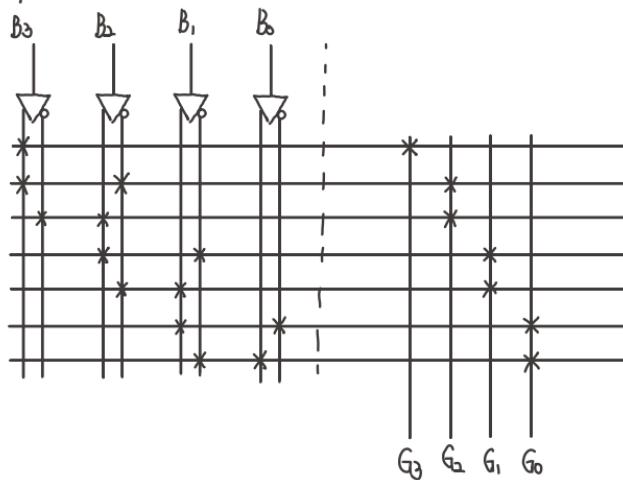
$$G_0 = B_3 \overline{B_0} + \overline{B_1} B_0$$

$$G_2 = B_3 \overline{B_2} + \overline{B_3} B_2$$

$$G_1 = B_3 \overline{B_1} + \overline{B_3} B_1$$

$$G_3 = B_3$$

PLA电路如下

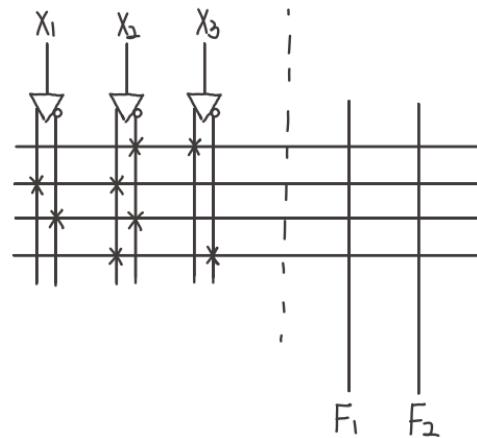


710解

卡诺图化简后得

$$F_1 = X_3 \overline{X_2} + X_2 X_1 \quad F_2 = \overline{X_1} \overline{X_3} + X_1 X_2 + \overline{X_3} X_2$$

PLA电路图如下



79解 列出状态表

输入 X	现态 $Q_2^n$		次态 $Q_2^{n+1}$		输出 Z
	$Q_1^n$	$Q_2^n$	$Q_2^{n+1}$	$Q_1^{n+1}$	
0	0	0	0	1	0
0	0	1	1	0	0
0	1	0	1	1	0
0	1	1	0	0	1
1	1	1	1	0	0
1	1	0	0	1	0
1	0	1	0	0	0
1	0	0	1	1	1

$$Q_2^{n+1} = X \oplus Q_1^n \oplus Q_2^n \quad Q_1^{n+1} = \overline{Q_1^n} \quad Z = \overline{X} Q_2^n Q_1^n + X \overline{Q_2^n} \overline{Q_1^n}$$

$$\therefore J_1 = K_1 = 1 \quad J_2 = K_2 = X \overline{Q_1^n} + \overline{X} Q_1^n$$

PLA 电路图如下

