

# **DSP/BIOSTM Link**

**Platform Guide** 

1.65

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# **Read This First**

#### **About This Manual**

This document describes platform specific information need for OMAPL138 platforms.

#### **How to Use This Manual**

This document includes the following chapters:

Please go through the Release Notes document available in the release package before starting the installation.

#### **Notation of information elements**

The document may contain these additional elements:



#### Warning

This is an example of warning message. It usually indicates a non-recoverable change.



#### Caution

This is an example of caution message.



#### **Important**

This is an example of important message.



#### **Note**

This is an example of additional note. This usually indicates additional information in the current context.



#### Tip

This is an example of a useful tip.



## If You Need Assistance

For any assistance, please send an mail to software support.

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# **Platform Guide for OMAPL138**

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## 1.1. Introduction

## 1.1.1. Purpose and Scope

DSP/BIOS<sup>TM</sup> LINK is foundation software for the inter-processor communication across the GPP-DSP boundary. It provides a generic API that abstracts the characteristics of the physical link connecting GPP and DSP from the applications. It eliminates the need for customers to develop such link from scratch and allows them to focus more on application development.

This document provides the users necessary information to install DSP/BIOS $^{\text{TM}}$  LINK on the development host.

This document corresponds to the product release Version 1.65.

#### 1.1.2. Terms and Abbreviations

CCS	Code Composer Studio
IPC	Inter Processor Communication
GPP	General Purpose e.g. ARM
DSP	Digital Signal Processor e.g. DM6437
DSPLink	A generic term used for DSP/BIOS $^{\rm TM}$ Link. It appears in italics in all usages
CGTools	Code Gen Tools, e.g. Compiler, Linker, Archiver

**Table 1.1. Terms and Abbreviations** 

#### 1.1.3. References

1	User Guide
2	InstallGuide_Linux_OMAPL138.pdf
3	Porting Guide

Table 1.2. References



# 1.2. Configuring CCS

# 1.2.1. Configuring CCS for OMAPL138

To use CCS for debugging the DSP side application, you will need to configure CCS to use both ARM and DSP with the OMAPL138 EVM.



# 1.3. Platform specific information

## 1.3.1. Boot mode support

 $DSP/BIOS^{TM}$  LINK supports both ARM – boot mode as well as DSP – boot mode on OMAPL138.



## 1.4. ADDITIONAL INFORMATION

## 1.4.1. Read write samples

The addresses to be passed as parameters for readwrite samples are platform specific.

Read write sample can be used for addresses in DDR, GEM L1D RAM and L2 RAM on OMAPL138 platform.

