

Virtual Address

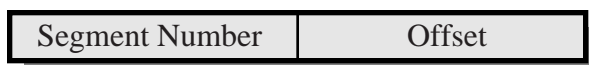


Page Table Entry



(a) Paging only

Virtual Address



Segment Table Entry



(b) Segmentation only

Virtual Address



Segment Table Entry



Page Table Entry



P= present bit
M = Modified bit

(c) Combined segmentation and paging

Figure 8.2 Typical Memory Management Formats