48

```
`timescale 1ns / 1ns // `timescale time_unit/time_precision
     3
                             //SW[3:0] data inputs
     4
                             //HEXO output display
      5
     6
                           module HEXdecoder(HEX0, SW);
    7
                                                  input [3:0] SW;
output [6:0] HEXO;
    8
    9
10
                                                  HEXdefinition hex0(
                                                                          .x1(SW[0]),
11
                                                                          .x2(SW[1])
12
                                                                          .x3(SW[2]),
13
14
                                                                          .x4(SW[3])
15
                                                                          .y0(HEX0[0]),
                                                                          .y1(HEX0[1]),
.y2(HEX0[2]),
16
17
                                                                          .y3(HEX0[3]),
18
                                                                          .y4(HEX0[4]),
.y5(HEX0[5]),
19
20
 21
                                                                           .y6(HEX0[6])
22
23
24
                            endmodule
25
26
27
                           module HEXdefinition (x1, x2, x3, x4, y0, y1, y2, y3, y4, y5, y6);
                                                   input x1; //input1
                                                   input x2; //input2
28
29
                                                   input x3; //input3
30
                                                   input x4; //input4
31
                                                  output y0; //output0
32
                                                  output y1; //output1
33
                                                  output y2; //output2
                                                 output y3; //output3
output y4; //output4
output y5; //output5
output y6; //output6
34
35
 36
37
38
                                                   assign y0 = ((-x1 \& -x2 \& -x3 \& x4) | (-x1 \& x2 \& -x3 \& -x4) | (x1 \& -x2 \& x3 \& x4) | (
39
                           x1 \& x2 \& \sim x3 \& x4)); //eq0
40
                                                   assign y1 = ((\sim x1 \& x2 \& \sim x3 \& x4) | (\sim x1 \& x2 \& x3 \& \sim x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& \sim x2 \& x3 \& x4) | (x1 \& x3 \& x4) | (x1 \& x3 \& x4) | (x1 \& x3 \& x3 \& x4) | (x1 \& x3 \& x3 \&
                                  & x2 & ~x3 & ~x4) | (x1 & x2 & x3 & ~x4) | (x1 & x2 & x3 & ~x4) | (x1 & x2 & x3 & x4));
41
                                                   assign y2 = ((x1 \& x2 \& x3 \& x4) | (x1 & x2 & x4) | (x1 & x2 & x3 & x4) | (x1 & x3 & x
                            & x3 & x4)); //eq2
42
                                                   & ~x2 & x3 & ~x4) | (x1 & x2 & x3 & x4)); //eq3
assign y4 = ((~x1 & ~x2 & ~x3 & x4) | (~x1 & ~x2 & x3 & x4) | (~x1 & x2 & ~x3 & ~x4) | (~x1
43
                                 & x2 & ~x3 & x4) | (~x1 & x2 & x3 & x4) | (x1 & ~x2 & ~x3 & x4)); //eq4
assign y5 = ((~x1 & ~x2 & ~x3 & x4)|(~x1 & ~x2 & x3 & ~x4)|(~x1 & ~x2 & x3 & x4)|(~x1 & ~x3 & x3 & x3 & 
44
                           45
                            x2 & ~x3 & ~x4)); //eq6
46
47
                            endmodule
```