	inpu	ıt		expected	segment									
x1	x2	х3	х4	output	0	1	2	3	4	5	6			
0	0	0	0	0	0	0	0	0	0	0	1			
0	0	0	1	1	1	0	0	1	1	1	1			
0	0	1	0	2	0	0	1	0	0	1	0			
0	0	1	1	3	0	0	0	0	1	1	0			
0	1	0	0	4	1	0	0	1	1	0	0			
0	1	0	1	5	0	1	0	0	1	0	0			
0	1	1	0	6	0	1	0	0	0	0	0			
0	1	1	1	7	0	0	0	1	1	1	1			
1	0	0	0	8	0	0	0	0	0	0	0			
1	0	0	1	9	0	0	0	0	1	0	0			
1	0	1	0	Α	0	0	0	1	0	0	0			
1	0	1	1	b	1	1	0	0	0	0	0			
1	1	0	0	С	0	1	1	0	0	0	1			
1	1	0	1	d	1	0	0	0	0	1	0			
1	1	1	0	E	0	1	1	0	0	0	0			
1	1	1	1	F	0	1	1	1	0	0	0			

☐ /HEXdecoder/SW	0110	0000	1000	0100	1100	0010	1010	0110	
- 4 [3]	St0								
-4 [2]	St1								
− 4 [1]	St1								
└ <u></u> � [0]	St0								
☐ ♦ /HEXdecoder/H		1000000	1111001	0100100	0110000	0011001	0010010	0000010	
− ♣ [6]	St0								
−⇔ [5]	St0								
−♦ [4]	St0								
-4 [3]	St0								
−⇔ [2]	St0								
⊢ ♣ [1]	St1								
_ 	St0								