*Resume*

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## Education

AUG, 2014 – NOW

- University of Science and Technology of China
- Ph.D. Candidate Microelectronics
- Research Focus: Reconfigurable Computing

AUG, 2012 – JUN, 2014

- University of Science and Technology of China
- Master Candidate Microelectronics
- Research Focus: Reconfigurable Computing

AUG, 2008 - JUN, 2012

- University of Science and Technology of China
- B.S. Applied Physics

## Awards and Group Memberships

- First-class Graduate School Scholarship 2015 – 2016
  University of Science and Technology of China
- First-class Graduate School Scholarship 2014 – 2015
  University of Science and Technology of China
- Second Prize Chinese Graduate Students electronic design contest 2012
  Chinese Institute of Electronics

## Publications

Xiang, T., Zhao, L., Jin, X., **Wang, T.**, Chu, S., Ma, C., ... & An, Q. (2014, May). A 56-ps multi-phase clock time-to-digital convertor based on Artix-7 FPGA. In *Real Time Conference (RT), 2014 19th IEEE-NPSS* (pp. 1-4). IEEE.

Xiang, T., Zhao, L., Jin, X., **Wang, T**., Chu, S., Ma, C., ... & Ben, X. (2014, May). A Multi-phase Clock Time-to-Digital Convertor Based on ISERDES Architecture. In *Field-Programmable Custom Computing Machines (FCCM), 2014 IEEE 22nd Annual International Symposium on* (pp. 35-35). IEEE.

Peng, B., Jin, X., **Wang, T**., & Du, X. (2015, May). Design of a Distributed Compressor for Astronomy SSD. In Field-Programmable Custom Computing Machines (FCCM), 2015 IEEE 23rd Annual International Symposium on (pp. 98-98). IEEE.

**Wang, T**., Peng, B., & Jin, X. (2016, February). An Extensible Heterogeneous Multi-FPGA Framework for Accelerating N-body Simulation. In Proceedings of the 2016 ACM/SIGDA International Symposium on Field-Programmable Gate Arrays (pp. 277-277). ACM.

**Wang, T**., Peng, B., & Jin, X. (2016, May). RP-ring: A Heterogeneous multi-FPGA Accelerating Solution for N-body Simulations. In Field-Programmable Custom Computing Machines (FCCM), 2016 IEEE 24th Annual International Symposium. IEEE