

Study of the effective mass dependent charge qubit performance in voltage-tunable double quantum dot channel nanowire FETs

Nilayan Paul¹, Sanatan Chattopadhyay^{1,2,*}

¹Department of Electronic Science, University of Calcutta, Kolkata, India

²Centre for Research in Nanoscience and Nanotechnology, University of Calcutta, Kolkata, India

*Corresponding author: scelec@caluniv.ac.in

Abstract:

Charge qubits in a double quantum dot (DQD) architecture [1] are attractive for quantum computers with simple operation [2] and potential compatibility with the existing CMOS technologies. However, short dephasing times (~ 1 -10 ns) [3] and \sim mK operational temperatures challenge such qubit operation. Therefore, exploration of novel device schemes in the existing state-of-the-art CMOS architectures [4-5] is the need of the hour. Recently, a GaAs nanowire FET, with two separated gates (Fig. 1(a)), has been proposed for charge qubit operation at room temperature [6] and the scheme shown in Fig. 1(b). The current work theoretically studies the effective mass dependent performance of such qubit device in terms of Bloch sphere coverage, charge stability and dephasing times by employing the NEGF approach for transport modeling. Increasing effective mass in the transport direction (m_z^*) from 0.04 to 0.1 increases dephasing time from ~ 40 ns to 120 ns (Fig. 2(a)), whereas it degrades anticrossing energy from ~ 20 meV to ~ 5 meV (Fig. 2(b)), in line with experimental reports [7]. The Bloch sphere coverage and charge stability diagrams are depicted in Fig. 3(a)–(d) and (e)–(h), respectively.

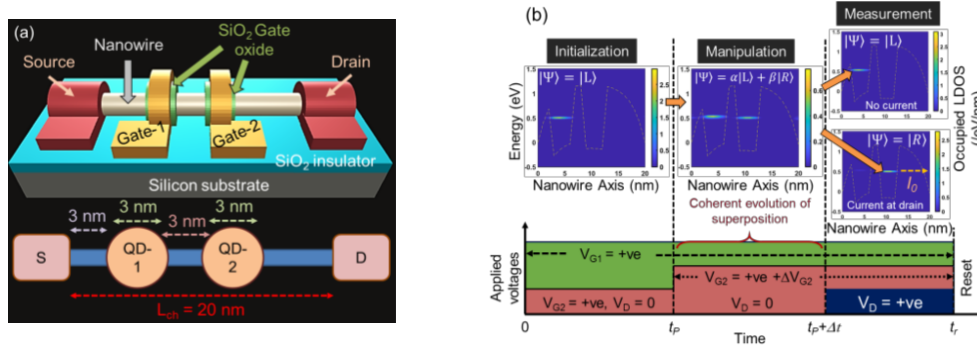


Fig. 1(a) Schematic of the charge qubit device; **(b)** Scheme of charge qubit operation.

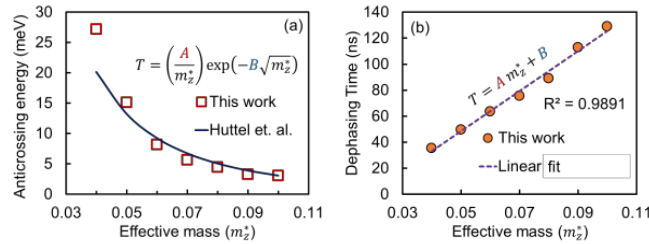


Fig. 2 Change in **(a)** anticrossing energy; **(b)** Dephasing time for the change in m_z^* from $0.04m_0$ to $0.10m_0$.

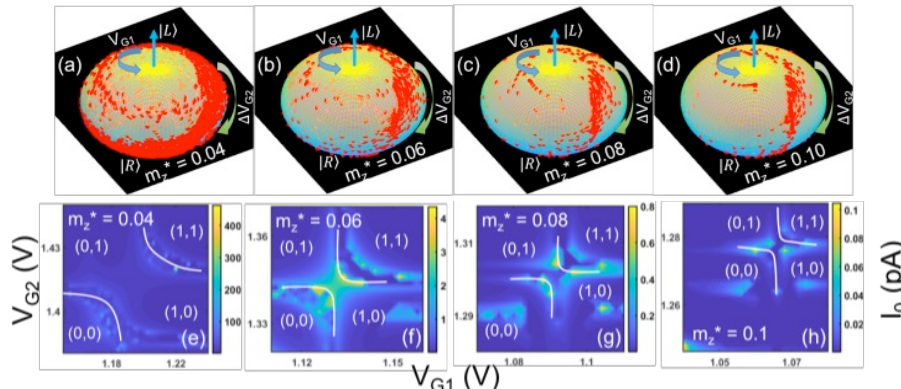


Fig. 3(a) – (d) Bloch sphere coverage; **(e) – (h)** Charge stability diagrams for m_z^* in the range of $0.04m_0$ to $0.10m_0$.

References:

- [1] A. Chatterjee, P. Stevenson, S. De Franceschi, A. Morello, N. P. de Leon, F. Kuemmeth, "Semiconductor qubits in practice", *Nat. Rev. Phys.* 3(3), 157-177 (2021).
- [2] T. Hayashi, T. Fujisawa, H. D. Cheong, Y. H. Jeong, Y. Hirayama, "Coherent Manipulation of Electronic States in a Double Quantum Dot", *Phys. Rev. Lett.* 91(22), 226804 (2003).
- [3] K. D. Petersson, J. R. Petta, H. Lu, A. C. Gossard, "Quantum Coherence in a One-Electron Semiconductor Charge Qubit", *Phys. Rev. Lett.* 105(24), 246804 (2010).
- [4] S. M. Frolov, S. R. Plissard, S. Nadj-Perge, L. P. Kouwenhoven, E. P. A. M. Bakkers, "Quantum computing based on semiconductor nanowires", *MRS Bulletin*, 38(10), 809-815 (2013).
- [5] A. V. Kuhlmann, V. Deshpande, L. C. Camenzind, D. M. Zumbühl, A. Fuhrer, "Ambipolar quantum dots in undoped silicon fin field-effect transistors", *Appl. Phys. Lett.* 17, 113 (12), 122107 (2018).
- [6] B. Nag. Chowdhury, S. Chattopadhyay, "Dual-Gate GaAs-Nanowire FET for Room Temperature Charge-Qubit Operation: A NEGF Approach", *Adv. Quantum Technol.*, 6(4), 2200072 (2023).
- [7] A. K. Hüttel, S. Ludwig, H. Lorenz, K. Eberl, J. P. Kotthaus, "Direct control of the tunnel splitting in a one-electron double quantum dot", *Phys. Rev. B* 72, 081310(R) (2005).