



		_	-			_	_	_	_	_	_	_
/testbench/rout8_tb 0												1
/testbench/rout9_tb 0												1
/testbench/routa_tb 0												1
/testbench/routb_tb 0											<u> </u>	
/testbench/routc_tb 0							†					_
/testbench/routd_tb 0										+	†	
/testbench/route_tb 0						+	+	-	+	+	+	
/testbench/routf_tb 0						+				+	+	₫
/testbench/rout0_tb 0												₫
/testbench/rout1_tb 0	26214									(21		₫
/testbench/busmuxout_tb 0	26214	0 (21	0	\d \(\)0	0		(4	0 (21	0 (4	0 (21	(0	₫
/testbench/PCout_tb 0												
/testbench/Zhigh_tb 0						-						
/testbench/LOout_tb 0						-						_
/testbench/HIout_tb 0						-			-	+		
/testbench/MDRout_tb 0	26214	(21		(4		(4			-	+		_
/testbench/routy_tb 0						-		(21	-	-	-	}
/testbench/Zlow_tb 0									(21			7
/testbench/Present_state d		d1 (Reg_l	load2	Reg_load3	3 \(\(\tau\)	T1	T2	T3)T4) T5		7
· <u>-</u>							,					
0 ps			500	000 ps	1 1	1000	000 ps	ilii	150000 ps		200	0000
5 PS			555	oo po		1000	00 po		200000 po			,,,,