

General test specification:

- Completely test the ALU functionality.
- Execute all lines of the ALU code.

Detailed test specification:

A. Operations & sequences

1. Test all operations.
2. Test operations with incorrect ALU operation code.
3. Test operations with incorrect number of data's bits.
4. Test operations with incorrect CRC.

B. Specific data corners

1. Simulate all zeros on an input for all operations.
2. Simulate all ones on an input for all operations.

<https://github.com/nmalota/VDIC/tree/lab01/lab01>