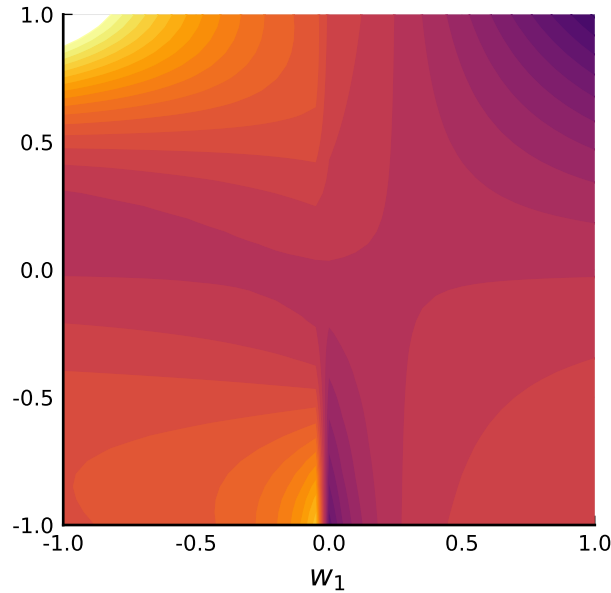
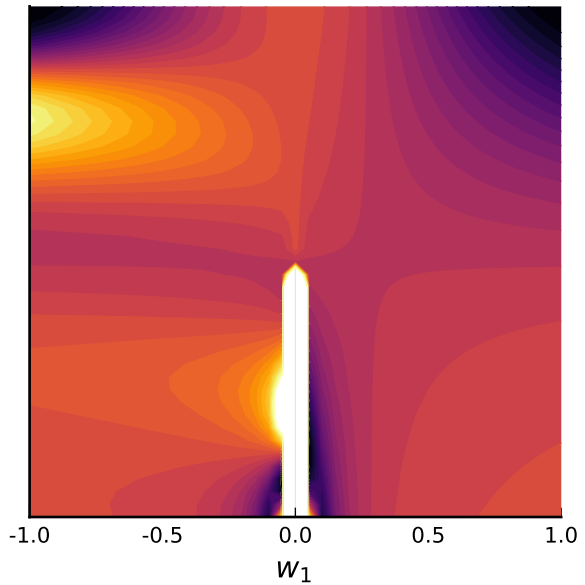


Gated NPU



NPU



NMU

