

ANIMESH JHA

Bachelor of Technology, Computer Science

animesjha590@gmail.com | (+91) 6205282733 | KP 5, KIIT University, Bhubaneswar

EDUCATION

Kalinga Institute of Industrial Technology

Bachelor's of Technology Computer Science

CGPA: 6.76

Bhubaneswar, Odisha

August 2021 - August 2025 (expected)

SKILLS

Programming Languages: C++, C, Low Latency Development
Libraries/Frameworks: WinSock, JUCE, SFML, GTK
Tools / Platforms: Vim, Geany, Visual Studio Code
Databases: GraphQL, Infinite Graph, Cassandra

PROJECTS / OPEN-SOURCE

Low Latency Packet Simulation Tool

C, Unix, WinSock, GNU socket library

- Developed a Low Latency simulation tool, in c used to demonstrate the latency, speed, message transfer rates among two different computers.
- Using GUI in c demonstrated the message transmission
- reduced the traditional latency by using low-compiler dependent coding method
- Aims to demonstrate how transfer of information happens between two exchanges over a factor of milliseconds

Assembly on Embedded System

C, Unix, Arduino UNO R3

- Demonstrated the implementation and working of assembly x86 on embedded system specifically on Arduino R3.
- Shown the working of Arithmetic and Boolean calculations.
- Using certain motion sensors deployed the assembly language instructions and performed various actions.
- Reduced performance latency by a factor of 500ms response time to 160-200ms.