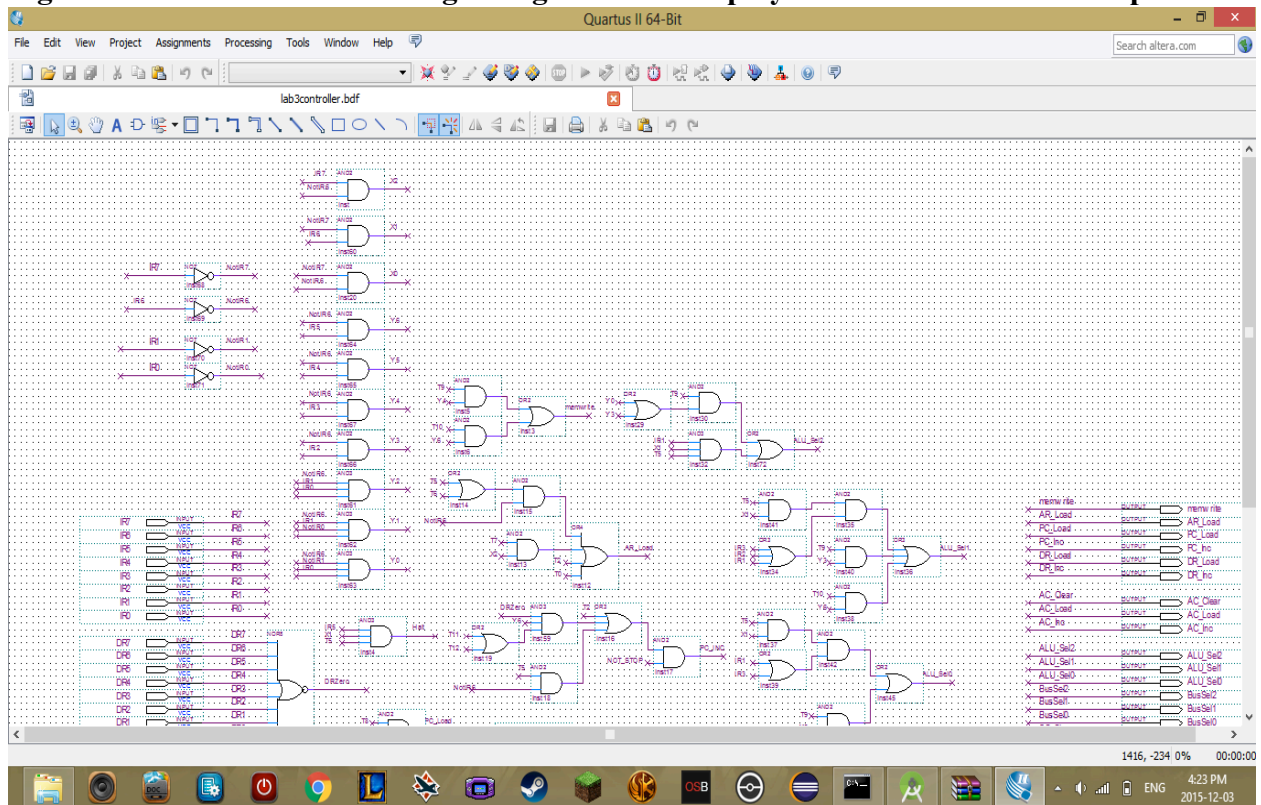
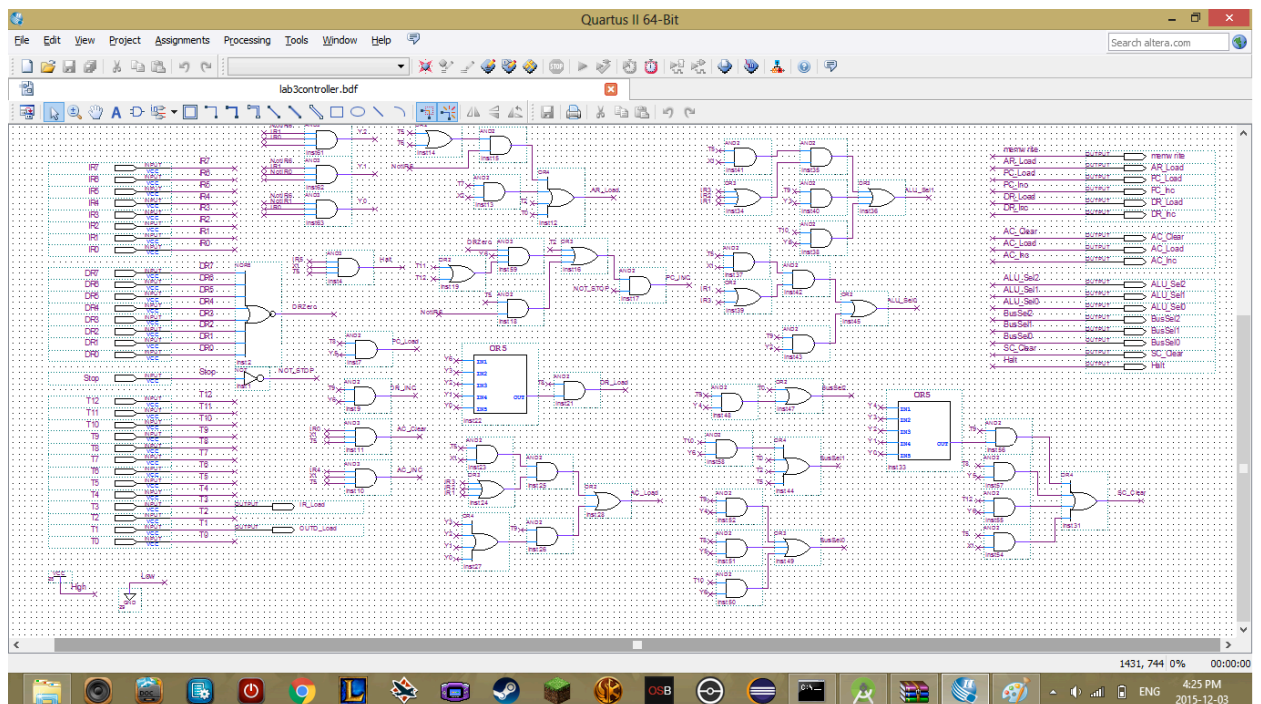


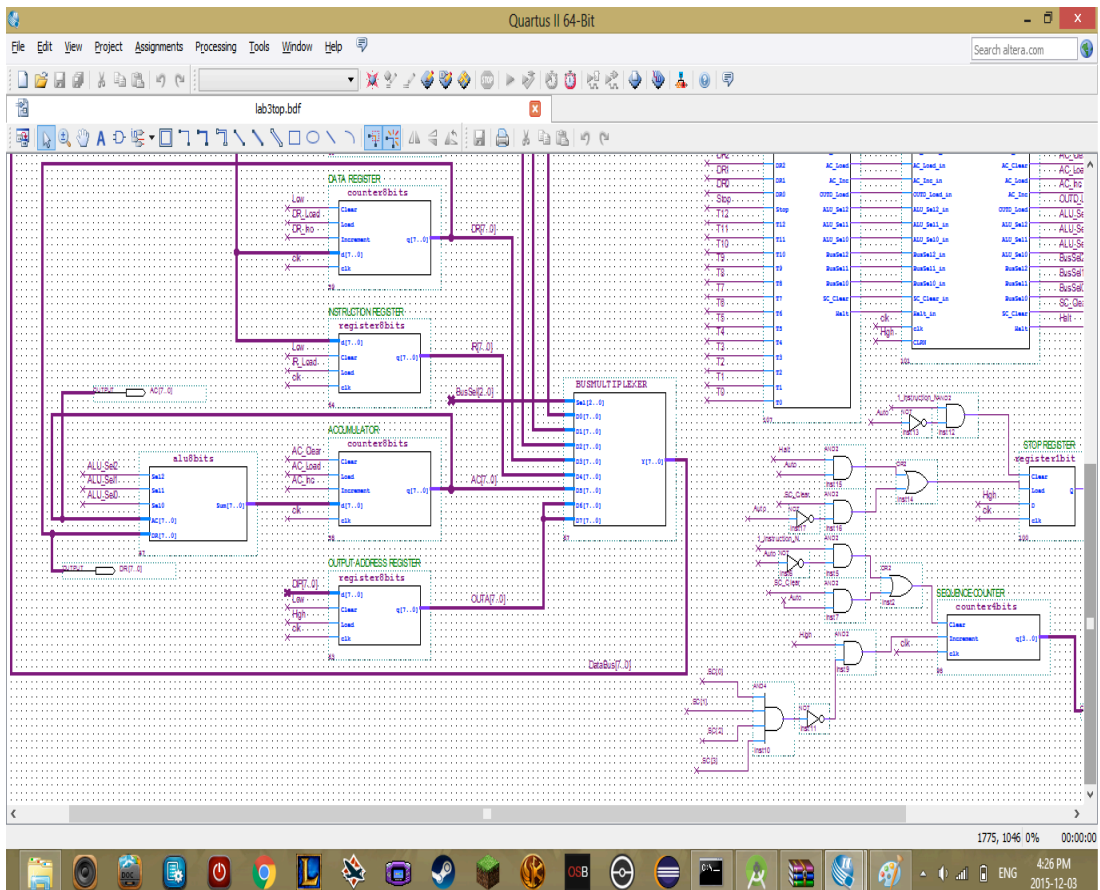
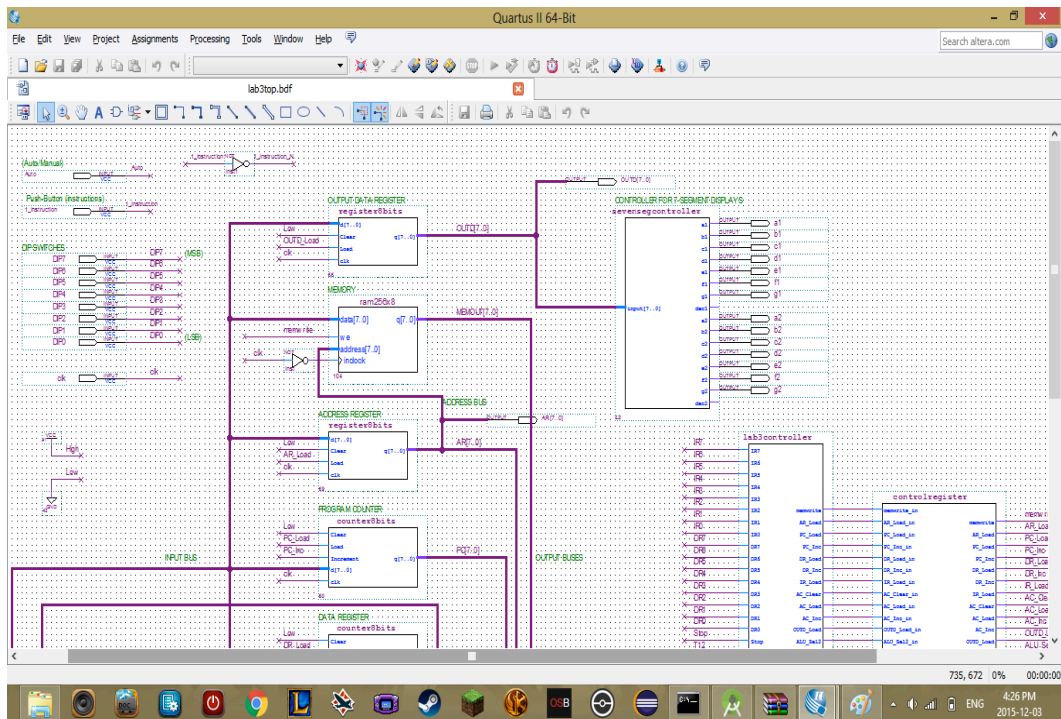
**Figure 1 – the first half of the logic diagrams that display the controller of the computer.**



**Figure 2- the second half of the logic diagrams that display the controller of the computer.**



**Figure 3- Screenshots of the logical components in our Top diagram.**



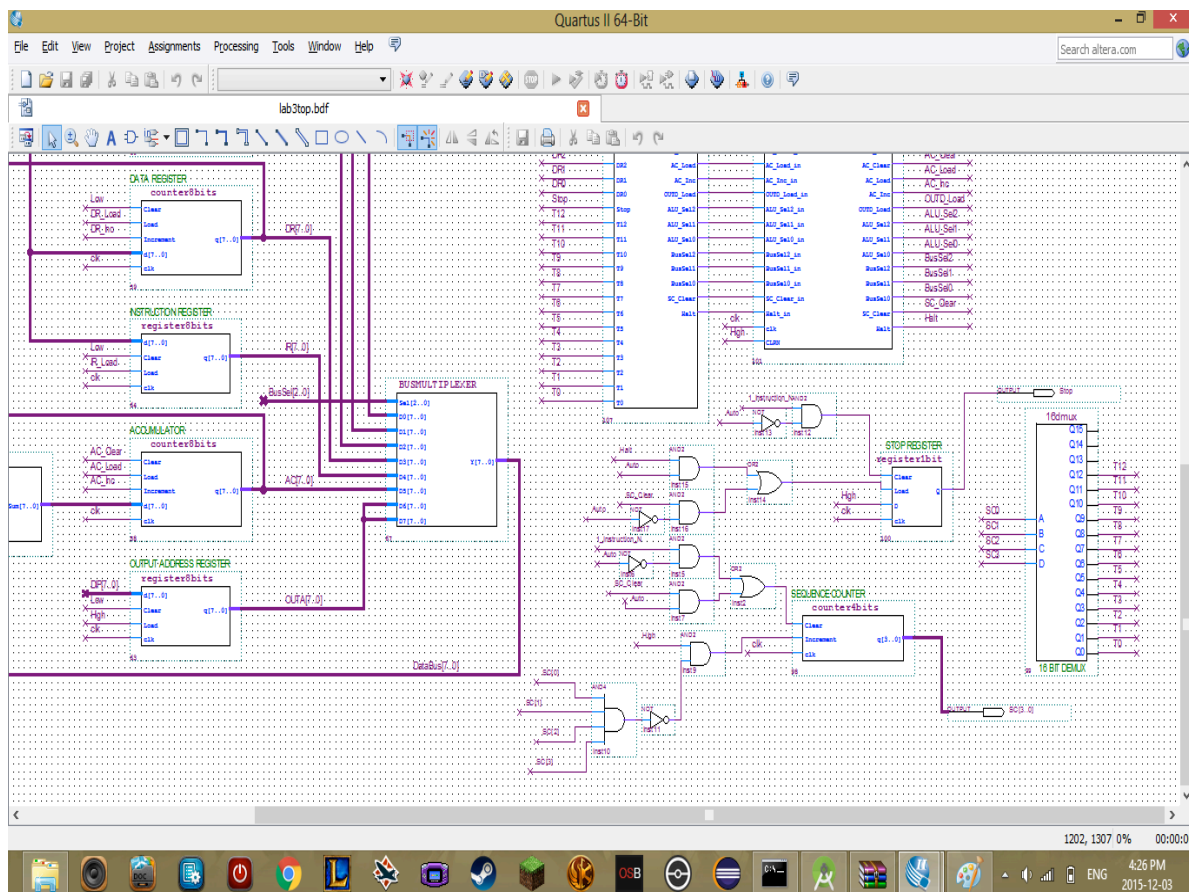


Figure 4- The MIF Files for both hardware and software (respectively)

Addr	+0	+1	+2	+3	+4	+5	+6	+7	ASCII
000	04	A0	10	05	60	04	A3	42	.....B
008	08	A3	20	A3	10	20	84	A4	.....
010	08	A5	60	00	00	00	00	00	.....
018	00	00	00	00	00	00	00	00	.....
020	84	A1	82	A2	88	A3	08	A1	.....
028	04	A2	08	A4	04	A2	50	08	.....P.
030	A2	10	00	00	00	00	00	00	.....
038	00	00	00	00	00	00	00	00	.....
040	00	00	00	00	00	00	00	00	.....
048	00	00	00	00	00	00	00	00	.....
050	00	00	00	00	00	00	00	00	.....
058	00	00	00	00	00	00	00	00	.....
060	00	00	00	00	00	00	00	00	.....
068	00	00	00	00	00	00	00	00	.....
070	00	00	00	00	00	00	00	00	.....
078	00	00	00	00	00	00	00	00	.....
080	21	B5	37	08	5C	84	A1	1D	1.7.A...
088	72	FF	F6	43	03	A9	D4	19	r..C....
090	31	D9	47	82	14	52	07	CA	1.G.R..



