



K932L219AD9F9C0C7X5AS4K
SF49G89GSKD439PDFOC8FD
9X C0B9E8 FDI349SD0230S9
P 9FD58CI49D9F87J4D0S0
9FGS9F8G7H5J4S6D70S8D5E
8G6H4J43D8G6J38D9S7F5G4
SD1A D0293U 0B8S 7F5H5A
3I49DSI233DS94J5LV23XV12
13 DK3215KFO GVEWG39YC
HI6D8SD9237F G9D 3MVC3
9E8CJ329657FKM493KDU238
SD72 7SA89D08G8923ID908
234NF8385YU ID93023 DI2
I JF749FJGU 49DJOS O230

Cryptography: DES Implementation

EEGR – 582 – ADVANCED CRYPTOGRAPHY (PROJECT 2 – NOTES)
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PROJECT CODE – IN GIT

<https://github.com/nnamdi2020/Cryptography/blob/main/DESCryptography.py>

Project Output

```
PS C:\Users\nosua\OneDrive - Strategic Generation\Certs-School\Morgan State PHD\Spring 2021 Semester\Cryptography
/Cryptography/Project 2/.venv/Scripts/python.exe" "c:/Users/nosua/OneDrive - Strategic Generation/Certs-School/Mo
This program encrypts your plain text using Data Encryption Standard (DES)

Please enter a key (ex: secret_k): (only 8 characters at this time) 12345678

Please enter your plain text (only 8 characters at this time) (example: iLLmatic): iLLmatic

plain text: iLLmatic
Ciphered: '\x04\x879Pq!è'
Deciphered: iLLmatic
```

What is DES?

The **DES** (Data **Encryption** Standard) algorithm is a symmetric-key block **cipher** created in the early 1970s by an IBM team and adopted by the National Institute of Standards and Technology (NIST). The algorithm takes the plain text in 64-bit blocks and converts them into ciphertext using 48-bit keys.

TOPIC: Data Encryption Standard (DES)

Definition: DES is a symmetric key block cipher that ~~is it~~ operates on a plaintext block of 64 bits and returns ciphertext of same size.

STEPS:

① Subkey Generation

↳ Given: M, K

↳ M → L, R

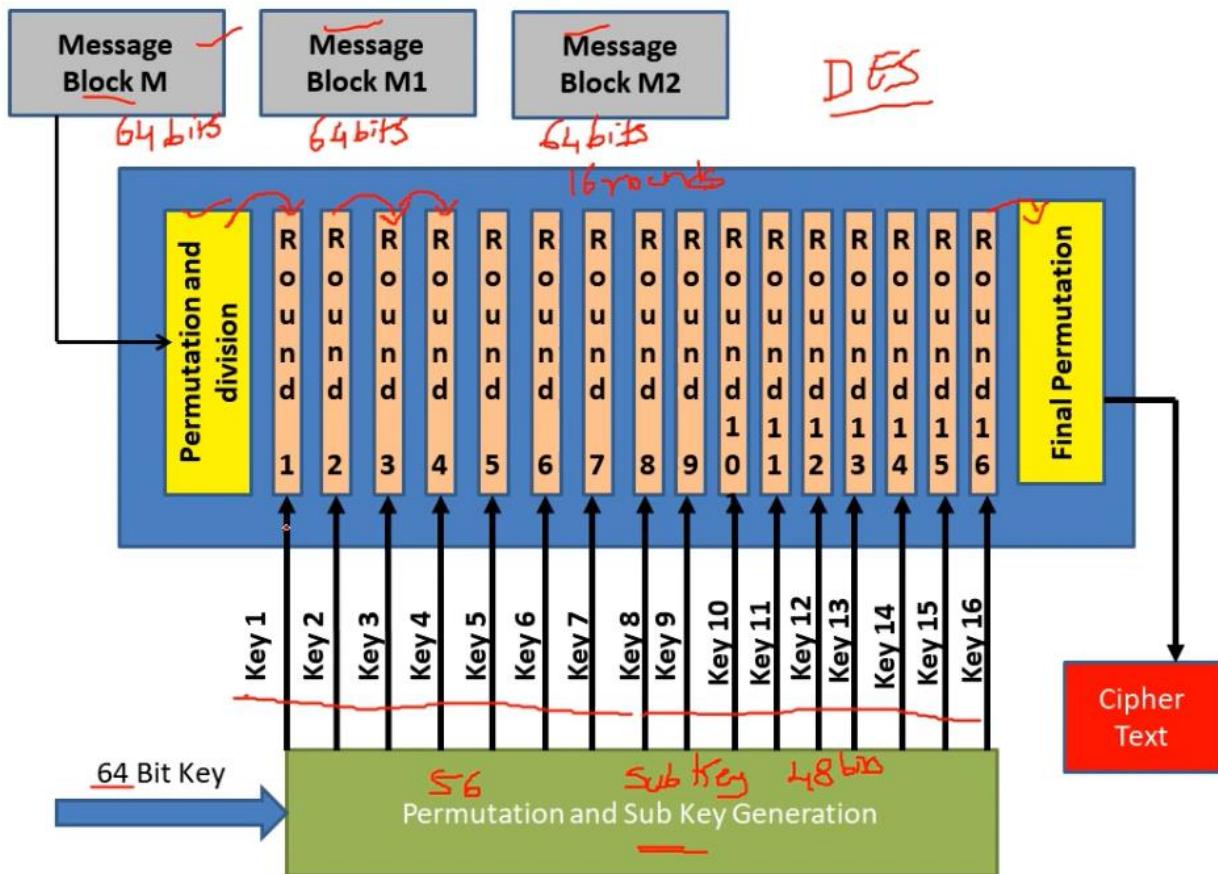
↳ K $\xrightarrow{PC-1}$ K' \longrightarrow C₀ D₀ $\xrightarrow[\text{(8x8 bits)}]{\substack{\text{left shift} \\ \text{table}}}$ upto C₁₆ D₁₆ (28 bits each)
 $(1 \leq n \leq 16)$

↳ Respective C_n D_n pairs $\xrightarrow{PC-2}$ K_n ($1 \leq n \leq 16$) (6x8 bits).

Grouping K into 8 8-bit groups, the last bit of each group will remain unused.

I found the following video on DES extremely informative and beneficial to the understanding the project. https://www.youtube.com/watch?v=-j80aA8q_IQ

DES Breakdown



- The message blocks are divided into 64-Bit blocks
- The key is divided into a 56-bit permutation and 16 sub keys (48-bit each).
- 16 rounds perform the same actions. The output of each round is given as input to the next round.

Phase-1 Generating 16 Sub Keys

Key in Hexadecimal = 133457799BBCDFF1 16

K = 00010011 00110100 01010111 01111001 10011011 10111100 11011111 11110001

K is 64 bits ($8 \times 8 = 64$)

Example of conversion: 13: 1 = 0001 , 3 = 0011 , 13 = 00010011

How to convert HEX to Binary

Convert each hex digit to 4 binary digits according to this table:

Hex	Binary
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001
A	1010
B	1011
C	1100
D	1101
E	1110
F	1111

HEX goes up to 16. A – F (10 – 15)

- $2^0 = 1$
- $2^1 = 2$
- $2^2 = 4$
- $2^3 = 8$

Converting Hexadecimal to signed 16-bit binary

Converter - <https://www.mathsisfun.com/binary-decimal-hexadecimal-converter.html>

<https://www.rapidtables.com/convert/number/how-hex-to-binary.html>

Key in Hexadecimal: 133457799BBCDFF1

Generating 16 Sub Keys

Generating 16 Sub Keys

- The 64-bit key is permuted according to the following table, PC-1.
- Note only 56 bits of the original key appear in the permuted key.

$K = 00010011\ 00110100\ 01010111\ 01111001\ 10011011\ 10111100\ \underline{11011111}\ \overset{64}{\text{11110001}}$

we get the 56-bit permutation
 $K+ = 1111000\ 0110011\ 0010101\ 0101111\ 0101010\ 1011001\ 1001111\ 0001111\ \overset{56 \text{ Bits}}{\text{---}}\ 12$

$\cancel{7x^3}$
 $\cancel{= 56}$

<u>57</u>	<u>49</u>	41	33	25	17	9
1	58	50	42	34	26	18
10	2	59	51	43	35	27
19	11	3	60	52	44	36
63	55	47	39	31	23	15
7	62	54	46	38	30	22
14	6	61	53	45	37	29
21	13	5	28	20	21	4

PC-1

Use all tables (PC-1, PC-2, IP) by going row by row (left to right). Go to the position of the number (e.g., 57 bit = 1 in K) and place the value in the 1st position of K+.

Split the K+ Key

Generating 16 Sub Keys

we get the 56-bit permutation

K+ = 1111000 0110011 0010101 0101111 0101010 1011001 1001111 0001111

P61

Next, split this key into left and right halves, C0 and D0, where each half has 28 bits.

From the permuted key K+, we get

C0 = 1111000 0110011 0010101 0101111

D0 = 0101010 1011001 1001111 0001111

Left Shifts

Question: How is the Number of Left Shifts generated – is it fixed?

Generating 16 Sub Keys

Iteration Number	Number of Left Shifts
1	1
2	1
3	2
4	2
5	2
6	2
7	2
8	2
9	1
10	2
11	2
12	2
13	2
14	2
15	2
16	1

From the previous slide
 $C_0 = 1111000011001100101010101111$
 $D_0 = 0101010101100110011110001111$

$C_1 = 11100001100110010101010101111$
 $D_1 = 0101010101100110011110001111$

$C_2 = 11000011001100101010101011111$
 $D_2 = 0101010110011001111000111101$

$C_3 = 0000110011001010101011111111$
 $D_3 = 0101011001100111100011110101$

$C_4 = 0011001100101010101111111100$
 $D_4 = 0101100110011110001111010101$

$C_5 = 1100110010101010111111110000$
 $D_5 = 0110011001111000111101010101$

schedule of "left shifts"

Generating 16 Sub Keys

C₆ = 001100101010111111000011

D₆ = 10011001111000111101010101

C₇ = 110010101011111100001100

D₇ = 01100111100011110101010110

C₈ = 0010101011111110000110011

D₈ = 100111100011110101010111001

C₉ = 0101010111111100001100110

D₉ = 0011110001111010101010110011

C₁₀ = 010101011111110000110011001

D₁₀ = 1111000111101010101011001100

C₁₁ = 010101111111000011001100101

D₁₁ = 1100011110101010101100110011

C₁₂ = 010111111100001100110010101

D₁₂ = 0001111010101010110011001111

C₁₃ = 011111110000110011001010101

D₁₃ = 0111101010101011001100111100

C₁₄ = 1111111000011001100101010101

D₁₄ = 1110101010101100110011110001

C₁₅ = 1111100001100110010101010111

D₁₅ = 1010101010110011001111000111

C₁₆ = 1111000011001100101010101111

D₁₆ = 0101010101100110011110001111

Generating the Sub Keys using the PC-2 Table

All of the sub keys are 48 bits

Generating 16 Sub Keys

We now form the keys K_n , for $1 \leq n \leq 16$, by applying the following permutation table to each of the concatenated pairs $C_n D_n$.

Each pair has 56 bits, but PC-2 only uses 48 of these.

$$\rightarrow C_1 = 1110000110011001010101011111$$

$$\rightarrow D_1 = 1010101011001100111100011110$$

$$C_1 D_1 = 1110000 \ 1100 \underline{110} \ 010 \underline{1010} \ 1011111 \ 1010101 \ 0110011 \ 0011110 \ 0011110$$

$K_1 = 000110 \ 110000 \ 001011 \ 101111 \ 111111 \ 000111 \ 000001 \ 110010$

PC-2

1st subkey *48 bits* *48 bits*

8x6	-14	17	11	24	1	5
-3	28	15	6	21	10	
-23	19	12	4	26	8	
-16	7	27	20	13	2	
-41	52	31	37	47	55	
-30	40	51	45	33	48	
-44	49	39	56	34	53	
-46	42	50	36	29	32	

C_n & D_n are concatenated (56-bits) then the PC-2 table is used to generate a 48-bit K_n

Generating 16 Sub Keys

$C_2 = 1100001100110010101010111111$

$D_2 = 0101010110011001111000111101$

$C_2 D_2 = 1100001 \underline{1001100} \underline{1010101} 0111111 0101010 1100110 0111100 0111101 \text{ 56}$

$K_2 = 0 \underline{110} 011010111011011001110110111100100111100101 \text{ 48 bits}$

skipped²

C_3

D_3

*

K_3

PC-2

14	17	11	24	1	5
3	28	15	6	21	10
23	19	12	4	26	8
16	7	27	20	13	2
41	52	31	37	47	55
30	40	51	45	33	48
44	49	39	56	34	53
46	42	50	36	29	32

Generating 16 Sub Keys

$K_1 = 000110\ 110000\ 001011\ 101111\ 111111\ 000111\ 000001\ 110010$

$K_2 = 011110\ 011010\ 111011\ 011001\ 110110\ 111100\ 100111\ 100101$

$K_3 = 010101\ 011111\ 110010\ 001010\ 010000\ 101100\ 111110\ 011001$

$K_4 = 011100\ 101010\ 110111\ 010110\ 110110\ 110011\ 010100\ 011101$

$K_5 = 011111\ 001110\ 110000\ 000111\ 111010\ 110101\ 001110\ 101000$

$K_6 = 011000\ 111010\ 010100\ 111110\ 010100\ 000111\ 101100\ 101111$

$K_7 = 111011\ 001000\ 010010\ 110111\ 111101\ 100001\ 100010\ 111100$

$K_8 = 111101\ 111000\ 101000\ 111010\ 110000\ 010011\ 101111\ 111011$

$K_9 = 111000\ 001101\ 101111\ 101011\ 111011\ 011110\ 011110\ 000001$

$K_{10} = 101100\ 011111\ 001101\ 000111\ 101110\ 100100\ 011001\ 001111$

$K_{11} = 001000\ 010101\ 111111\ 010011\ 110111\ 101101\ 001110\ 000110$

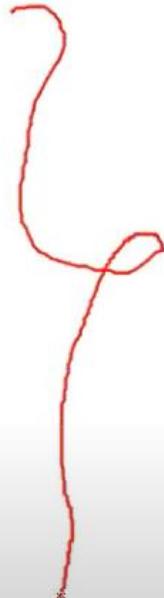
$K_{12} = 011101\ 010111\ 000111\ 110101\ 100101\ 000110\ 011111\ 101001$

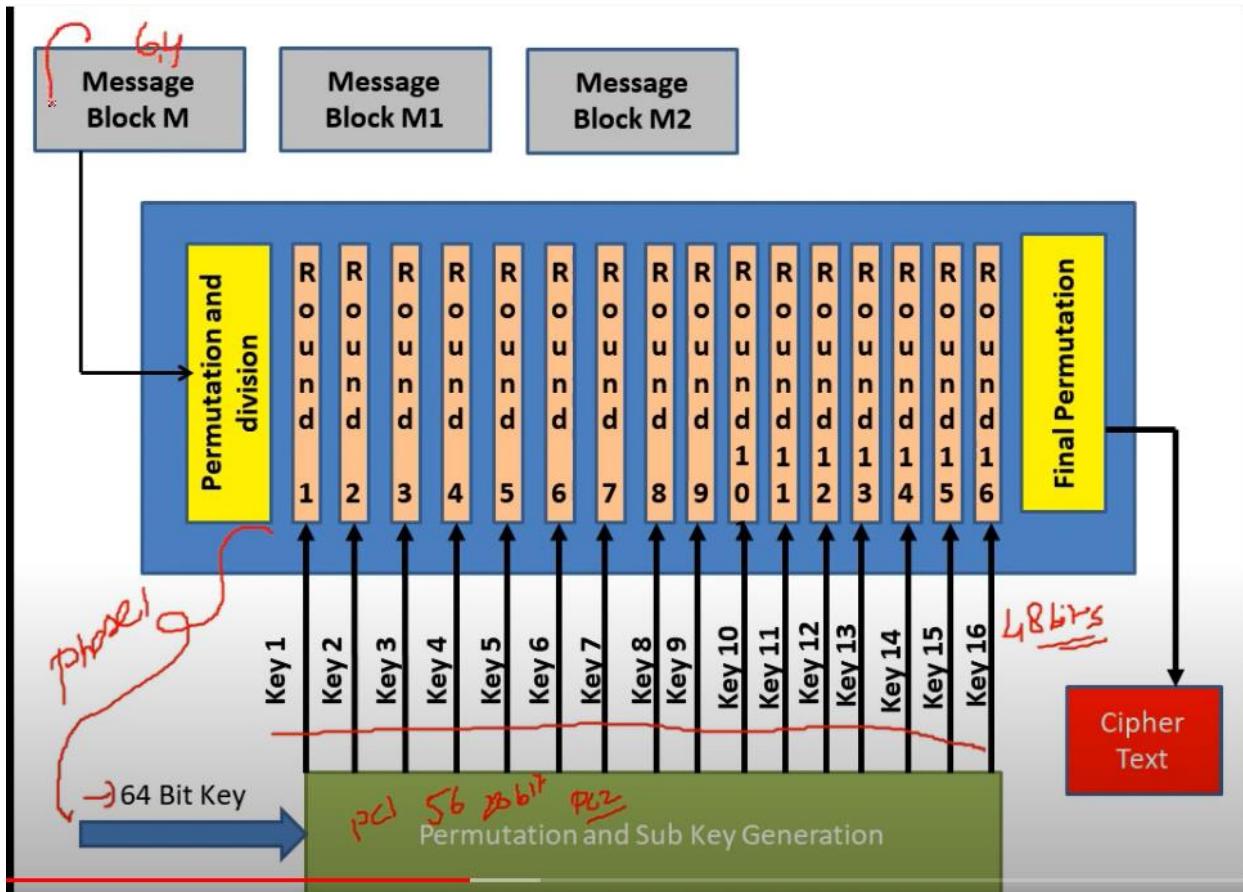
$K_{13} = 100101\ 111100\ 010111\ 010001\ 111110\ 101011\ 101001\ 000001$

$K_{14} = 010111\ 110100\ 001110\ 110111\ 111100\ 101110\ 011100\ 111010$

$K_{15} = 101111\ 111001\ 000110\ 001101\ 001111\ 010011\ 111100\ 001010$

$K_{16} = 110010\ 110011\ 110110\ 001011\ 000011\ 100001\ 011111\ 110101$





- We took a 64 bit Key
- applied the PC-1 table to get 56-bit keys ($K+$)
- Divided $K+$ it to 28 bits keys C0 D0 keys
- Used left shifts to get C0..16 , D0..16
- Applied the PC-2 table to each C0..16, D0..16 to get K1..16 subkeys . Note $K = C \& D$ concatenated with the PC-2 applied to generate a 48-bit key

Phase-2 Permutation and division

Permutation is just rearranging the bits in the message. We will use the IP table

Step 2: Encode each 64-bit block of data.

$M = 0000\ 0001\ 0010\ 0011\ 0100\ 0101\ 0110\ 0111\ 1000\ 1001\ 1010\ 1011\ 1100\ 1101\ 1110\ 1111$
64 bits

There is an initial permutation IP of the 64 bits of the message data M

$IP = 1100\ 1100\ 0000\ 0000\ 1100\ 1100\ 1111\ 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$

IP							
58	50	42	34	26	18	10	2
60	52	44	36	28	20	12	4
62	54	46	38	30	22	14	6
64	56	48	40	32	24	16	8
57	49	41	33	25	17	9	1
59	51	43	35	27	19	11	3
61	53	45	37	29	21	13	5
63	55	47	39	31	23	15	7

Divide the IP value (64 Bits) to L0(32 bits) and R0(32 bits)

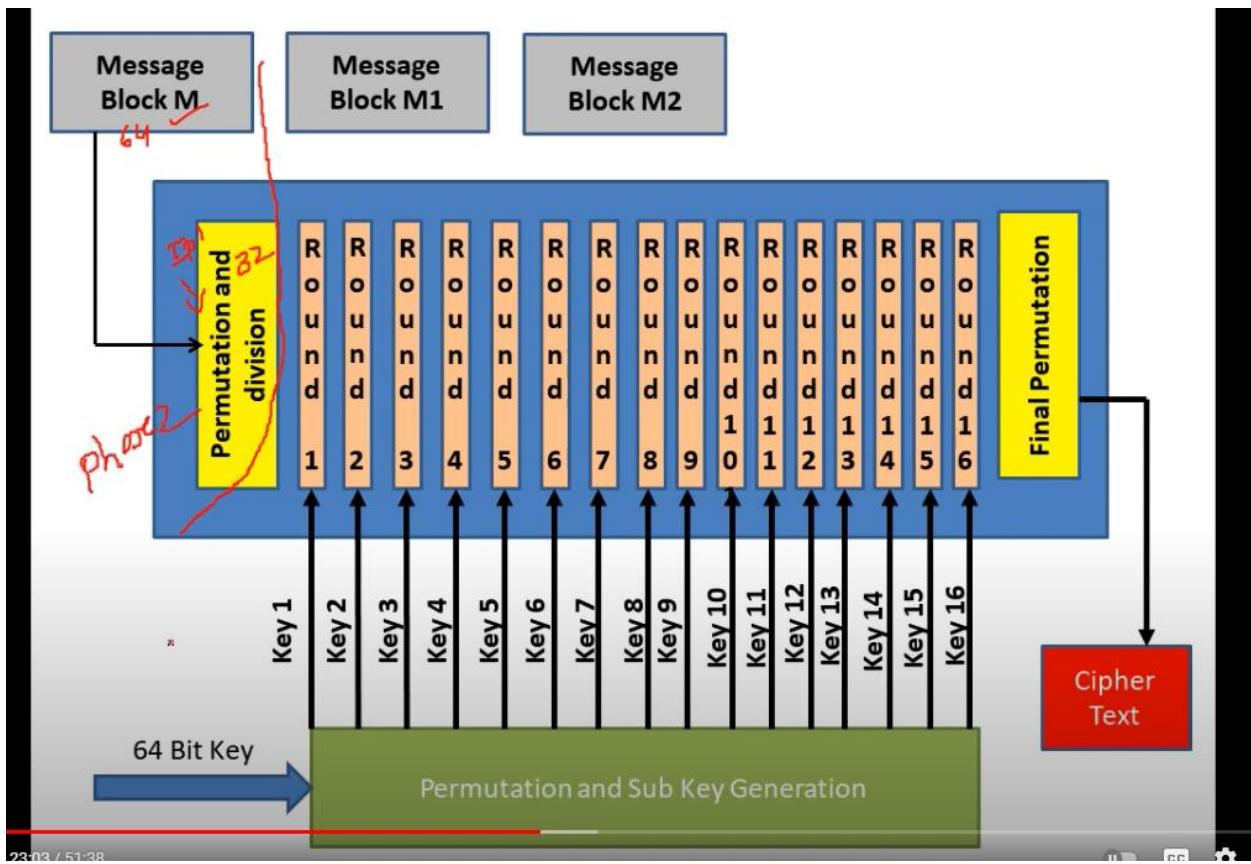
Step 2: Encode each 64-bit block of data

$IP = 1100\ 1100\ 0000\ 0000\ 1100\ 1100\ 1111\ 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$

Next divide the permuted block IP into a
left half L_0 of 32 bits,
and a right half R_0 of 32 bits.

$L_0 = 1100\ 1100\ 0000\ 0000\ 1100\ 1100\ 1111\ 1111$ *32 bits*
 $R_0 = 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$ *32 bits*

Phase 2 completed = applying the IP table to the Message Block and dividing it into 2-32bit values



Phase 3 – Rounds

Same formula for all 16 rounds. Output of Round 1 will be Input of Round 2 and so forth until Round 16

Formula

16 Rounds - Take a look at first round

$$\begin{aligned} L_0 &= 1100 \ 1100 \ 0000 \ 0000 \ 1100 \ 1100 \ 1111 \ 1111 \ \swarrow \text{32 bits} \\ R_0 &= 1111 \ 0000 \ 1010 \ 1010 \ 1111 \ 0000 \ 1010 \ 1010 \ \swarrow \text{32 bits} \end{aligned}$$

$$\left\{ \begin{array}{l} L_n = R_{n-1} \quad \text{for } n=1 \text{ for round 1} \\ R_n = L_{n-1} + f(R_{n-1}, K_n) \quad L_1 = R_{1-1} \\ \text{Let + denote XOR addition} \quad R_1 = L_{1-1} + f(R_{1-1}, K_1) \end{array} \right.$$

Output Round
Input Round

$$\left\{ \begin{array}{l} L_1 = R_0 \\ R_1 = L_0 + f(R_0, K_1) \end{array} \right. \text{Subkey!}$$

For $n = 1$, we have

$$\begin{aligned} K_1 &= 000110 \ 110000 \ 001011 \ 101111 \ 111111 \ 000111 \ 000001 \ 110010 \\ L_1 &= R_0 = 1111 \ 0000 \ 1010 \ 1010 \ 1111 \ 0000 \ 1010 \ 1010 \\ R_1 &= L_0 + f(R_0, K_1) \end{aligned}$$

Need to compute R_1 (understand $f(R_0, K_1)$). The following have been previously computed: K_1, L_1, R_0, L_0

Step 1 – expand R_0 from 32 Bits to 48 bits

What is $f(R_0, K_1)$?

$$\underline{R_0} = \underline{1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010} \quad \text{32 bits} \rightarrow \text{Expand} \rightarrow \text{48 bits}$$

- To calculate f , we first expand each block R_0 from 32 bits to 48 bits.
- This is done by using a selection table that repeats some of the bits in R_0 .
- We'll call the use of this selection table the function E .
- Thus $E(R_0)$ has a 32 bit input block, and a 48 bit output block.

$$\underline{E(R_0)} = \underline{011110\ 100001\ 010101\ 010101\ 011110\ 100001\ 010101\ 010101}$$

48 bits

E BIT-SELECTION TABLE

	32	1	2	3	4	5
6x8 = 48	.4	5	6	7	8	9
	.8	9	10	11	12	13
	12	13	14	15	16	17
	16	17	18	19	20	21
	20	21	22	23	24	25
	24	25	26	27	28	29
	28	29	30	31	32	1

Perform XOR (+ = XOR)

With XOR two of anything = 0. Other than that the answer = 1

Example

$$1 \text{ XOR } 1 = 0$$

$$0 \text{ XOR } 0 = 0$$

$$1 \text{ XOR } 0 = 1$$

$$0 \text{ XOR } 1 = 1$$

$$f(R_0, K_1)$$

$$\underline{E(R_0)} = 011110 \ 100001 \ 010101 \ 010101 \ 011110 \ 100001 \ 010101 \ 010101$$

Next in the f calculation, we XOR the output $E(R_0)$ with the key K_1 :

$$\underline{K_1} + \underline{E(R_0)}$$

$$\underline{K_1} = 000110 \ 110000 \ 001011 \ 101111 \ 111111 \ 000111 \ 000001 \ 110010$$

$$\underline{E(R_0)} = 011110 \ 100001 \ 010101 \ 010101 \ 011110 \ 100001 \ 010101 \ 010101$$

$$\underline{K_1+E(R_0)} = 011000 \ 010001 \ 011110 \ 111010 \ 100001 \ 100110 \ 010100 \ 100111.$$

The result is 48 bits or we can say eight group containing six bits

S Boxes Computation

$$\rightarrow f(R_0, K_1)$$

We now do something strange with each group of six bits: we use them as addresses in tables called "S boxes".

$$\underline{K_1+E(R_0)} = \underline{011000} \ \underline{010001} \ \underline{011110} \ \underline{111010} \ \underline{100001} \ \underline{100110} \ \underline{010100} \ \underline{100111}.$$

$$K1+E(R0) = \underline{\underline{B1}} \quad \underline{\underline{B2}} \quad \underline{\underline{B3}} \quad \underline{\underline{B4}} \quad \underline{\underline{B5}} \quad \underline{\underline{B6}} \quad \underline{\underline{B7}} \quad \underline{\underline{B8}}$$

We now calculate

$$\underline{S_1(B_1)} \ \underline{S_2(B_2)} \ \underline{S_3(B_3)} \ S_4(B_4) \ S_5(B_5) \ S_6(B_6) \ S_7(B_7) \ S_8(B_8)$$

$$f(R_0, K_1) - S_1(B_1)$$

$$K_1 + E(R_0) = \underline{011000} \quad 010001 \quad 011110 \quad 111010 \quad 100001 \quad 100110 \quad 010100 \quad 100111.$$

$$K_1 + E(R_0) = \underline{B1} \quad B2 \quad B3 \quad B4 \quad B5 \quad B6 \quad B7 \quad B8$$

We now calculate

$$\underline{S_1(B_1)} \quad S_2(B_2) \quad S_3(B_3) \quad S_4(B_4) \quad S_5(B_5) \quad S_6(B_6) \quad S_7(B_7) \quad S_8(B_8)$$

$$B_1 \quad \begin{matrix} 011000 \\ \cancel{1100} \\ \downarrow \end{matrix} \rightarrow \underline{12} \quad \begin{matrix} 00 \\ \rightarrow 0 \end{matrix} \quad 0101 \rightarrow S_1(B_1)$$

Row No.	Column Number															
	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	14	4	13	1	2	15	11	8	3	10	6	12	5	9	0	7
1	0	15	7	4	14	2	13	1	10	6	12	11	9	5	3	8
2	4	1	14	8	13	6	2	11	15	12	9	7	3	10	5	0
3	15	12	8	2	4	9	1	7	5	11	3	14	10	0	6	13

Where are the S box tables generated? Is it Fixed?

Steps to computing S Boxes

Using $B_1 = 011000$

1. Take the 1st and last bit and XOR [0 0 -> 0] this produces the row 0
2. Take the 4 middle bits [1100 and calculate using base 2]
 - a. $2^2 + 2^3 = 4 + 8 = 12$ – this produces the column
3. Find the value of the row, column [0,12] in the S1 table = 5
4. Convert 5 to binary (use base 2 , $2^3, 2^2, 2^1, 2^0$) = 0101
5. $S_1(B_1) = 0101$
6. Note there are different S box tables for S1,S2,S3 .. S8

$$S_1(B_1) \quad S_2(B_2) \quad \underline{S_3(B_3)} \quad S_4(B_4) \quad S_5(B_5) \quad S_6(B_6) \quad S_7(B_7) \quad S_8(B_8)$$

S2

$$f(R_0, K_1) - S_2(B_2)$$

$$K_1 + E(R_0) = 011000 \underline{010001} 011110 111010 100001 100110 010100 100111.$$

$$K1 + E(R0) = \quad B1 \quad \underline{B2} \quad B3 \quad B4 \quad B5 \quad B6 \quad B7 \quad B8$$

We now calculate

$$S_1(B_1) \underline{S_2(B_2)} S_3(B_3) S_4(B_4) S_5(B_5) S_6(B_6) S_7(B_7) S_8(B_8)$$

$$B_2 \quad \begin{array}{c} 010001 \\ | \quad | \end{array} \quad 01 - 1 \quad \underline{1000} \rightarrow 8$$

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	15	1	8	14	6	11	3	4	9	7	2	13	12	0	5	10
1	3	13	4	7	15	2	8	14	12	0	1	10	6	9	11	5
2	0	14	7	11	10	4	13	1	5	8	12	6	9	3	2	15
3	13	8	10	1	3	15	4	2	11	6	7	12	0	5	14	9

S3

$$f(R_0, K_1) - S_3(B_3)$$

$K_1 + E(R_0) = 011000 010001 \underline{011110} 111010 100001 100110 010100 100111.$

$K1 + E(R0) = B1 B2 \underline{B3} B4 B5 B6 B7 B8$

We now calculate

$S_1(B_1) S_2(B_2) \underline{S_3(B_3)} S_4(B_4) S_5(B_5) S_6(B_6) S_7(B_7) S_8(B_8)$

B_3 011110

$00 \rightarrow 0$

$S_3(B_3)$ 1000

$1111 \rightarrow 15$

$8 - \underline{1000}$

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
→ D	10	0	9	14	6	3	15	5	1	13	12	7	11	4	2	8
1	13	7	0	9	3	4	6	10	2	8	5	14	12	11	15	1
2	13	6	4	9	8	15	3	0	11	1	2	12	5	10	14	7
3	1	10	13	0	6	9	8	7	4	15	14	3	11	5	2	12

$$f(R_0, K_1) - S_4(B_4)$$

$K_1 + E(R_0) = 011000 010001 011110 111010 100001 100110 010100 100111.$

$K1 + E(R0) = \quad B1 \quad B2 \quad B3 \quad B4 \quad B5 \quad B6 \quad B7 \quad B8$

We now calculate

$S_1(B_1) \ S_2(B_2) \ S_3(B_3) \ S_4(B_4) \ S_5(B_5) \ S_6(B_6) \ S_7(B_7) \ S_8(B_8)$

$\underline{S_4(B_4)} \quad \underline{\underline{111010}}$

$10 - 2 \quad 2 - 0010$

$S_4(B_4) \quad \underline{\underline{0010}}$

$|10| \rightarrow 13$

$S4$

13

0	7	13	14	3	0	6	9	10	1	2	8	5	11	12	4	15
1	13	8	11	5	6	15	0	3	4	7	2	12	1	10	14	9
→2	10	6	9	0	12	11	7	13	15	1	3	14	5	2	8	4
	3	15	0	6	10	1	13	8	9	4	5	11	12	7	2	14

$f(R_0, K_1) - S_5(B_5)$

$K_1 + E(R_0) = 011000 010001 011110 111010 100001 100110 010100 100111.$

$K1 + E(R0) = \quad B1 \quad B2 \quad B3 \quad B4 \quad B5 \quad B6 \quad B7 \quad B8$

We now calculate

$S_1(B_1) \ S_2(B_2) \ S_3(B_3) \ S_4(B_4) \ S_5(B_5) \ S_6(B_6) \ S_7(B_7) \ S_8(B_8)$

$S_5(B_5) \quad 100001$

$S_5(B_5) \quad 1011$

s5

2	12	4	1	7	10	11	6	8	5	3	15	13	0	14	9
14	11	2	12	4	7	13	1	5	0	15	10	3	9	8	6
4	2	1	11	10	13	7	8	15	9	12	5	6	3	0	14
11	8	12	7	1	14	2	13	6	15	0	9	10	4	5	3

S6

$S_6(B_6) \quad 100110$

$\underline{S_6(B_6)} \quad 0101$

s6

12	1	10	15	9	2	6	8	0	13	3	4	14	7	5	11
10	15	4	2	7	12	9	5	6	1	13	14	0	11	3	8
9	14	15	5	2	8	12	3	7	0	4	10	1	13	11	6
4	3	2	12	9	5	15	10	11	14	1	7	6	0	8	13

S7

$S_7(B_7)$ 010100

$S_7(B_7)$ 1001

<u>S7</u>															
4	11	2	14	15	0	8	13	3	12	9	7	5	10	6	1
13	0	11	7	4	9	1	10	14	3	5	12	2	15	8	6
1	4	11	13	12	3	7	14	10	15	6	8	0	5	9	2
6	11	13	8	1	4	10	7	9	5	0	15	14	2	3	12

S8

$S_8(B_8)$ 100111

$S_8(B_8)$ 0111

<u>S8</u>															
13	2	8	4	6	15	11	1	10	9	3	14	5	0	12	7
1	15	13	8	10	3	7	4	12	5	6	11	0	14	9	2
7	11	4	1	9	12	14	2	0	6	10	13	15	3	5	8
2	1	14	7	4	10	8	13	15	12	9	0	3	5	6	11

$\rightarrow \underline{f(R_0, K_1)}$ - Final stage of f - Permutation

$S_1(B_1)S_2(B_2)S_3(B_3)S_4(B_4)S_5(B_5)S_6(B_6)S_7(B_7)S_8(B_8)$

$[= 0101 \underline{1100} 1000 \underline{0010} \underline{1011} 0101 1001 0111]$

We get

$\underline{f(R_0, K_1)} = \underline{0010 \underline{0011} \underline{0100} \underline{1010} \underline{1010} \underline{1001} \underline{1011} \underline{1011}}$ 32 P 4

16	7	20	21
29	12	28	17
1	15	23	26
5	18	31	10
2	8	24	14
32	27	3	9
19	13	30	6
22	11	4	25

Remember why we computed $f(R_0, K_1)$
 phase 1 \rightarrow Round 1

$$\underline{L_0} = 1100 \underline{1100} 0000 0000 1100 \underline{1100} 1111 1111 \swarrow$$

$$\underline{R_0} = 1111 0000 1010 1010 1111 0000 1010 1010 \swarrow$$

$$\left\{ \begin{array}{l} L_n = R_{n-1} \\ R_n = L_{n-1} + f(R_{n-1}, K_n) \\ \text{Let } + \text{ denote XOR addition} \end{array} \right. \quad \left. \begin{array}{l} n=1 \text{ for round 1} \\ L_1 = R_{1-1} \\ R_1 = L_{1-1} + f(R_{1-1}, K_1) \end{array} \right.$$

$$\underline{L_1} = \underline{R_0} \swarrow$$

$$\underline{R_1} = \underline{L_0} + \underline{f(R_0, K_1)}$$

For $n = 1$, we have

$$K_1 = 000110 110000 001011 101111 111111 000111 000001 110010$$

$$L_1 = R_0 = 1111 0000 1010 1010 1111 0000 1010 1010$$

$$R_1 = L_0 + f(R_0, K_1)$$

Finding output of Round 1

$$\underline{f(R_0, K_1)} = 0010\ 0011\ 0100\ 1010\ 1010\ 1001\ 1011\ 1011$$



$$\rightarrow \underline{L_0} = 1100\ 1100\ 0000\ 0000\ 1100\ 1100\ 1111\ 1111$$

$$\underline{R_0} = 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$$

For $n = 1$, we have

$$L_1 = R_0 = 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$$

$$R_1 = L_0 + \underline{f(R_0, K_1)}$$

$$= 1100\ 1100\ 0000\ 0000\ 1100\ 1100\ 1111\ 1111 \xrightarrow{L_0} f(R_0, K_1)$$

$$+ 0010\ 0011\ 0100\ 1010\ 1010\ 1001\ 1011\ 1011$$

$$R_1 = 1110\ 1111\ 0100\ 1010\ 0110\ 0101\ 0100\ 0100$$

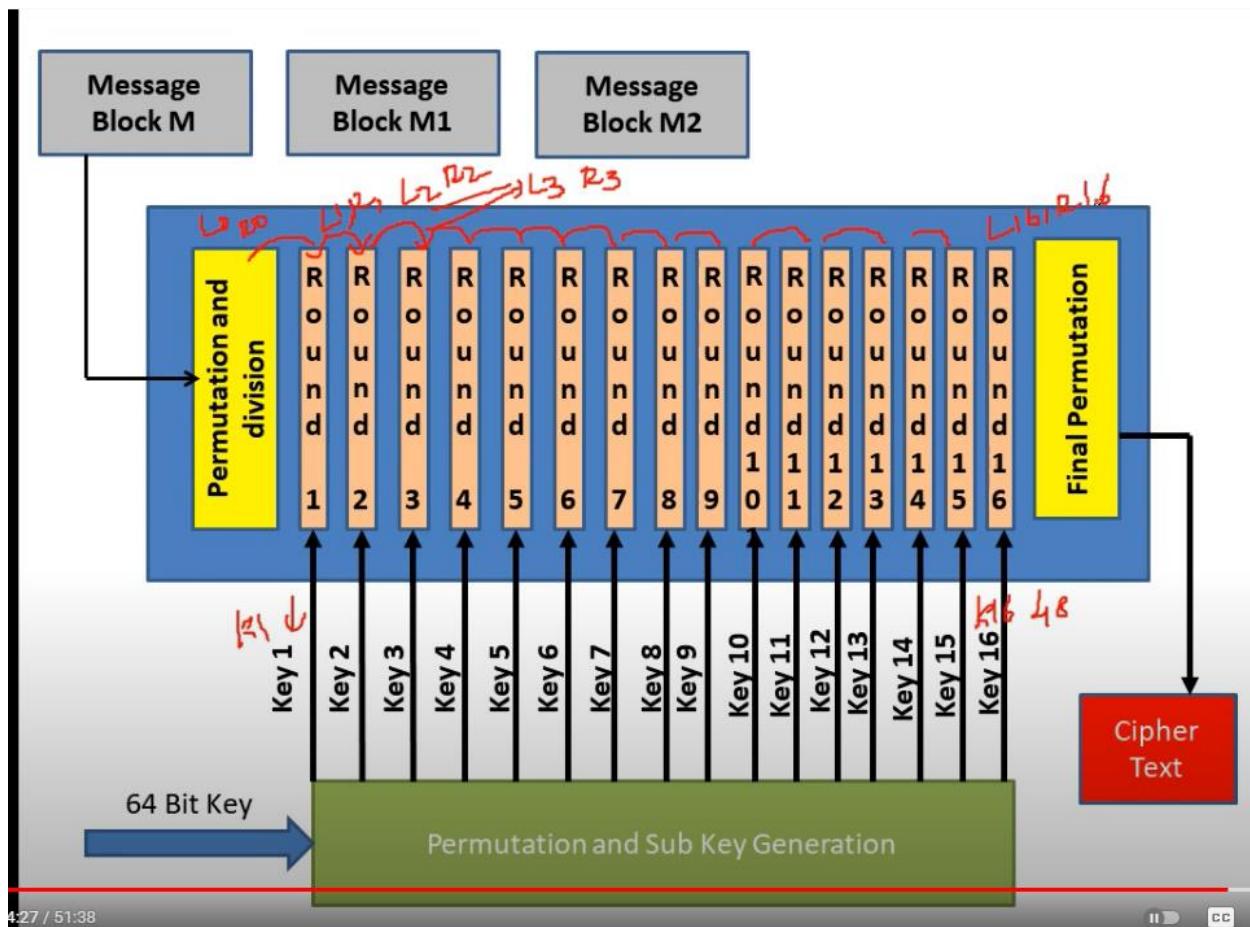
$$L_1 = 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$$

input for round 2



Output for Round 1

Revisit Phase 3



Let us start Round 2

$$\underline{L_1} = 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$$

$$\underline{R_1} = 1110\ 1111\ 0100\ 1010\ 0110\ 0101\ 0100\ 0100$$

$$\underline{L_n} = \underline{R_{n-1}}$$

$$\underline{R_n} = \underline{L_{n-1}} + f(\underline{R_{n-1}}, K_n)$$

Let + denote XOR addition

$n=2$ for round 2

$$\underline{L_2} = \underline{R_{2-1}}$$

$$\underline{R_2} = \underline{L_{2-1}} + f(\underline{R_{2-1}}, K_2)$$

$$\underline{L_2} = \underline{R_1}$$

$$\underline{R_2} = \underline{L_1} + f(\underline{R_1}, K_2)$$

For $n = 1$, we have

$$K_2 = 011110\ 011010\ 111011\ 011001\ 110110\ 111100\ 100111\ 100101$$

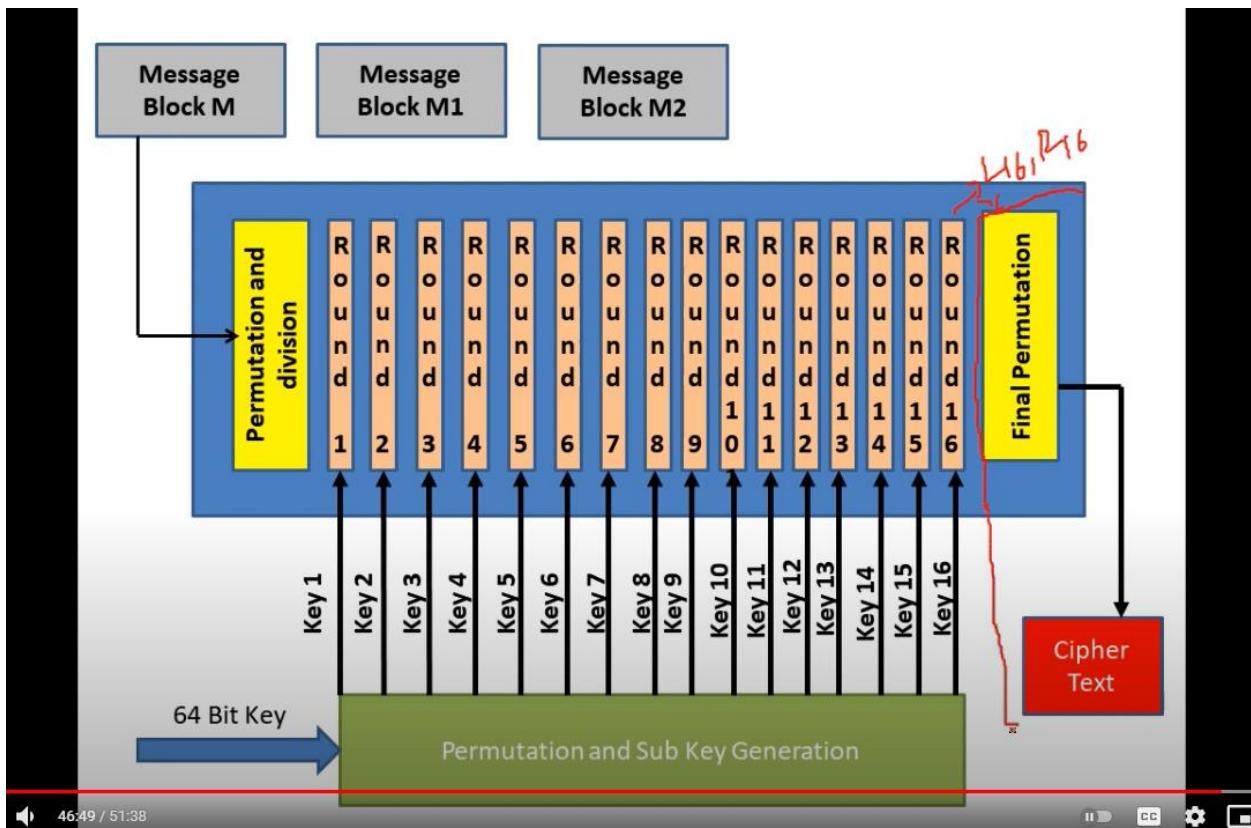
$$\underline{L_2} = \underline{R_1} = 1111\ 0000\ 1010\ 1010\ 1111\ 0000\ 1010\ 1010$$

$$\underline{R_2} = \underline{L_1} + f(\underline{R_1}, K_2)$$

$$\{\underline{L_2}, \underline{R_2}\}$$

Repeat the Above steps for 16 Rounds (Ln, Rn-1, S boxes, etc)

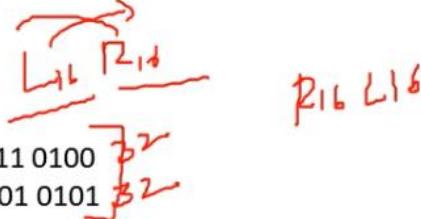
Phase 4 – Final Permutation into Cipher Text



Finally after 16 Rounds

Output of 16 Rounds

$$L_{16} = 0100\ 0011\ 0100\ 0010\ 0011\ 0010\ 0011\ 0100$$
$$R_{16} = 0000\ 1010\ 0100\ 1100\ 1101\ 1001\ 1001$$



$R_{16}\ L_{16}$

We reverse the order of these two blocks and apply the final permutation to

$$R_{16}L_{16} = 00001010\ 01001100\ 11011001\ 10010101\ 01000011\ 01000010\ 00110010\ 00110100$$

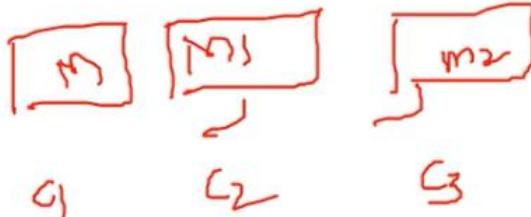
$$IP^{-1} = 10000101\ 11101000\ 00010011\ 01010100\ 00001111\ 00001010\ 10110100\ 00000101 \rightarrow 64\ bits$$

Ciphertext which in hexadecimal format is

85E813540F0AB405.

cipher text

40	8	48	16	56	24	64	32
39	7	47	15	55	23	63	31
38	6	46	14	54	22	62	30
37	5	45	13	53	21	61	29
36	4	44	12	52	20	60	28
35	3	43	11	51	19	59	27
34	2	42	10	50	18	58	26
33	1	41	9	49	17	57	25



Like that we encrypt every block of message

The Truth

64 → 56

- DES is insecure due to the relatively short 56-bit key size.
- In January 1999, distributed.net and the Electronic Frontier Foundation collaborated to publicly break a DES key in 22 hours and 15 minutes
- This cipher has been superseded by the Advanced Encryption Standard (AES).
- DES has been withdrawn as a standard by the National Institute of Standards and Technology.