

VE320 Homework 5

Due Nov. 11, 23:59pm

In the following problems, if not stated, $T = 300\text{K}$, and:

For silicon pn junctions: $D_n=25\text{cm}^2/\text{s}$, $D_p=10\text{cm}^2/\text{s}$, $\tau_{n0}=5\times 10^{-7}\text{s}$, $\tau_{p0}=10^{-7}\text{s}$.

For GaAs pn junctions: $D_n=205\text{cm}^2/\text{s}$, $D_p=9.8\text{cm}^2/\text{s}$, $\tau_{n0}=5\times 10^{-8}\text{s}$, $\tau_{p0}=10^{-8}\text{s}$.

1. Consider a silicon n^+p junction diode. The critical electric field for breakdown in silicon is approximately $E_{crit} = 4 \times 10^5 \text{ V/cm}$. Determine the maximum p-type doping concentration such that the breakdown voltage is (a) 40 V and (b) 20 V.
2. A silicon p^+n junction has doping concentrations of $N_a = 2 \times 10^{17} \text{ cm}^{-3}$ and $N_d = 2 \times 10^{15} \text{ cm}^{-3}$. The cross-sectional area is 10^{-5} cm^2 . Calculate (a) V_{bi} and (b) the junction capacitance at reverse bias V_R (i) $V_R = 1\text{V}$, (ii) $V_R = 3\text{V}$, and (iii) $V_R = 5\text{V}$. (c) plot $1/C^2$ versus V_R and identify how the slope and intercept at the voltage axis are related to N_d and V_{bi} , respectively.
3. Consider an ideal silicon pn junction diode.
 - (a) What must be the ratio of N_d/N_a so that 90% of the current in the depletion region is due to the flow of electrons?
 - (b) Repeat part (a) if 80% of the current in the depletion region is due to the flow of holes?
4. Consider a silicon pn junction diode with an applied reverse-biased voltage of $V_R = 5\text{V}$. The doping concentrations are $N_d = N_a = 4 \times 10^{16} \text{ cm}^{-3}$ and the cross-sectional area is $A = 10^{-4} \text{ cm}^2$. Assume minority carrier lifetimes of $\tau_0 = \tau_{n0} = \tau_{p0} = 10^{-7}\text{s}$. Calculate
 - (a) the ideal reverse-saturation current,
 - (b) the reverse-biased generation current,
 - (c) the ratio of the generation current to ideal saturation current.
5. Consider a GaAs pn junction diode with a cross-sectional area of $A = 2 \times 10^{-4} \text{ cm}^2$ and doping concentrations of $N_d = N_a = 7 \times 10^{16} \text{ cm}^{-3}$. The electron and hole mobility values are $\mu_n = 5500 \text{ cm}^2/\text{V} \cdot \text{s}$ and $\mu_p = 220 \text{ cm}^2/\text{V} \cdot \text{s}$, respectively, and the lifetime values are $\tau_0 = \tau_{n0} = \tau_{p0} = 2 \times 10^{-8}\text{s}$. Calculate the ideal diode current at a
 - (a) reverse-biased voltage of $V_R = 3\text{V}$
 - (b) forward-bias voltage of $V_a = 0.6\text{V}$
 - (c) forward-bias voltage of $V_a = 0.8\text{V}$
 - (d) forward-bias voltage of $V_a = 1\text{V}$

The following questions are about BJT, you can use the transistor geometry shown below. You may also find the last page useful.

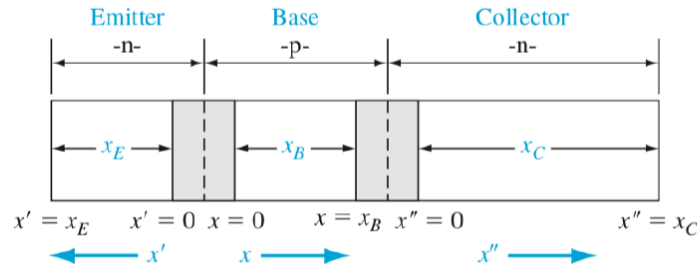


Figure 12.13 | Geometry of the npn bipolar transistor used to calculate the minority carrier distribution.

6. For a uniformly doped $n^{++}p^{+}n$ bipolar transistor in the thermal equilibrium,
 - (a) Sketch the energy-band diagram.
 - (b) Sketch the electric field through the device.
 - (c) Repeat parts (a) and (b) for the transistor biased in the forward-active region.
7. A uniformly doped silicon npn bipolar transistor at $T = 300K$ is biased in the forward-active mode. The doping concentrations are $N_E = 8 \times 10^{17} \text{ cm}^{-3}$, $N_B = 10^{16} \text{ cm}^{-3}$, and $N_C = 10^{15} \text{ cm}^{-3}$.
 - (a) Find the thermal-equilibrium values p_{E0} , n_{B0} , and p_{C0} .
 - (b) Calculate the values of n_B at $x = 0$ and p_E at $x' = 0$ for $V_{BE} = 0.64V$.
 - (c) Sketch the minority carrier concentrations through the device and label each curve.
8.
 - (a) The following currents are measured in a uniformly doped npn bipolar transistor. $I_{nE} = 0.50 \text{ mA}$, $I_{nC} = 0.495 \text{ mA}$, $I_{pE} = 3.5 \mu\text{A}$, $I_R = 5 \mu\text{A}$, $I_G = 0.5 \mu\text{A}$, $I_{pC0} = 0.5 \mu\text{A}$. Determine the following current gain parameters: γ , α_T , δ , α , β (see next page).
 - (b) If the required value of common-emitter current gain is $\beta = 120$, determine new values of I_{nC} , I_{pE} and I_R to meet this specification assuming $\gamma = \alpha_T = \delta$.
9. An npn bipolar transistor is biased in the forward-active mode. (a) The collector current is $I_C = 1.2 \text{ mA}$ when biased at $V_{CE} = 2 \text{ V}$. The Early voltage is $V_A = 120 \text{ V}$. Determine (i) the output resistance r_o , (ii) the output conductance g_o , and (iii) the collector current when biased at $V_{CE} = 4 \text{ V}$. (b) Repeat part (a) if the collector current is $I_C = 0.25 \text{ mA}$ when biased at $V_{CE} = 2 \text{ V}$ and the Early voltage is $V_A = 160 \text{ V}$.
10. A uniformly doped pnp silicon bipolar transistor has a base doping of $N_B = 10^{16} \text{ cm}^{-3}$, a collector doping of $N_C = 10^{15} \text{ cm}^{-3}$, a metallurgical base width of $x_{B0} = 0.70 \mu\text{m}$, a base minority carrier diffusion coefficient of $D_B = 10 \text{ cm}^2/\text{s}$, and a B-E cross-sectional area of $A_{BE} = 10^{-4} \text{ cm}^2$. The transistor is biased in the forward-active mode with $V_{EB} = 0.625 \text{ V}$. Neglecting the B-E space charge width and assuming $x_B \ll L_B$, (a) determine the change in neutral base width as V_{BC} changes from 1 to 5 V, (b) find the corresponding change in collector current, (c) estimate the Early voltage, and (d) find the output resistance.

$$\alpha = \left(\frac{J_{nE}}{J_{nE} + J_{pE}} \right) \left(\frac{J_{nC}}{J_{nE}} \right) \left(\frac{J_{nE} + J_{pE}}{J_{nE} + J_R + J_{pE}} \right)$$