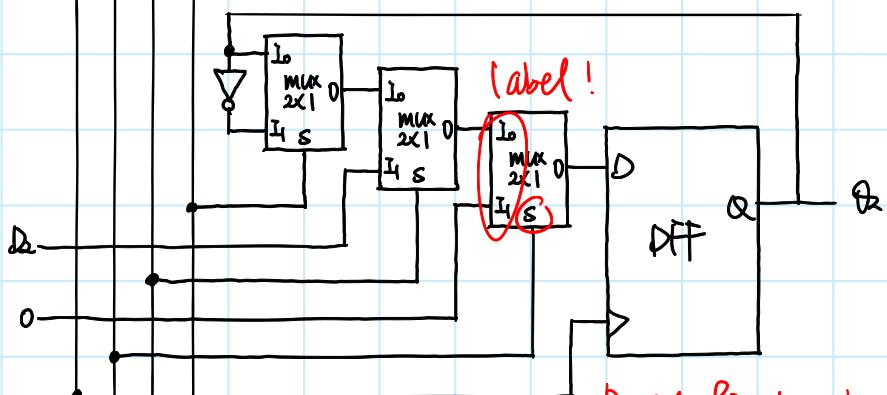
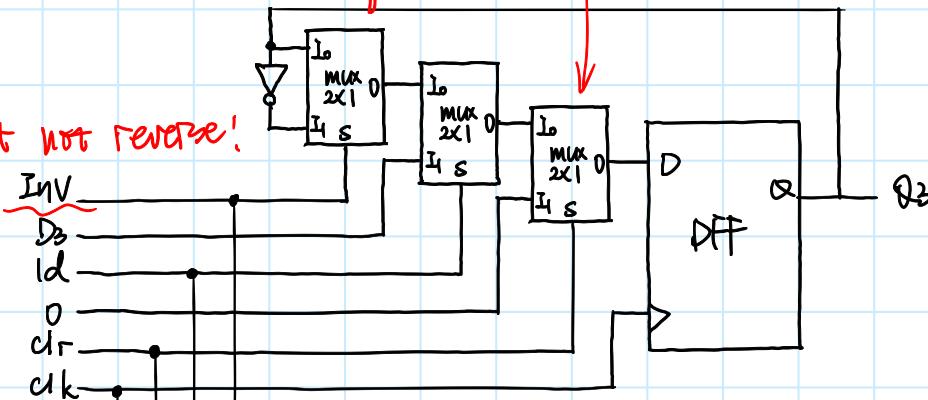


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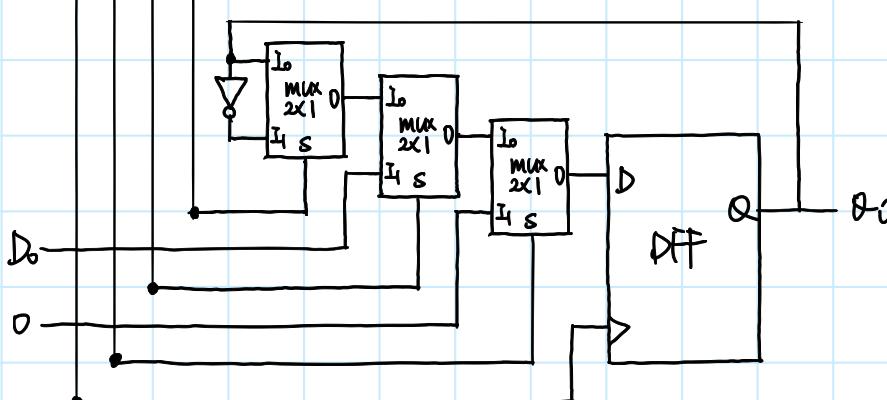
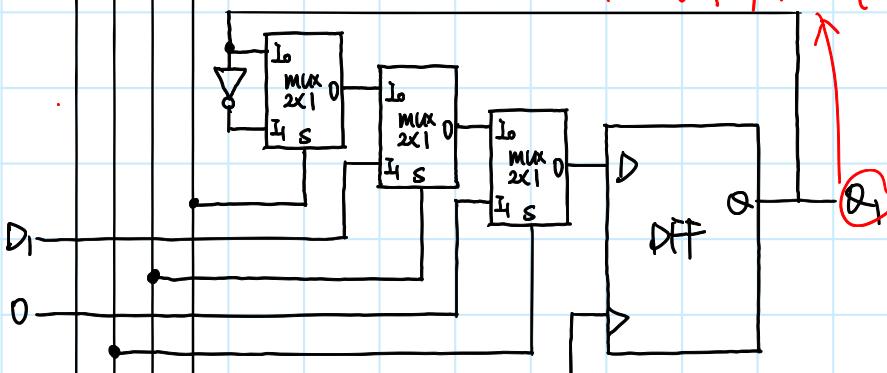
Problem 1

Mind the sequence here. Highest priority

Invert not reverse!



Do not forget output.



Note: There is no "4-bit Dff". it is a 4-bit register actually. Also a FF without any control signal does not have a "reset".

Also, draw your circuit diagram clearly. Any wire mixing together or anything confusing will be considered incorrect.

Please do not take it for granted that I will struggle to figure out what you are drawing. For any drawing that makes me feel uncomfortable, deductions are made.

Especially, we are studying FSM now, circuits are becoming more complex, please make sure you draw everything clearly to make my life easier and make your life easier as well in return.

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Problem 2.

Design a circuit whose 8-bit output is 5 times of its 8-bit input which is an unsigned binary number.
Ignore overflow issues. (Use building blocks)

