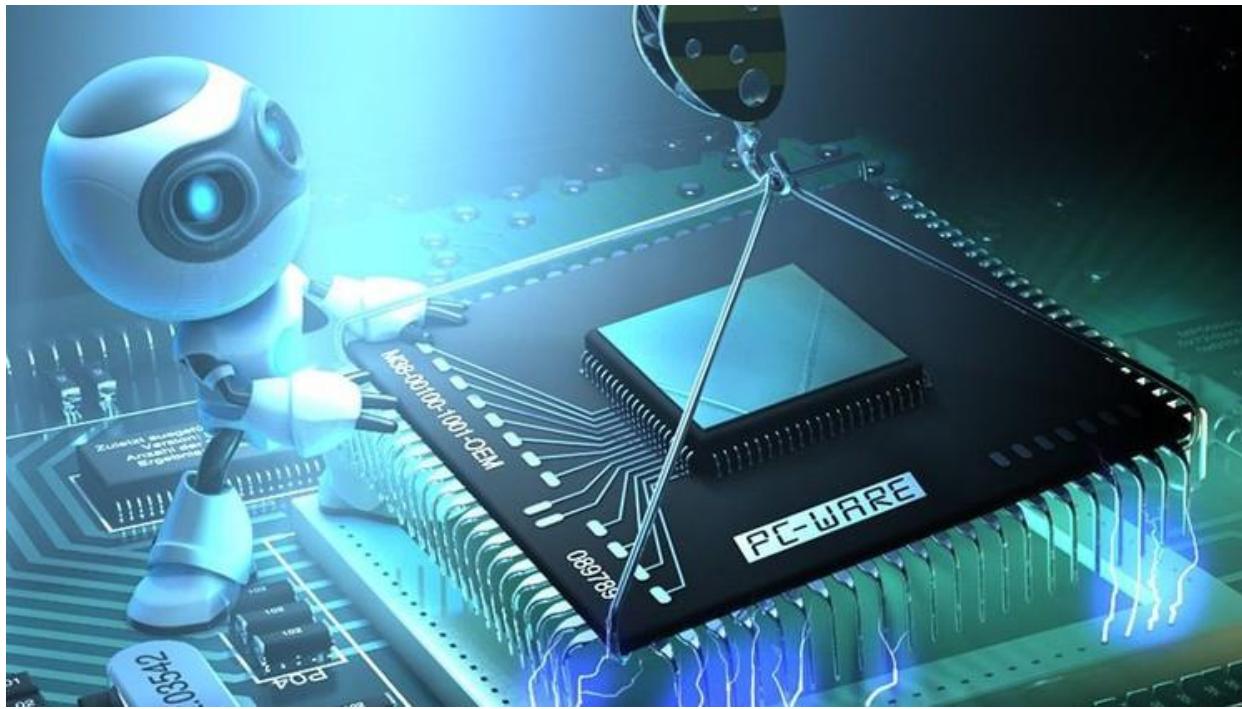


# Available Registers



# Programmer Vision



## ❑ Available registers

32 bits	16 bits	8 bits	
%eax	%ax	%ah , %al	
%ebx	%bx	%bh , %bl	
%ecx	%cx	%ch , %cl	
%edx	%dx	%dh , %dl	
%esi	%si		
%edi	%di		
%esp	%sp		Reserved for specific use of subroutines
%ebp	%bp		
%eip			Program counter
%eflags			Status word



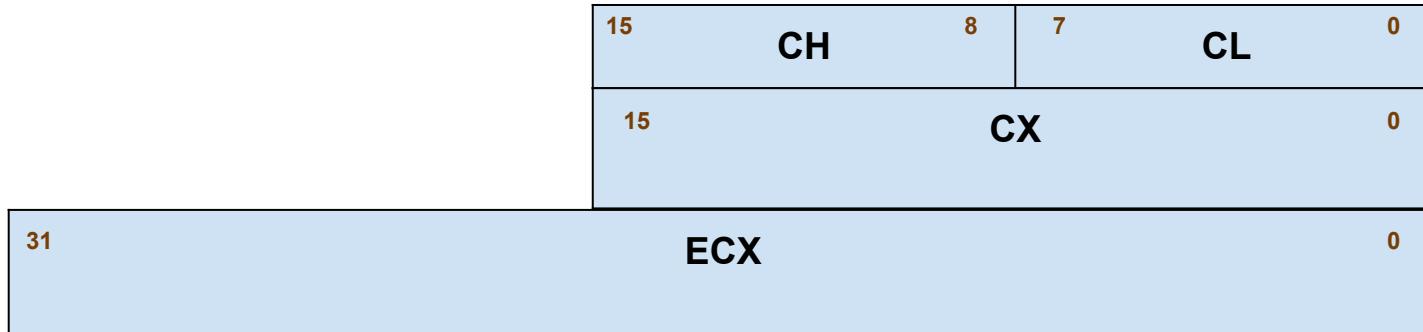
# Example

15	CH	8	7	CL	0
15		CX			0
31	ECX				0



# Example

**%CX = 0x42A5**

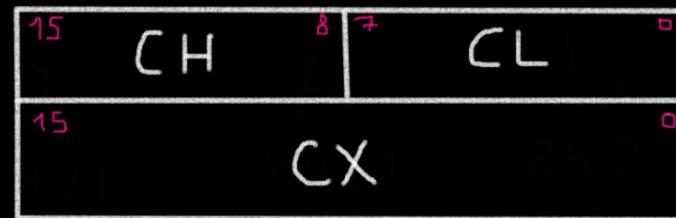




# Example

$$\therefore CH = 0100\ 0010$$

$$\therefore CL = 1010\ 0101$$



$$\therefore CX = 0x42A5 = \begin{smallmatrix} 15 \\ 0100\ 0010\ 1010\ 0101 \end{smallmatrix}$$



# Basic Data Types

