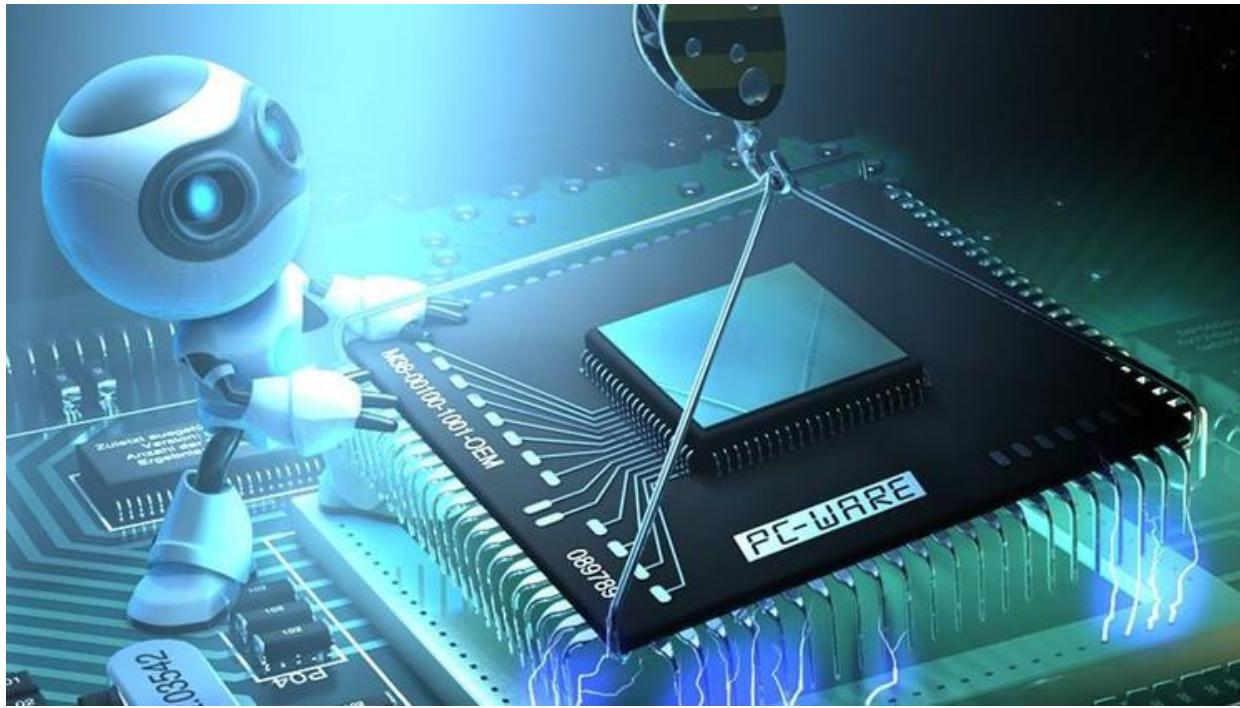


# Laboratory Session 1



# Objective



The objective of this session is to introduce assembly programming for the x86 architecture. Specifically, aspects such as the programming of control structures (conditional and iterative) and access to structured elements (vectors and matrices) will be worked on.

# Tasks



This laboratory session is divided into two tasks:

- Previous Study
- Work to be done during practice

# Prior Knowledge



To carry out this practice you should review the direct translations from C to x86 assembler of the control structures that you have seen in the theory lectures. Also you should review the x86 addressing modes.

# Prior Knowledge



## Vectors

- Declaration in C:

```
type name[size];           // indexed starting at 0
```

- Storage in consecutive memory locations

- Access element  $V[i]$ : **@start  $V + i * size$**  ( $size$ : size of the elements of  $V$ )

# Prior Knowledge



## Vectors

- Declaration in C:

```
type name[size];           // indexed starting at 0
```

- Storage in consecutive memory locations

- Access element  $V[i]$ :  $\text{@start } V + i * \text{size}$  ( $\text{size}$ : size of the elements of  $V$ )

$V$

5	2	-4	1	3
0	1	2	3	4

$\text{@}V \equiv V[0]$

$V[3] \rightarrow \text{@}V + 3 * 4$

# Prior Knowledge



## Matrices

- Declaration in C:

```
type name[NumRows][NumColumns];      // indexed starting at (0,0)
```

- Storage by rows in consecutive memory locations

- Access element A[i][j]: **@start A + (i\*NumColumns + j) \* size**

(size: size of the elements of A)