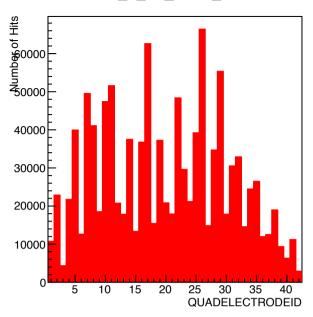
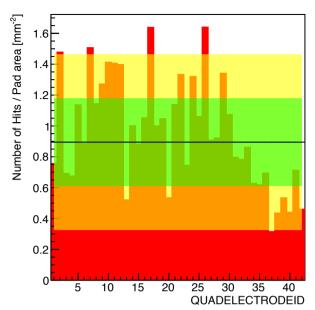
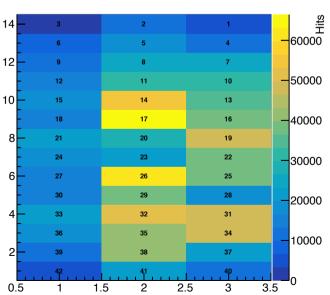
QS3C_2_L1_31kV_280519



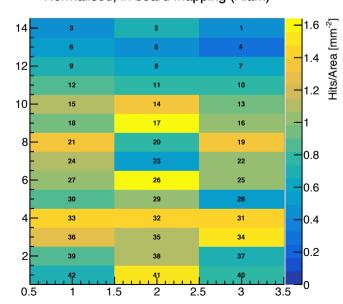
Normalised to pad area (Benoit)

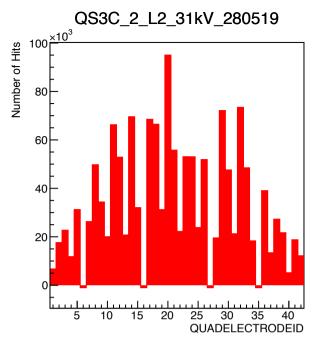


Profile, in board mapping (Alam)

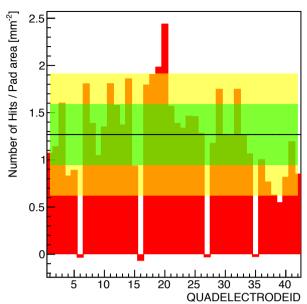


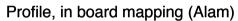
Normalised, in board mapping (Alam)

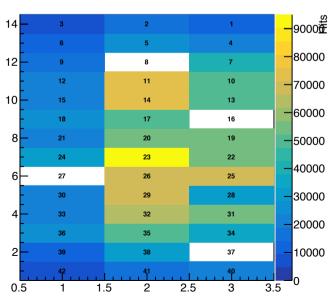


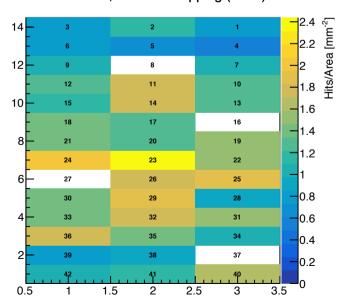




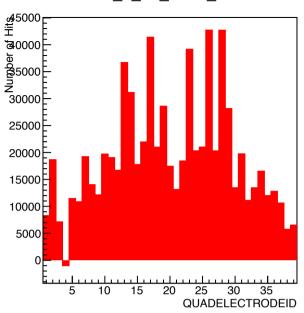




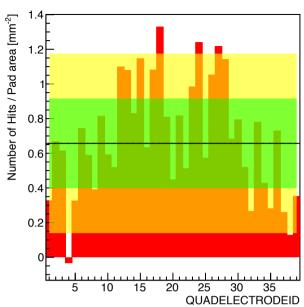




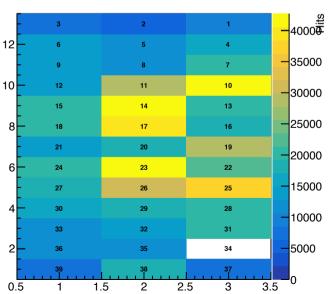
QS3C_2_L3_31kV_280519



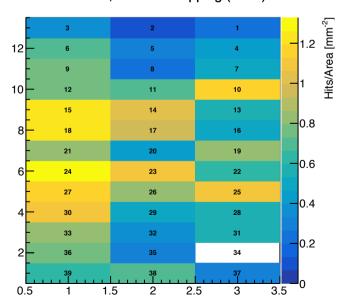
Normalised to pad area (Benoit)



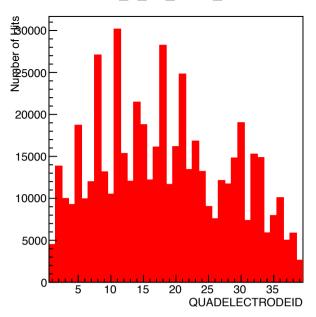
Profile, in board mapping (Alam)

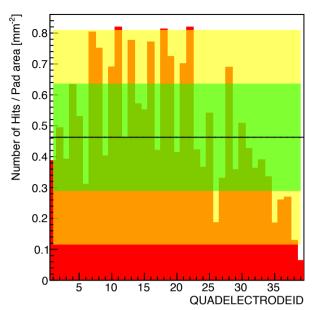


Normalised, in board mapping (Alam)

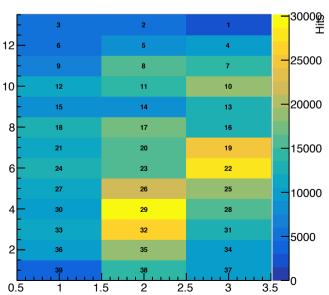


QS3C_2_L4_31kV_280519

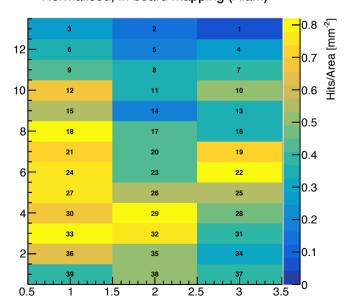




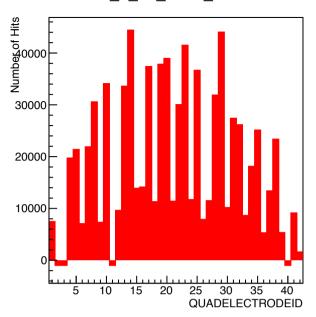
Profile, in board mapping (Alam)

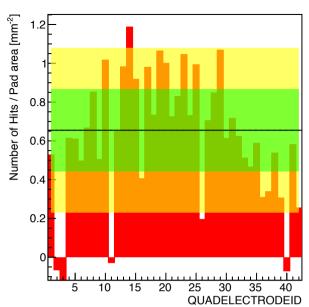


Normalised, in board mapping (Alam)

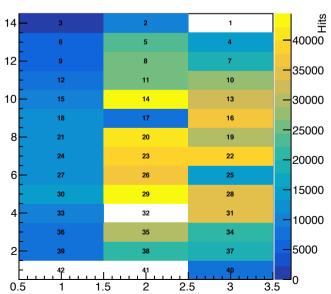


QS3C_3_L1_31kV_20190410

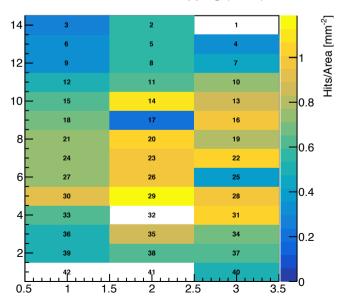




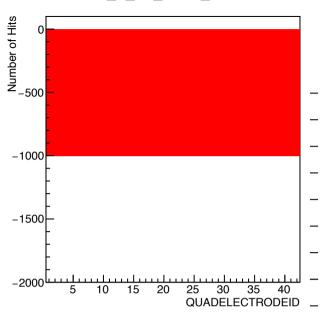
Profile, in board mapping (Alam)



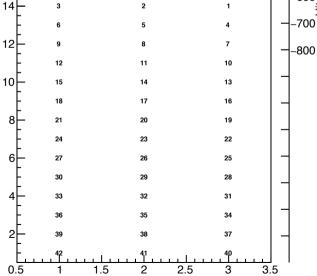
Normalised, in board mapping (Alam)



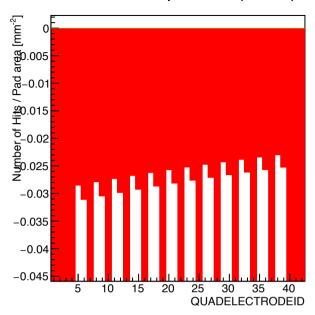
QS3C_3_L2_31kV_20190410

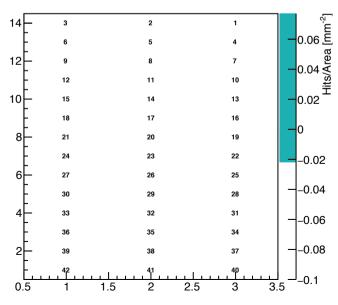


Profile, in board mapping (Alam) --500

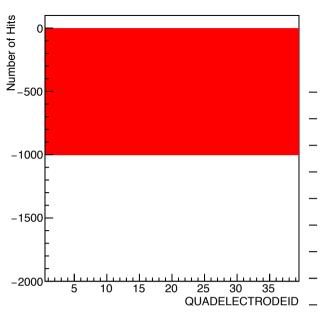


Normalised to pad area (Benoit)



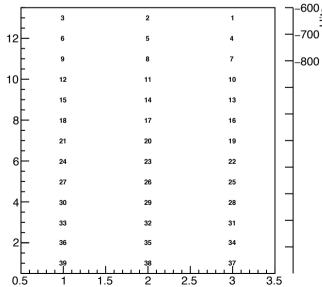


QS3C_3_L3_31kV_20190410

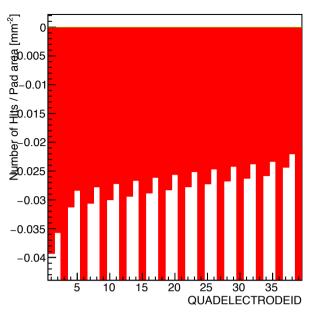


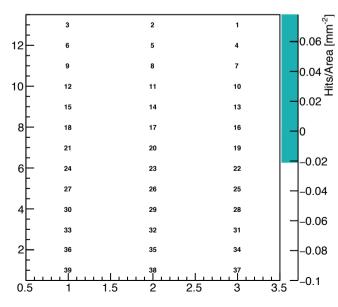
Profile, in board mapping (Alam)

--500

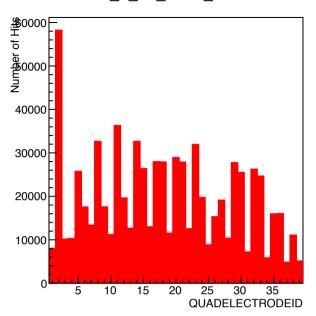


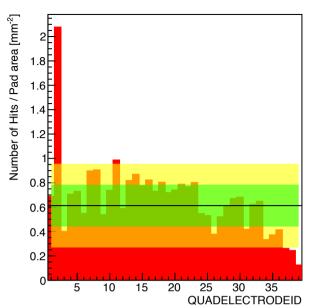
Normalised to pad area (Benoit)



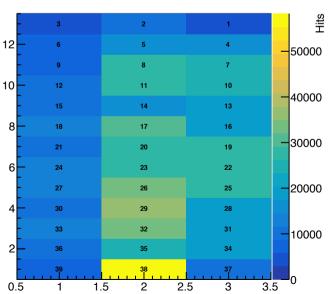


QS3C_3_L4_31kV_20190410

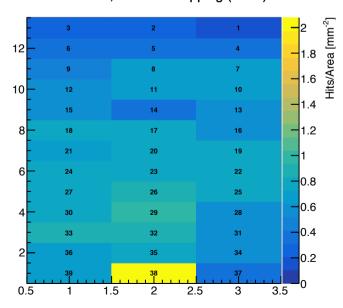




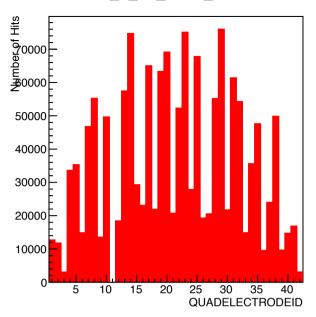
Profile, in board mapping (Alam)

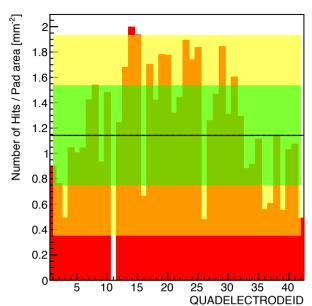


Normalised, in board mapping (Alam)

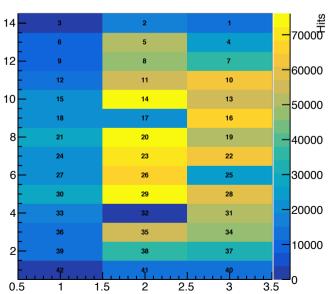


QS3C_3_L1_31kV_17041805

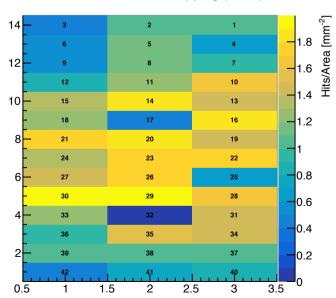




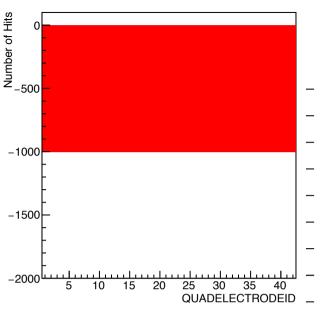
Profile, in board mapping (Alam)



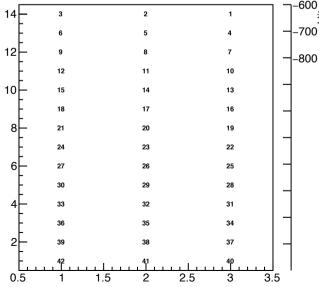
Normalised, in board mapping (Alam)

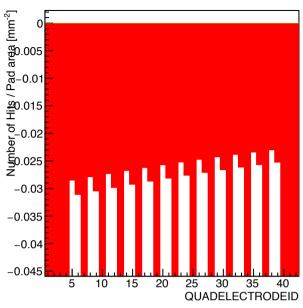


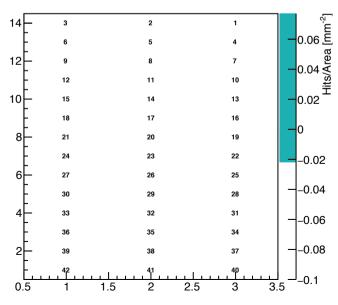
QS3C_3_L2_31kV_17041805



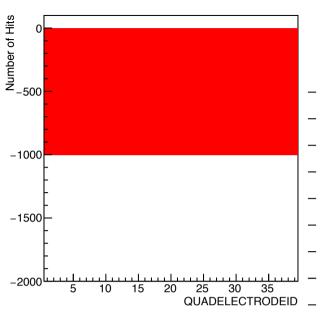






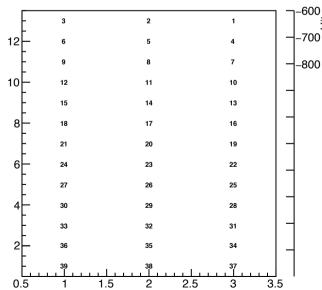


QS3C_3_L3_31kV_17041805

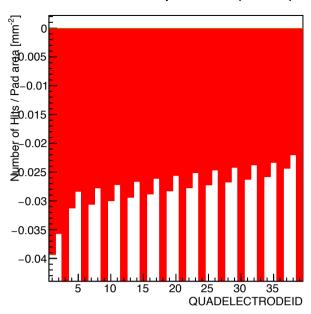


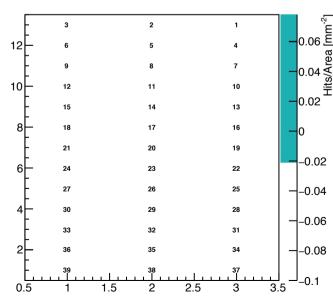
Profile, in board mapping (Alam)

--500

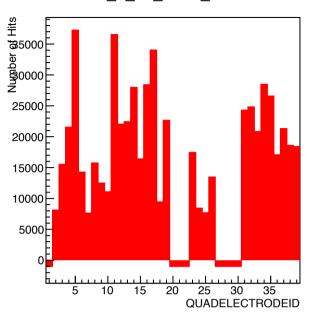


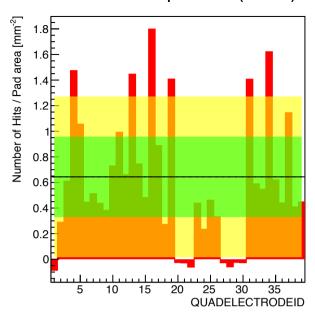
Normalised to pad area (Benoit)



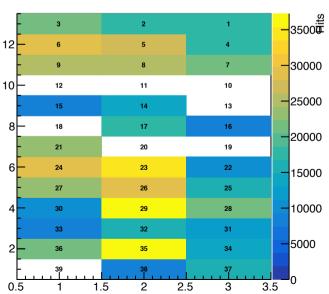


QS3C_3_L4_31kV_17041805





Profile, in board mapping (Alam)



Normalised, in board mapping (Alam)

