

M1	Refresh	Mem Read	M1	Refresh	Mem Read	Mem Read	Mem Write	M1	Refresh	Mem Read	I/O Write (OUT)	M1	Refresh	Mem Read	I/O Read (IN)	M1
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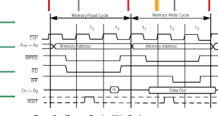


Figure 6. Memory Read or Write Cycle

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