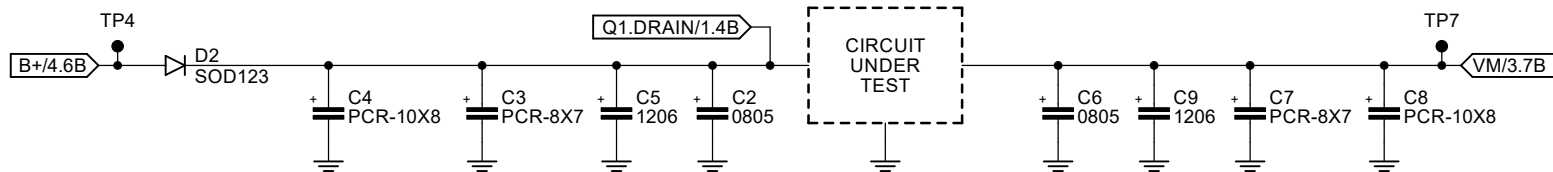
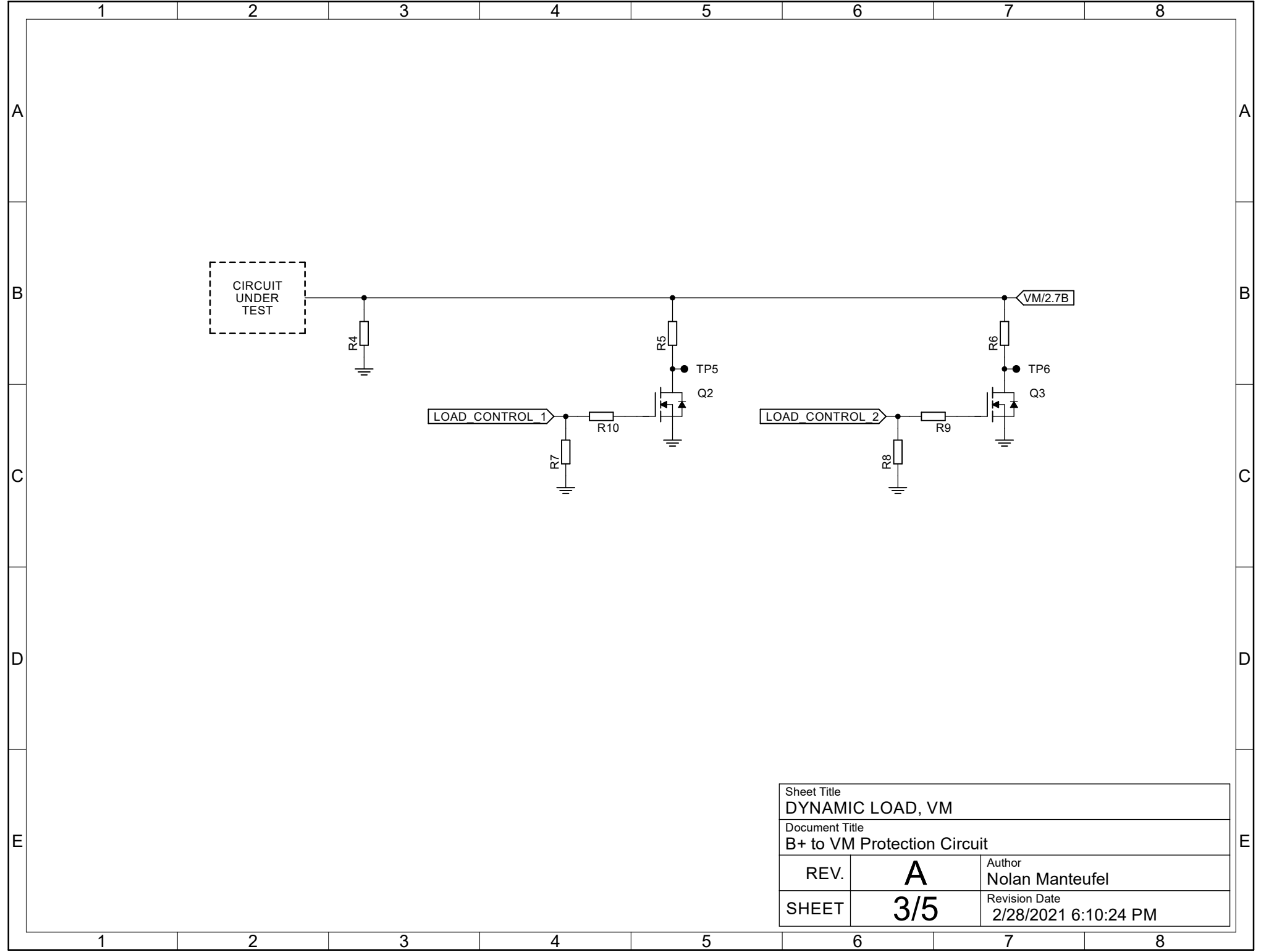


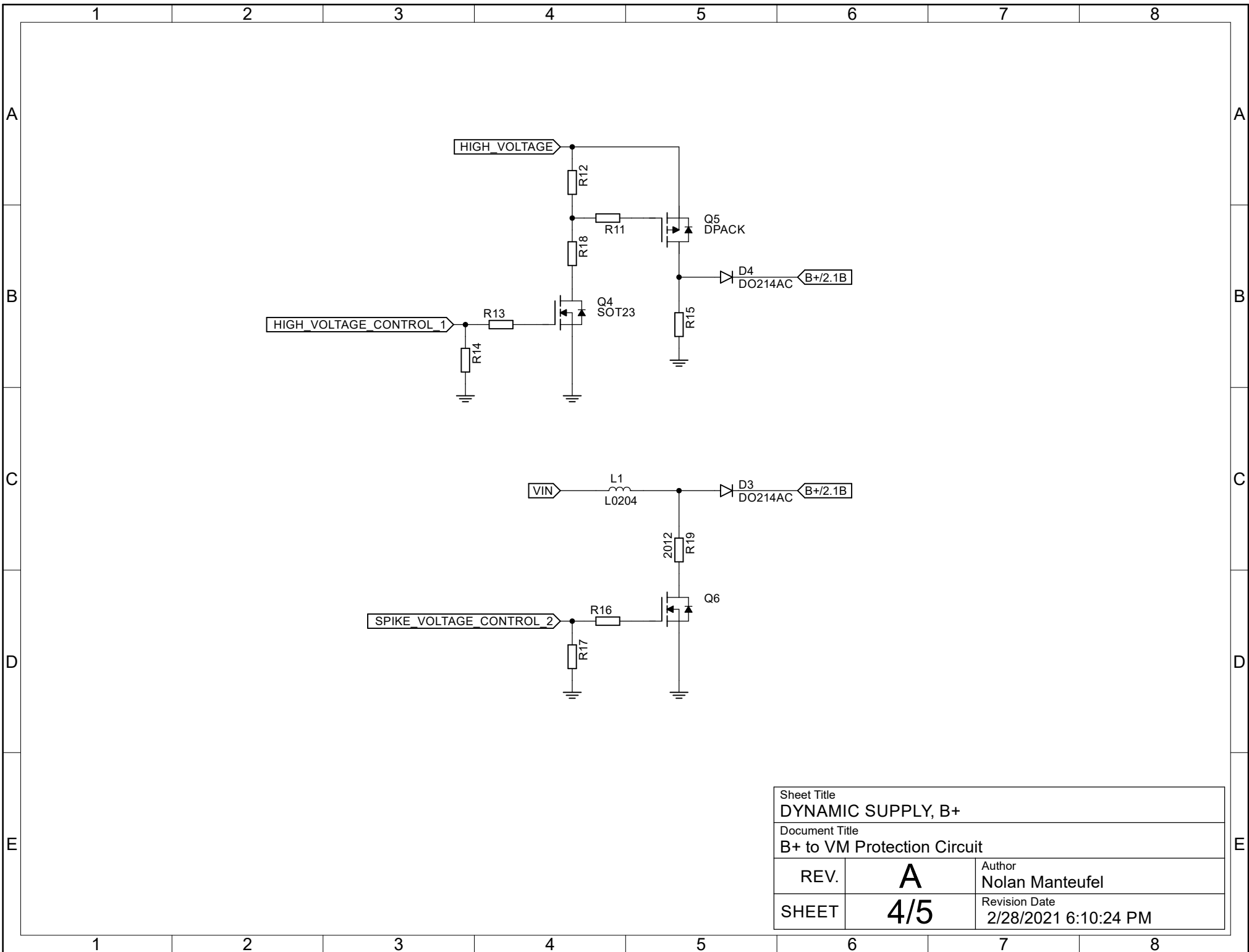
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CIRCUIT UNDER TEST		
Document Title		
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REV.	A	Author Nolan Manteufel
SHEET	1/5	Revision Date 2/28/2021 6:10:24 PM



Sheet Title		
VARIOUS CAPACITORS, Q1.DRAIN AND VM		
Document Title		
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REV.	A	Author Nolan Manteufel
SHEET	2/5	Revision Date 2/28/2021 6:10:24 PM



Sheet Title		
DYNAMIC LOAD, VM		
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SHEET	3/5	Revision Date 2/28/2021 6:10:24 PM



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A									A
B									B
C									C
D									D
E									E
	1	2	3	4	5	6	7	8	

Sheet Title		
BOARD INFORMATION		
Document Title		
B+ to VM Protection Circuit		
REV.	A	Author Nolan Manteufel
SHEET	5/5	Revision Date 2/28/2021 6:10:24 PM