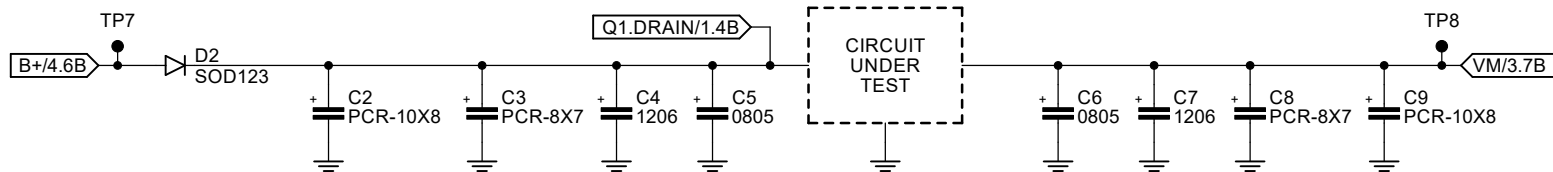
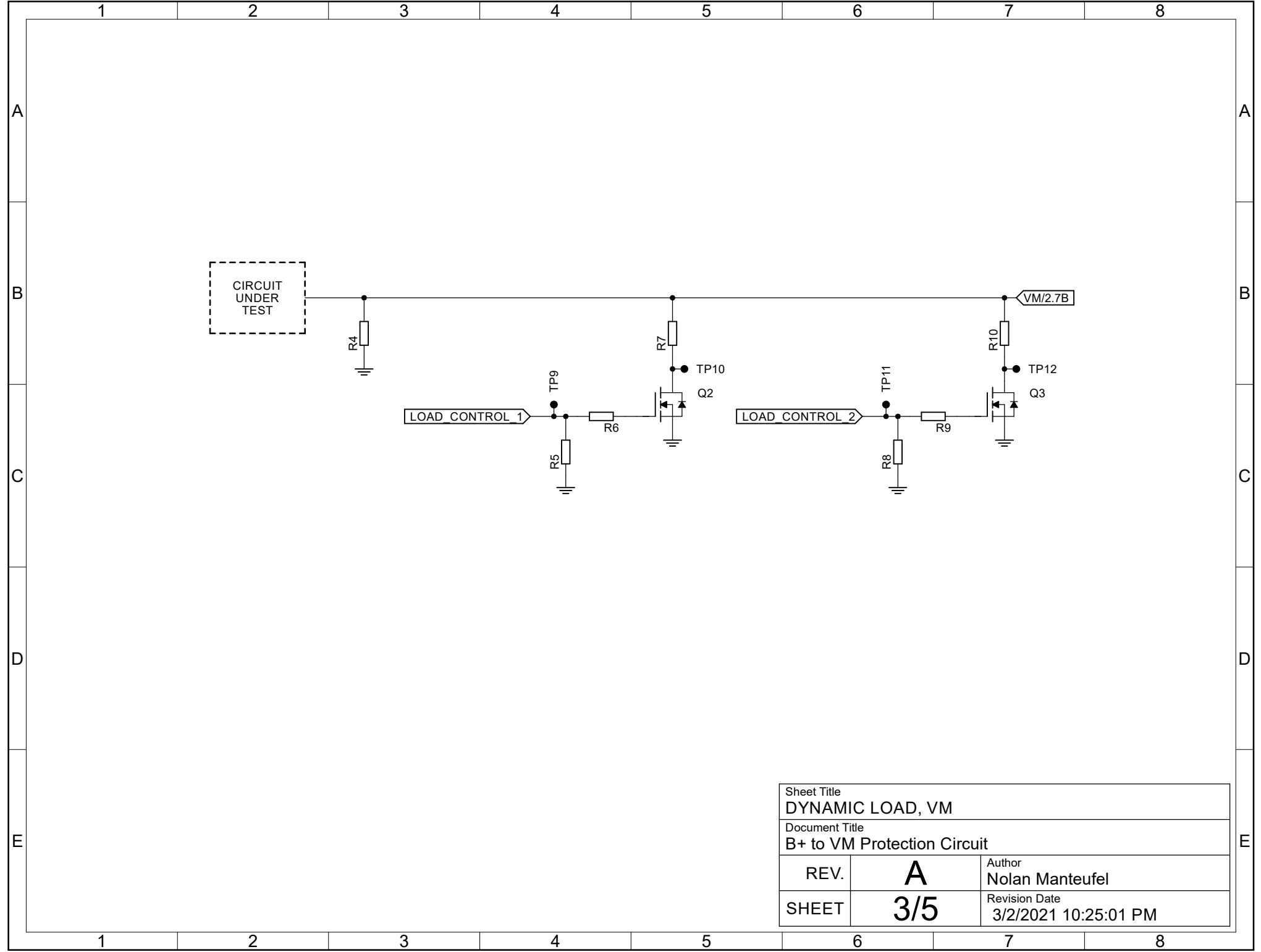


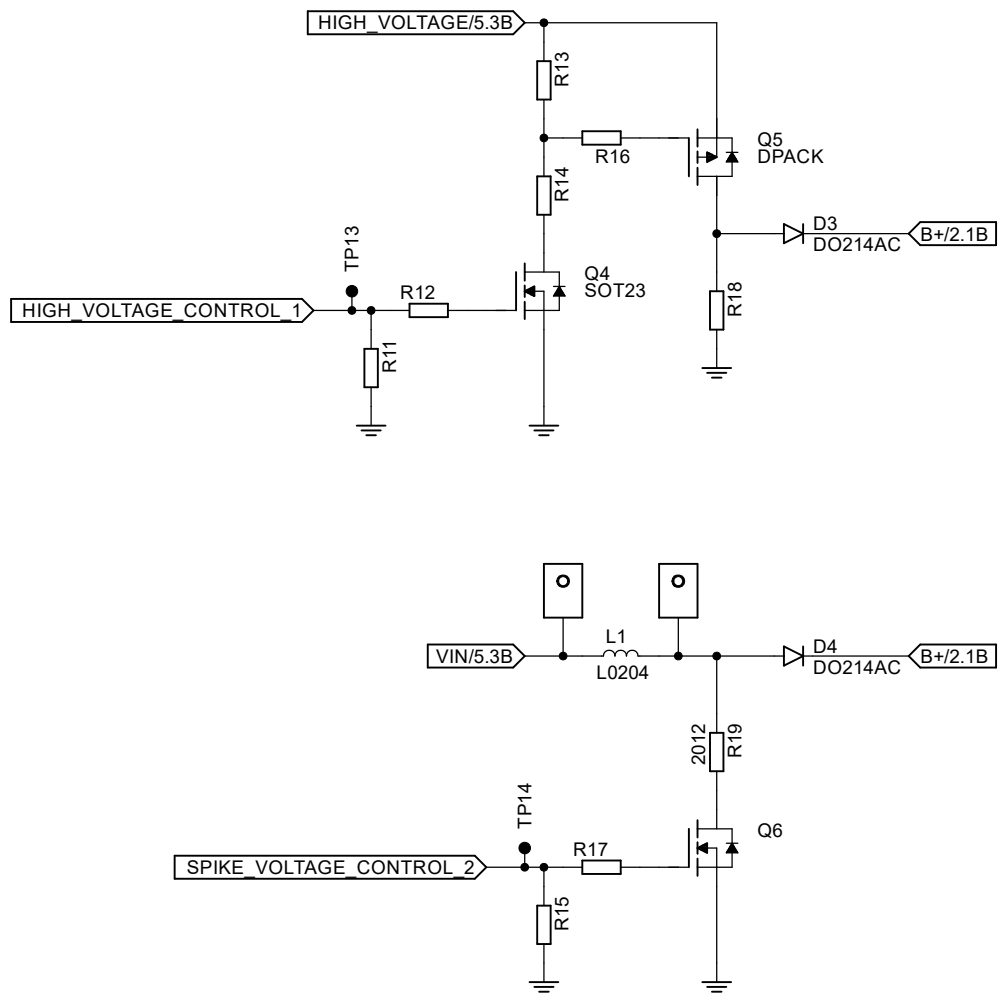
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B+ to VM Protection Circuit		
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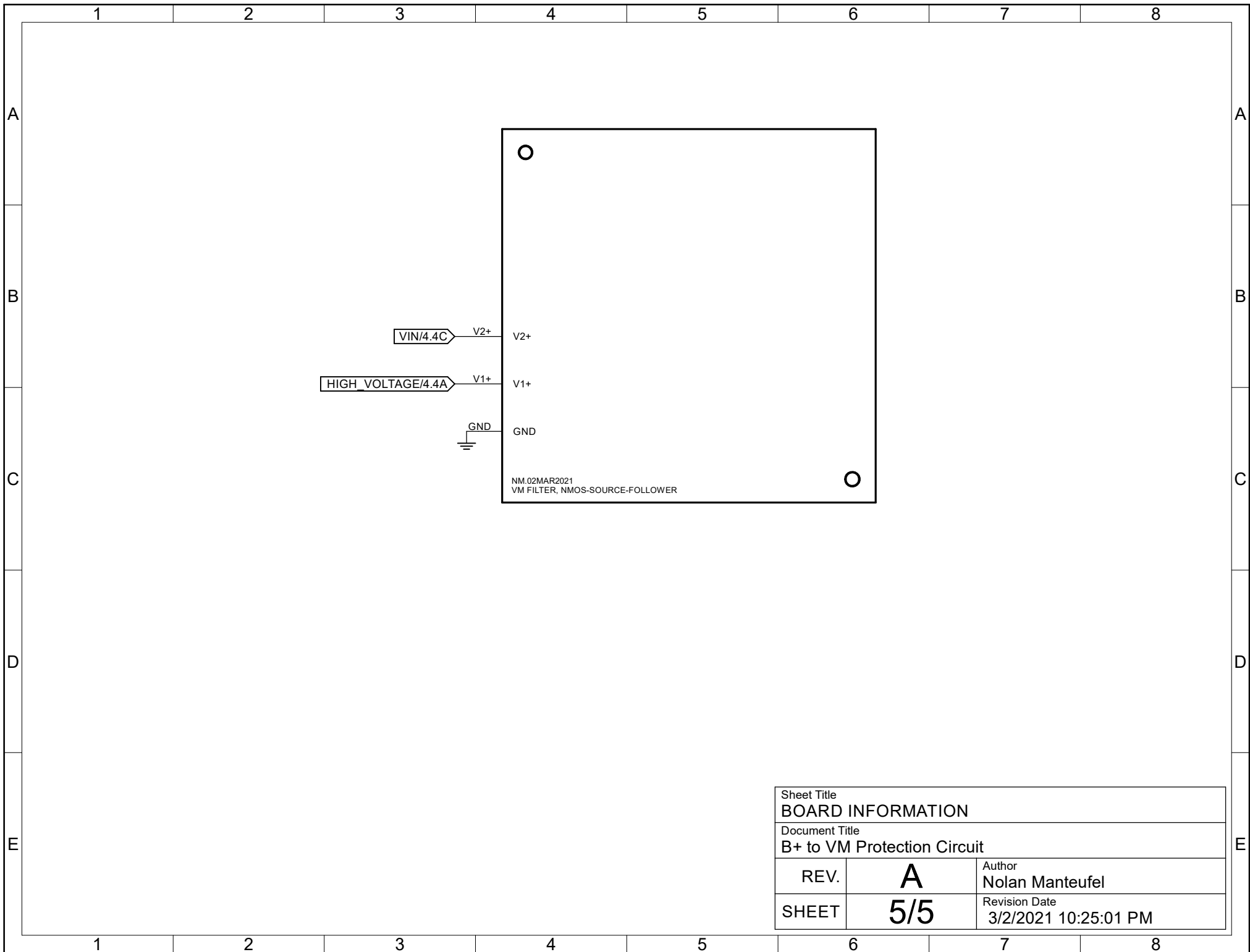
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VARIOUS CAPACITORS, Q1.DRAIN AND VM		
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REV.	A	Author Nolan Manteufel
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DYNAMIC LOAD, VM		
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DYNAMIC SUPPLY, B+		
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BOARD INFORMATION		
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