

# Selecting Parts for Your miniPCB

## Introduction

This document provides information about component footprints and packages typically used on miniPCBs.

## Revision History

Revision	Note	Date
A	Initial Release	DDMMYYYY

## Resistors

Surface Mount: 0805

Through Hole: 7 mm between holes

## Capacitors

Surface Mount: 0805

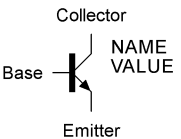
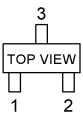
Through Hole: 5 mm between holes

## Transistors

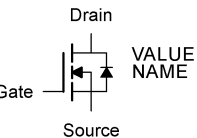
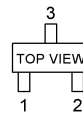
BJT symbols are used in all miniPCB schematics. It is possible to use MOSFET devices that have the footprints and pinouts shown here.

N-type MOSFETs can be used in place of NPN BJTs; and P-type MOSFETs can be used in place of PNP BJTs.

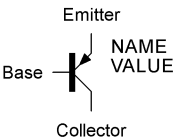
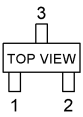
### Surface Mount: SOT23 (3 pin)

PART: NPN Bipolar Junction Transistor									
FOOTPRINT: SOT23									
SCHEMATIC SYMBOL	PINOUT								
	 <table border="1"> <thead> <tr> <th>PIN</th><th>DESCRIPTION</th></tr> </thead> <tbody> <tr> <td>1</td><td>Base</td></tr> <tr> <td>2</td><td>Emitter</td></tr> <tr> <td>3</td><td>Collector</td></tr> </tbody> </table>	PIN	DESCRIPTION	1	Base	2	Emitter	3	Collector
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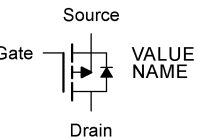
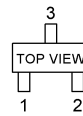
Note: Pinouts vary per parts. Double-check that your part matches this pinout.

PART: N-Channel MOSFET, Enhancement Mode									
FOOTPRINT: SOT23									
SCHEMATIC SYMBOL	PINOUT								
	 <table border="1"> <thead> <tr> <th>PIN</th><th>DESCRIPTION</th></tr> </thead> <tbody> <tr> <td>1</td><td>Gate</td></tr> <tr> <td>2</td><td>Source</td></tr> <tr> <td>3</td><td>Drain</td></tr> </tbody> </table>	PIN	DESCRIPTION	1	Gate	2	Source	3	Drain
PIN	DESCRIPTION								
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Note: Pinouts vary per parts. Double-check that your part matches this pinout.

PART: PNP Bipolar Junction Transistor									
FOOTPRINT: SOT23									
SCHEMATIC SYMBOL	PINOUT								
	 <table border="1"> <thead> <tr> <th>PIN</th><th>DESCRIPTION</th></tr> </thead> <tbody> <tr> <td>1</td><td>Base</td></tr> <tr> <td>2</td><td>Emitter</td></tr> <tr> <td>3</td><td>Collector</td></tr> </tbody> </table>	PIN	DESCRIPTION	1	Base	2	Emitter	3	Collector
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## Through Hole: TO92

PART: NPN Bipolar Junction Transistor									
FOOTPRINT: TO-92									
<p>SCHEMATIC SYMBOL</p>	<p>PINOUT</p> <table border="1"> <thead> <tr> <th>PIN</th><th>DESCRIPTION</th></tr> </thead> <tbody> <tr> <td>1</td><td>Emitter</td></tr> <tr> <td>2</td><td>Base</td></tr> <tr> <td>3</td><td>Collector</td></tr> </tbody> </table>	PIN	DESCRIPTION	1	Emitter	2	Base	3	Collector
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Note: Pinouts vary per parts. Double-check that your part matches this pinout.

PART: N-Channel MOSFET, Enhancement Mode									
FOOTPRINT: TO-92									
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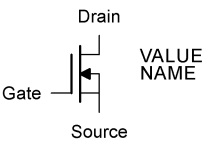
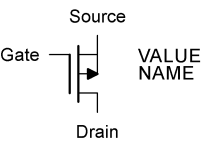
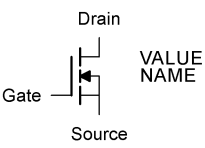
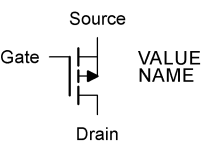
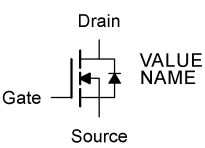
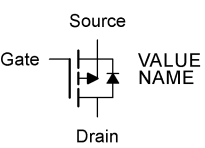
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FOOTPRINT: TO-92									
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PIN	DESCRIPTION								
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Note: Pinouts vary per parts. Double-check that your part matches this pinout.

It is good to know that a MOSFET is either a depletion-mode type or enhancement-mode type.

Standard schematic symbols for each type are shown here:

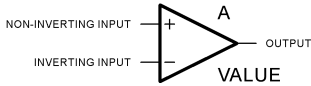

	NMOS	PMOS
DEPLETION MODE		
ENHANCEMENT MODE		
ENHANCEMENT MODE ...with body-diode drawn		

## Operational Amplifiers

### Single Opamp

Surface Mount: SOT23-5 (5 pins)

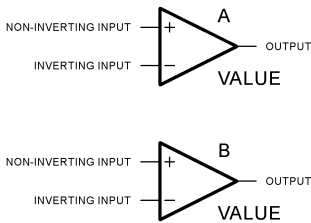
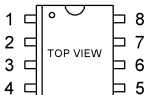
Through Hole: N/A (no miniPCBs use a THD single opamp)

SINGLE OPAMP													
FOOTPRINT: SOT23-5													
<b>SCHEMATIC SYMBOL</b> 	<b>PINOUT</b>  <table border="1"> <thead> <tr> <th>PIN</th><th>DESCRIPTION</th></tr> </thead> <tbody> <tr> <td>1</td><td>Output, A</td></tr> <tr> <td>2</td><td>-V Supply (Vss)</td></tr> <tr> <td>3</td><td>Non-Inverting Input, A</td></tr> <tr> <td>4</td><td>Inverting Input, A</td></tr> <tr> <td>5</td><td>+V Supply (Vdd)</td></tr> </tbody> </table>	PIN	DESCRIPTION	1	Output, A	2	-V Supply (Vss)	3	Non-Inverting Input, A	4	Inverting Input, A	5	+V Supply (Vdd)
PIN	DESCRIPTION												
1	Output, A												
2	-V Supply (Vss)												
3	Non-Inverting Input, A												
4	Inverting Input, A												
5	+V Supply (Vdd)												

### Dual Opamp

Surface Mount: SOIC8 (8 pins)

Through Hole: DIP8 (8 pins)

DUAL OPAMP																			
FOOTPRINT: SOIC8, DIP8																			
<b>SCHEMATIC SYMBOL</b> 	<b>PINOUT</b>  <table border="1"> <thead> <tr> <th>PIN</th><th>DESCRIPTION</th></tr> </thead> <tbody> <tr> <td>1</td><td>Output, A</td></tr> <tr> <td>2</td><td>Inverting Input, A</td></tr> <tr> <td>3</td><td>Non-Inverting Input, A</td></tr> <tr> <td>4</td><td>-V Supply</td></tr> <tr> <td>5</td><td>Non-Inverting Input, B</td></tr> <tr> <td>6</td><td>Inverting Input, B</td></tr> <tr> <td>7</td><td>Output, B</td></tr> <tr> <td>8</td><td>+V Supply</td></tr> </tbody> </table>	PIN	DESCRIPTION	1	Output, A	2	Inverting Input, A	3	Non-Inverting Input, A	4	-V Supply	5	Non-Inverting Input, B	6	Inverting Input, B	7	Output, B	8	+V Supply
PIN	DESCRIPTION																		
1	Output, A																		
2	Inverting Input, A																		
3	Non-Inverting Input, A																		
4	-V Supply																		
5	Non-Inverting Input, B																		
6	Inverting Input, B																		
7	Output, B																		
8	+V Supply																		