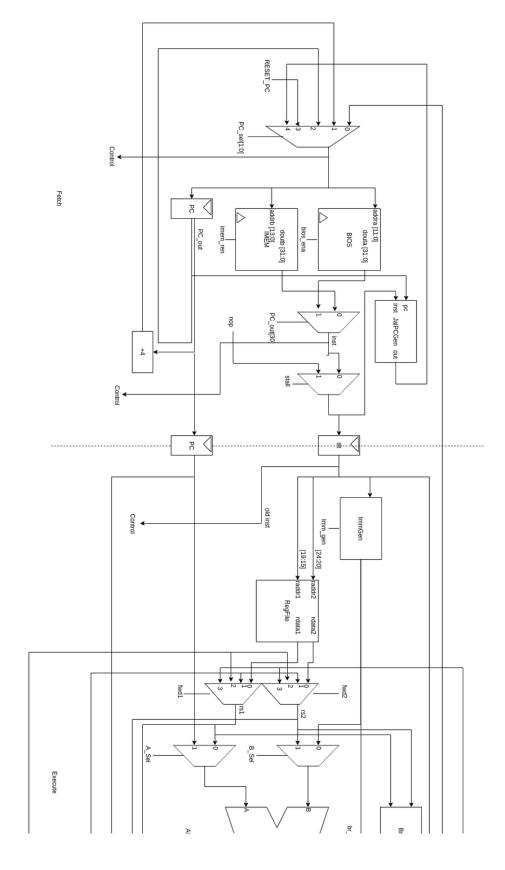
## **Appendix**



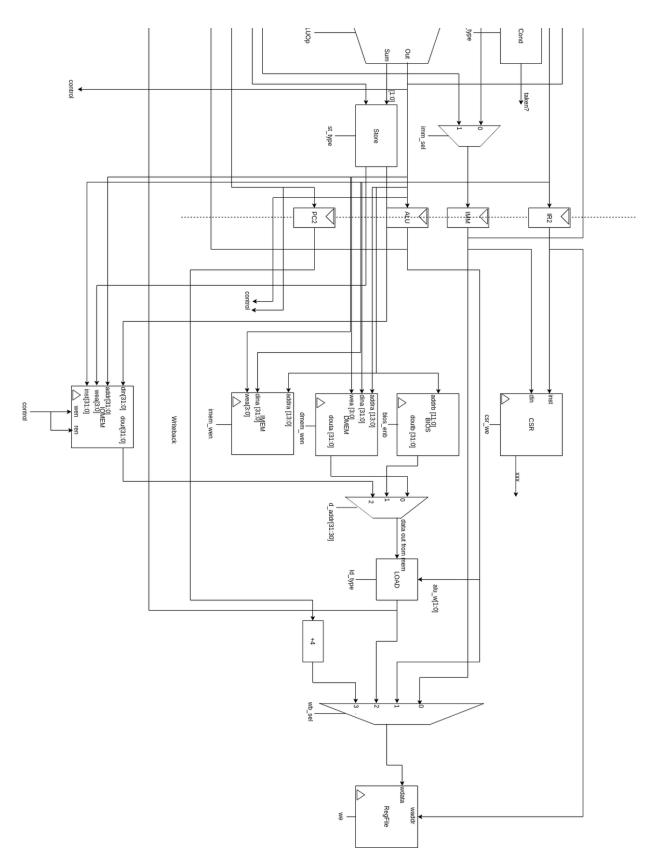


Figure 1: RISC-V CPU datapath.