## **Outputs**

```
VCD info: dumpfile alu_testbench.vcd opened for output.
Signed Add: a = 10, b = -2, out = 8
Unsiged Add: a = 1, b = 4294967294, out = 4294967295
Signed Subtract: a = 10, b = -2, out = 12
Unsigned Subtract: a = 4294967294, b = 1, out = 4294967293
And: a = 0, b = 1, out = 0
Or: a = 0, b = 1, out = 1
Shift Right Arithmetic: a = -4, b = 1, out = 2
Pass
```

```
~/Documents/eecs151/hw/2 vvp hamming
VCD info: dumpfile hamming_testbench.vcd opened for output.
Error code: 11
Error code: 10
Error code: 00
Error code: 00
Error code: 01
Pass
```

```
~/Documents/eecs151/hw/2 vvp count
VCD info: dumpfile count_testbench.vcd opened for output.
Count: 50
Count: 25
Pass
```