

# Ananya Singh

AI Engineer

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## Contact

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Address: San Francisco, USA

<https://linkedin.com/in/ananyasingh> | <https://github.com/ananyasingh> | <https://ananya-ai.dev>

Date of Birth: 1996-03-20

Nationality: Indian

## Profile

AI Engineer with 5+ years of experience in NLP, LLM fine-tuning, and applied machine learning. Published author in AI conferences and contributor to open-source ML frameworks.

## Experience

### AI Research Engineer — DeepTech Labs (San Francisco, USA)

Jan 2022 – Present

- Developed large-scale transformer models optimized for deployment.
- Published 3 research papers in ACL and NeurIPS.
- Mentored junior ML engineers on distributed training.

Tech: PyTorch, TensorFlow, Transformers, CUDA

## Projects

ChatSummarizer — Python, Transformers, FastAPI

AI tool for summarizing Slack/Teams meetings using LLMs.

<https://github.com/ananyasingh/chatsummarizer>

May 2021 – Oct 2021

## Education

Master — Stanford University

Faculty: Computer Science • Major: Artificial Intelligence • CGPA: 3.9

2018 – 2020

- Best Thesis Award

Bachelor — Indian Institute of Technology, Bombay

Faculty: Computer Engineering • Major: Computer Engineering • CGPA: 9.2

2014 – 2018

## Skills

- Python
- PyTorch
- TensorFlow

- NLP
- LLMs
- Deep Learning
- MLOps
- CUDA
- Distributed Training
- Data Engineering

## **Awards**

NeurIPS Best Poster — NeurIPS, 2023

For paper on efficient fine-tuning of LLMs.

## **Certificates**

- AWS Certified Machine Learning – Specialty

## **Publications**

- Singh, A. 'Optimizing Large Language Models for Efficiency', NeurIPS 2023.

## **Languages**

- English — Fluent
- Hindi — Native