



U3A

PA0_WKUP	PA0	AA3	PA0/TIM2_TIM5_CH1/TIM2_TIM8_ETR/TIM15_BKIN/USART2_CTS_NSS/UART4_TX/SDMMC2_CMD/SAI2_SD_B/ETH1_GMII_MII_CRS/ADC1_INP16/WKUP1
ETH_RX_CLK	PA1	V4	PA1/ETH_CLK/TIM2_TIM5_CH2/LPTIM3_OUT/TIM15_CH1N/USART2_RTS_DE/UART4_RX/QUADSPI_BK1_IO3/SAI2_MCLK_B/ETH1_GMII_MII_RGMII_RX_CLK/ETH1_RMII_REF_CLK/LCD_R2/ADC1_INP17_INN16
ETH_MDIO	PA2	AB2	PA2/TIM2_TIM5_CH3/LPTIM4_OUT/TIM15_CH1/USART2_TX/SAI2_SCK_B/SDMMC2_D0DIR/ETH1_MDIO/MDIOS_MDIO/LCD_R1/ADC1_INP14/WKUP2
	PA3	T4	PA3/TIM2_TIM5_CH4/LPTIM5_OUT/TIM15_CH2/USART2_RX/LCD_B2/ETH1_GMII_MII_COL/LCD_B5/ADC1_INP15/PVD_IN
ADC118_DAC1	PA4	V6	PA4/HDP0/TIM5_ETR/SAI4_D2/SPI1_SPI3_SPI6_NSS/I2S1_I2S3_WS/USART2_CK/SAI4_FS_A/DCMI_HSYNC/LCD_VSYNC/ADC1_ADC2_INP18/DAC_OUT1
ADC119_DAC2	PA5	U5	PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SAI4_CK1/SPI1_SPI6_SCK/I2S1_CK/SAI4_MCLK_A/LCD_R2/ADC1_ADC2_INP19_INN18/DAC_OUT
DCMI_PIXCLK	PA6	W9	PA6/TIM1_TIM8_BKIN/TIM3_CH1/SAI4_CK2/SPI1_SPI6_MISO/I2S1_SDI/TIM13_CH1/MDIOS_MDC/SAI4_SCK_A/DCMI_PIXCLK/LCD_G2/ADC1_ADC2_INP3
ETH_RX_DV	PA7	Y9	PA7/TIM1_TIM8_CH1N/TIM3_CH2/SAI4_D1/SPI1_SPI6_MOSI/I2S1_SDO/TIM14_CH1/QUADSPI_CLK/ETH1_GMII_MII_RMII_RX_DV/ETH1_RGMII_RX_CTL/SAI4_SD_A/ADC1_ADC2_INP7_INN3
SDMMC2_D4	PA8	B13	PA8/MCO1/TIM1_CH1/TIM8_BKIN2/I2C3_SCL/SPI3_MOSI/I2S3_SDO/USART1_CK/SDMMC2_CKIN/SDMMC2_D4/OTG_FS_SOF/OTG_HS_SOF/SAI4_SD_B/UART7_RX/LCD_R6
SDMMC2_D5	PA9	A11	PA9/TIM1_CH2/I2C3_SMBA/SPI2_SCK/I2S2_CK/USART1_TX/SDMMC2_CDIR/SDMMC2_D5/DCMI_D0/LCD_R5
OTG_ID	PA10	Y17	PA10/TIM1_CH3/SPI3_NSS/I2S3_WS/USART1_RX/MDIOS_MDIO/SAI4_FS_B/DCMI_D1/LCD_B1/OTG_FS_HS_ID
I2C5_SCL	PA11	Y16	PA11/TIM1_CH4/I2C6_I2C5_SCL/SPI2_NSS/I2S2_WS/UART4_RX/USART1_USART1_NSS/FDCAN1_RX/LCD_R4/OTG_FS_DM
I2C5_SDA	PA12	W16	PA12/TIM1_ETR/I2C6_SDA/I2C5_SDA/UART4_TX/USART1_RTS_DE/SAI2_FS_B/FDCAN1_TX/LCD_R5/OTG_FS_DP
	PA13	W3	PA13/DBTRGO/DBTRGI/MCO1/UART4_TX/BOOTFAILN
	PA14	R3	PA14/DBTRGO/DBTRGI/MCO2
DSI_RESET	PA15	E11	PA15/DBTRGI/TIM2_CH1_ETR/SAI4_D2/SDMMC1_CDIR/CEC/SPI1_SPI3_SPI6_NSS/I2S1_I2S3_WS/UART4_RTS_DE/SDMMC1_SDMMC2_D5/SDMMC2_CDIR/SAI4_FS_A/UART7_TX/LCD_R1
			PB0/TIM1_TIM8_CH2N/TIM3_CH3/DFSDM1_CKOUT/UART4_CTS/LCD_R3/ETH1_GMII_MII_RGMII_RXD2/MDIOS_MDIO/LCD_G1/ADC1_INP9_INN5/ADC2_INP9_INN5
ETH_RXD2	PB0	AB5	PB1/TIM1_TIM8_CH3N/TIM3_CH4/DFSDM1_DATIN1/LCD_R6/ETH1_GMII_MII_RGMII_RXD3/MDIOS_MDC/LCD_G0/ADC1_ADC2_INP5
ETH_RXD3	PB1	AA5	PB2/RTC_OUT2/SAI1_D1/DFSDM1_CKIN1/USART1_RX/I2S_CKIN/SAI1_SD_A/SPI3_MOSI/I2S3_SDO/UART4_RX/QUADSPI_CLK
UART4_RX	PB2	V13	PB3/TIM2_CH2/SAI4_CK1/SPI1_SPI3_SPI6_SCK/I2S1_I2S3_CK/SDMMC2_D2/SAI4_MCLK_A/UART7_RX
SDMMC2_D2	PB3	A12	PB4/TIM16_BKIN/TIM3_CH1/SAI4_CK2/SPI1_SPI3_SPI6_MISO/I2S1_I2S3_SDI/I2S2_WS/SDMMC2_D3/SAI4_SCK_A/UART7_TX
SDMMC2_D3	PB4	C13	PB5/ETH_CLK/TIM17_BKIN/TIM3_CH2/SAI4_D1/I2C1_I2C4_SMBA/SPI1_SPI3_SPI6_MOSI/I2S1_I2S3_SDO/FDCAN2_RX/SAI4_SD_A/ETH1_PPS_OUT/UART5_RX/DCMI_D10/LCD_G7
SPDIF_TX	PB5	AA8	PB6/TIM16_CH1N/TIM4_CH1/I2C1_I2C4_SCL/CEC/USART1_TX/FDCAN2_TX/QUADSPI_BK1_NCS/DFSDM1_DATIN5_UART5_TX/DCMI_D5
HDMI_CEC	PB6	W13	PB7/TIM17_CH1N/TIM4_CH2/I2C1_I2C4_SDA/USART1_RX/SDMMC2_D1/DFSDM1_CKIN5_FMC_NL/DCMI_VSYNC
DCMI_VSYNC	PB7	F11	PB8/HDP6/TIM16_CH1/TIM4_CH3/DFSDM1_CKIN7/SDMMC1_SDMMC2_CKIN/I2C1_I2C4_SCL/UART4_RX/FDCAN1_RX/SDMMC1_SDMMC2_D4/ETH1_GMII_MII_RGMII_TXD3/DCMI_D6/LCD_B6
DCMI_D6	PB8	AB8	PB9/HDP7/TIM17_CH1/TIM4_CH4/DFSDM1_DATIN7/I2C1_I2C4_SDA/SPI2_NSS/I2S2_WS/SDMMC1_SDMMC2_CDIR/UART4_TX/FDCAN1_TX/SDMMC1_SDMMC2_D5/DCMI_D7/LCD_B7
SDMMC1_CDIR	PB9	F12	PB10/TIM2_CH3/LPTIM2_IN1/I2C2_SCL/SPI2_SCK/I2S2_CK/DFSDM1_DATIN7/USART3_TX/QUADSPI_BK1_NCS/ETH1_GMII_MII_RX_ER/LCD_G4
	PB10	V9	PB11/TIM2_CH4/LPTIM2_ETR/I2C2_SDA/DFSDM1_CKIN7/USART3_RX/ETH1_GMII_MII_RMII_TX_EN/ETH1_RGMII_EX_CTL/DSI_DE/LCD_G5
ETH_TX_EN	PB11	Y5	PB12/TIM1_BKIN/I2C6_I2C2_SMBA/SPI2_NSS/I2S2_WS/DFSDM1_DATIN1/USART3_CK_RX/FDCAN2_RX/ETH1_GMII_MII_RGMII_RMII_TXD0/UART5_RX
USART5_RX	PB12	AA7	PB13/TIM1_CH1N/DFSDM1_CKOUT/LPTIM2_OUT/SPI2_SCK/I2S2_CK/DFSDM1_CKIN1/USART3_CTS_NSS/FDCAN2_TX/ETH1_GMII_MII_RGMII_RMII_TXD1/UART5_TX
USART5_TX	PB13	V10	PB14/TIM1_TIM8_CH2N/TIM12_CH1/USART2_TX/SPI2_MISO/I2S2_SDI/DFSDM1_DATIN2/USART3_RTS_DE/SDMMC2_D0
SDMMC2_D0	PB14	A13	PB15/RTC_REFIN/TIM1_TIM8_CH3N/TIM12_CH2/USART1_RX/SPI2_MOSI/I2S2_SDO/DFSDM1_CKIN2/SDMMC2_D1
SDMMC2_D1	PB15	B12	
			PC0/DFSDM1_CKIN0_DATIN4/LPTIM2_IN2/SAI2_FS_B/QUADSPI_BK2_NCS/LCD_R5/ADC1_ADC2_INP10
QSPI_BK2_NCS	PC0	U10	PC1/SAI1_D1/DFSDM1_DATIN0_CKIN4/SPI2_MOSI/I2S2_SDO/SAI1_SD_A/SDMMC2_CK/ETH1_MDC/MDIOS_MDC/ADC1_INP11_INN10/ADC2_INP11_INN10/TAMP_IN3/WKUP6
ETH_MDC	PC1	AB3	PC2/DFSDM1_CKIN1_CKOUT/SPI2_MISO/I2S2_SDI/ETH1_GMII_MII_RGMII_TXD2/DCMI_PIXCLK/ADC1_INP12_INN11
ETH_TXD2	PC2	Y1	PC3/DFSDM1_DATIN1/SPI2_MOSI/I2S2_SDO/ETH1_GMII_MII_TX_CLK/ADC1_INP13_INN12
	PC3	U3	PC4/DFSDM1_CKIN2/I2S1_MCK/SPDIFRX_IN2/ETH1_GMII_MII_RGMII_RMII_RXD0/ADC1_ADC2_INP4
ETH_RXD0	PC4	AB6	PC5/SAI1_D3/DFSDM1_DATIN2/SAI1_SAI4_D4/SPDIFRX_IN3/ETH1_GMII_MII_RGMII_RMII_RXD1/SAI4_D3/ADC1_INP8_INN4/ADC2_INP8_INN4
ETH_RXD1	PC5	AA6	PC6/HDP1/TIM3_TIM8_CH1/DFSDM1_CKIN3/I2S2_MCK/USART6_TX/SDMMC1_D0DIR/SDMMC1_SDMMC2_D6/DSI_TE/DCMI_D0/LCD_HSYNC
DSI_TE	PC6	E13	PC7/HDP4/TIM3_TIM8_CH2/DFSDM1_DATIN3/I2S3_MCK/USART6_RX/SDMMC1_SDMMC2_D123DIR/SDMMC1_SDMMC2_D7/DCMI_D1/LCD_G6
SDMMC1_D123DIR	PC7	D13	PC8/TIM3_TIM8_CH3/UART4_TX/USART6_CK/UART5_RTS_DE/SDMMC1_D0/DCMI_D2
SDMMC1_D0	PC8	E14	PC9/TIM3_TIM8_CH4/I2C3_SDA/I2S_CKIN/UART5_CTS/QUADSPI_BK1_IO0/SDMMC1_D1/DCMI_D3/LCD_B2
SDMMC1_D1	PC9	D14	PC10/DFSDM1_CKIN5/SPI3_SCK/I2S3_CK/USART3_RX/UART4_TX/QUADSPI_BK1_IO1/SAI4_MCLK_B/SDMMC1_D2/DCMI_D8/LCD_R2
SDMMC1_D2	PC10	F14	PC11/DFSDM1_DATIN5/SPI3_MISO/I2S3_SDI/USART3_RX/UART4_RX/QUADSPI_BK2_NCS/SAI4_SCK_B/SDMMC1_D3/DCMI_D4
SDMMC1_D3	PC11	D15	PC12/MCO2/SAI4_D3/SPI3_MOSI/I2S3_SDO/USART3_CK/UART5_TX/SAI4_SD_B/SDMMC1_CK/DCMI_D9
SDMMC1_CK	PC12	E12	PC13/RTC_OUT1_TS_LSCO/TAMP_IN1_OUT2_OUT3/WKUP3
	PC13	N2	PC14-OSC32_IN
			PC15-OSC32_OUT

STM32MP175DAA1

U3B

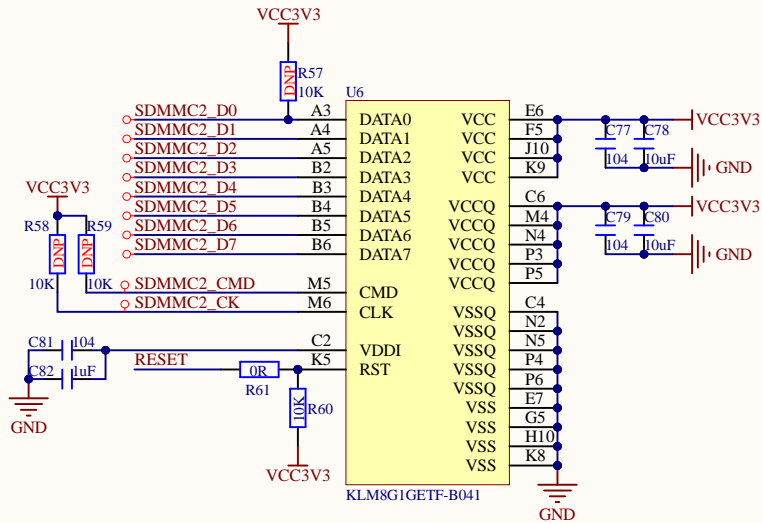
NAND_D2	PD0	C10	PD0/I2C6_I2C5_SDA/DFSDM1_CKIN6/SAI3_SCK_A/UART4_RX/FDCAN1_RX/SDMMC3_CMD/DFSDM1_DATIN7/FMC_D2/FMC_DA2
NAND_D3	PD1	B10	PD1/I2C6_I2C5_SCL/DFSDM1_DATIN6/SAI3_SD_A/UART4_TX/FDCAN1_TX/SDMMC3_D0/DFSDM1_CKIN7/FMC_D3/FMC_DA3
SDMMC1_CMD	PD2	D12	PD2/TIM3_ETR/I2C5_SMBA/UART4_RX/UART5_RX/SDMMC1_CMD/DCML_D11
SDMMC2_D7	PD3	B11	PD3/HDP5/DFSDM1_CKOUT/SP12_SCK/I2S2_CK/DFSDM1_DATIN0/UART2_CTS_NSS/SDMMC1_SDMMC2_D123DIR/SDMMC1_SDMMC2_D7/FMC_CLK/DCML_D5/LCD_G7
NAND_NOE	PD4	C9	PD4/SAI3_FS_A/USART2_RTS_DE/SDMMC3_D1/DFSDM1_CKIN0/FMC_NOE
NAND_NWE	PD5	A9	PD5/USART2_TX/SDMMC3_D2/FMC_NWE
NAND_NWAIT	PD6	L3	PD6/TIM16_CH1N/SAI1_D1/DFSDM1_CKIN4_DATIN1/SP13_MOSI/I2S3_SDO/SAI1_SD_A/USART2_RX/FMC_NWAIT/DCML_D10/LCD_B2
SDMMC3_D3	PD7	F10	PD7/DFSDM1_DATIN4_CKIN1/I2C2_SCL/USART2_CK/SPDIFRX_IN0/SDMMC3_D3/FMC_NE1
USART3_TX	PD8	M1	PD8/DFSDM1_CKIN3/SAI3_SCK_B/USART3_TX/SPDIFRX_IN1/FMC_D13/FMC_DA13/LCD_B7
USART3_RX	PD9	M2	PD9/DFSDM1_DATIN3/SAI3_SD_B/USART3_RX/FMC_D14/FMC_DA14/DCML_HSYNC/LCD_B0
uSD_DETECT	PD10	A8	PD10/RTC_REFIN/TIM16_BKIN/DFSDM1_CKOUT/I2C5_SMBA/SP13_MISO/I2S3_SDI/SAI3_FS_B/USART3_CK/FMC_D15/FMC_DA15/LCD_B3
NAND_CLE	PD11	AB9	PD11/LPTIM2_IN2/I2C4_I2C1_SMBA/USART3_CTS_NSS/QUADSP1_BK1_IO0/SAI2_SD_A/FMC_CLE/FMC_A16
NAND_ALE	PD12	W12	PD12/LPTIM1_LPTIM2_IN1/TIM4_CH1/I2C4_I2C1_SCL/USART3_RTS_DE/QUADSP1_BK1_IO1/SAI2_FS_A/FMC_ALE/FMC_A17
LCD_BL	PD13	V14	PD13/LPTIM1_OUT/TIM4_CH2/I2C4_I2C1_SDA/I2S3_MCK/QUADSP1_BK1_IO3/SAI2_SCK_A/FMC_A18
NAND_D0	PD14	M3	PD14/TIM4_CH3/SAI3_MCLK_B/UART8_CTS/FMC_D0/FMC_DA0
NAND_D1	PD15	L1	PD15/TIM4_CH4/SAI3_MCLK_A/UART8_CTS/FMC_D1/FMC_DA1/LCD_R1
SAI2_MCLKA	PE0	C5	PE0/LPTIM1_LPTIM2_ETR/TIM4_ETR/SP13_SCK/I2S3_CK/SAI4_MCLK_B/UART8_RX/SAI2_MCLK_A/FMC_NBL0/DCML_D2
DCML_RESET	PE1	D7	PE1/LPTIM1_IN2/I2S2_MCK/SAI3_SD_B/UART8_TX/FMC_NBL1/DCML_D3
ETH_TXD3	PE2	Y2	PE2/SAI1_CK1/I2C4_SCL/SP14_SCK/SAI1_MCLK_A/QUADSP1_BK1_IO2/ETH1_GMII_MII_RGMII_TXD3/FMC_A23
SDMMC2_CK	PE3	A10	PE3/TIM15_BKIN/SAI1_SD_B/SDMMC2_CK/FMC_A19
SDMMC1_CKIN	PE4	F15	PE4/SAI1_D2/DFSDM1_DATIN3/TIM15_CH1/SP14_NSS/SAI1_FS_A/SDMMC1_SDMMC2_CKIN/SDMMC1_SDMMC2_D4/FMC_A20/DCML_D4/LCD_B0
SDMMC2_D6	PE5	C12	PE5/SAI1_CK2/DFSDM1_CKIN3/TIM15_CH1/SP14_MISO/SAI1_SCK_A/SDMMC1_SDMMC2_D0DIR/SDMMC1_SDMMC2_D6/FMC_A21/DMIC_D6/LCD_G0
DCML_D7	PE6	E9	PE6/TIM1_BKIN2/SAI1_D1/TIM15_CH2/SP14_MOSI/SAI1_SD_A/SDMMC2_D0/SDMMC1_D2/SAI2_MCLK_B/FMC_A22/DCML_D7/LCD_G1
NAND_D4	PE7	W10	PE7/TIM1_TIM3_ETR/DFSDM1_DATIN2/UART7_RX/QUADSP1_BK2_IO0/FMC_D4/FMC_DA4
NAND_D5	PE8	Y12	PE8/TIM1_CH1N/DFSDM1_CKIN2/UART7_TX/QUADSP1_BK2_IO1/FMC_D5/FMC_DA5
NAND_D6	PE9	W11	PE9/TIM1_CH1/DFSDM1_CKOUT/UART7_RTS_DE/QUADSP1_BK2_IO2/FMC_D6/FMC_DA6
NAND_D7	PE10	W14	PE10/TIM1_CH2N/DFSDM1_DATIN4/UART7_CTS/QUADSP1_BK2_IO3/FMC_D7/FMC_DA7
DCML_PWDN	PE11	D5	PE11/TIM1_CH2/DFSDM1_CKIN4/SP14_NSS/USART6_CK/SAI2_SD_B/FMC_D8/FMC_DA8/DCML_D4/LCD_G3
SAI2_SCKB	PE12	E4	PE12/TIM1_CH3N/DFSDM1_DATIN5/SP14_SCK/SDMMC1_D0DIR/SAI2_SCK_B/FMC_D9/FMC_DA9/LCD_B4
SAI2_FSB	PE13	A4	PE13/HDP2/TIM1_CH3/DFSDM1_CKIN5/SP14_MISO/SAI2_FS_B/FMC_D10/FMC_DA10/DCML_D6/LCD_DE
SAI2_MCLKB	PE14	B4	PE14/TIM1_CH4/SP14_MOSI/UART8_RTS_DE/SAI2_MCLK_B/SDMMC1_D123DIR/FMC_D11/FMC_DA11/LCD_G0/LCD_CLK
T_MISO	PE15	C4	PE15/HDP3/TIM1_BKIN/TIM15_BKIN/USART2_CTS_NSS/UART8_CTS/FMC_NCE2/FMC_D12/FMC_DA12/LCD_R7
SDMMC3_D0	PF0	E10	PF0/I2C2_SDA/SDMMC3_D0_CKIN/FMC_A0
SDMMC3_CMD	PF1	B9	PF1/I2C2_SCL/SDMMC3_CMD_CDIF/FMC_A1
SDMMC1_D0DIR	PF2	F13	PF2/I2C2_SMBA/SDMMC1_SDMMC2_SDMMC3_D0DIR/FMC_A2
	PF3	V3	PF3/ETH1_GMII_MII_TX_ER/FMC_A3
SDMMC3_D1	PF4	F9	PF4/USART2_RX/SDMMC3_D1/SDMMC3_D123DIR/FMC_A4
SDMMC3_D2	PF5	D9	PF5/USART2_TX/SDMMC3_D2/FMC_A5
QSPL_BK1_IO3	PF6	AA11	PF6/TIM16_CH1/SP15_NSS/SAI1_SD_B/UART7_RX/QUADSP1_BK1_IO3/SAI4_SCK_B
QSPL_BK1_IO2	PF7	AA10	PF7/TIM17_CH1/SP15_SCK/SAI1_MCLK_B/UART7_TX/QUADSP1_BK1_IO2
QSPL_BK1_IO0	PF8	AB10	PF8/TIM16_CH1N/SP15_MISO/SAI1_SCK_B/UART7_RTS_DE/TIM13_CH1/QUADSP1_BK1_IO0
QSPL_BK1_IO1	PF9	AB11	PF9/TIM17_CH1N/SP15_MOSI/SAI1_FS_B/UART7_CTS/TIM14_CH1/QUADSP1_BK1_IO1
QSPL_CLK	PF10	V12	PF10/TIM16_BKIN/SAI1_D3/SAI1_SAI4_D4/QUADSP1_CLK/SAI4_D3/DCML_D11/LCD_DE
SAI2_SDB	PF11	W8	PF11/SP15_MOSI/SAI2_SD_B/DCML_D12/LCD_G5/ADC1_INP2
	PF12	V8	PF12/ETH1_GMII_RXD4/FMC_A6/ADC1_INP6_INN2
	PF13	W7	PF13/DFSDM1_DATIN6_DATIN3/I2C1_I2C4_SMBA/ETH1_GMII_RXD5/FMC_A7/ADC2_INP2
	PF14	V7	PF14/DFSDM1_CKIN6/I2C1_I2C4_SCL/ETH1_GMII_RXD6/FMC_A8/ADC2_INP6_INN2
	PF15	W6	PF15/I2C1_I2C4_SDA/ETH1_GMII_RXD7/FMC_A9

STM32MP175DAA1

Title: STM32MP157_DEF.SchDoc		
Project: ATK-CLMP157B_V1.4.PrjPcb		
Size: A4	Author: lycreturn@ALIENTEK	
Date: 2021/6/9	Version: V1.0	Sheet: 3 of 10



EMMC



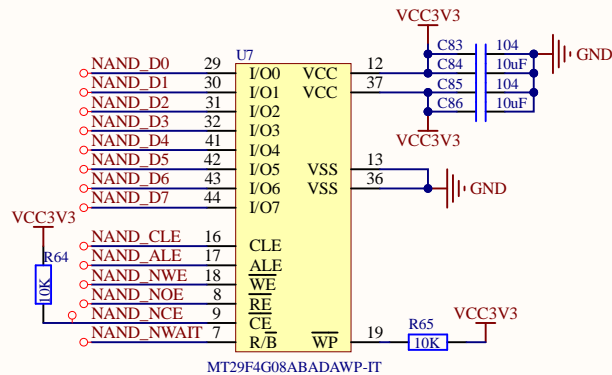
STM32MP157_JKZ

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LCD_R1	PJ0	J2
LCD_R2	PJ1	L6
LCD_R3	PJ2	K4
LCD_R4	PJ3	J1
LCD_R5	PJ4	K2
LCD_R6	PJ5	K1
LCD_R7	PJ6	L5
LCD_G0	PJ7	L4
LCD_G1	PJ8	H6
LCD_G2	PJ9	L2
LCD_G3	PJ10	J3
LCD_G4	PJ11	K6
LCD_B0	PJ12	B8
LCD_B1	PJ13	A7
LCD_B2	PJ14	B7
LCD_B3	PJ15	C7
LCD_G5	PK0	D8
LCD_G6	PK1	E7
LCD_G7	PK2	E8
LCD_B4	PK3	B6
LCD_B5	PK4	A6
LCD_B6	PK5	C6
LCD_B7	PK6	A5
LCD_DE	PK7	B5
SPI1_SCK	PZ0	G2
SPI1_MISO	PZ1	H5
SPI1_MOSI	PZ2	K5
SPI1_NSS	PZ3	F4
I2C4_SCL	PZ4	G1
I2C4_SDA	PZ5	H4
I2C6_SCL	PZ6	G3
I2C6_SDA	PZ7	H3

PJ0/LCD_R7/LCD_R1
PJ1/LCD_R2
PJ2/DSI_TE/LCD_R3
PJ3/LCD_R4
PJ4/LCD_R5
PJ5/HDP2/LCD_R6
PJ6/HDP3/TIM8_CH2/LCD_R7
PJ7/TIM8_CH2N/LCD_G0
PJ8/TIM1_CH3N/TIM8_CH1/UART8_TX/LCD_G1
PJ9/TIM1_CH3/TIM8_CH1N/UART8_RX/LCD_G2
PJ10/TIM1_CH2N/TIM8_CH2/SPI5_MOSI/LCD_G3
PJ11/TIM1_CH2/TIM8_CH2N/SPI5_MISO/LCD_G4
PJ12/LCD_G3/LCD_B0
PJ13/LCD_G4/LCD_B1
PJ14/LCD_B2
PJ15/LCD_B3
PK0/TIM1_CH1N/TIM8_CH3/SPI5_SCK/LCD_G5
PK1/TIM1_CH1/HDP4/TIM8_CH3N/SPI5_NSS/LCD_G6
PK2/TIM1_BKIN/HDP5/TIM8_BKIN/LCD_G7
PK3/LCD_B4
PK4/LCD_B5
PK5/HDP6/LCD_B6
PK6/HDP7/LCD_B7
PK7/LCD_DE
PZ0/I2C6_I2C2_SCL/SPI1_SCK/I2S1_CK/USART1_CK/SPI6_SCK
PZ1/I2C6_I2C2_I2C5_I2C4_SDA/SPI1_SPI6_MISO/I2S1_SDI/USART1_RX
PZ2/I2C6_I2C2_SCL/I2C5_I2C4_SMBA/SPI1_SPI6_MOSI/I2S1_SDO/USART1_TX
PZ3/I2C6_I2C2_I2C5_I2C4_SDA/SPI1_SPI6_NSS/I2S1_WS/USART1_CTS_NSS
PZ4/I2C6_I2C2_I2C5_I2C4_SCL
PZ5/I2C6_I2C2_I2C5_I2C4_SDA/USART1_RTS_DE
PZ6/I2C6_I2C2_SCL/USART1_CK/I2S1_MCK/I2C4_SMBA/USART1_RX
PZ7/I2C6_I2C2_SDA/USART1_TX

STM32MP175DAA1

NAND



Title: STM32MP157_JKZ&FLASH.SchDoc		
Project: ATK-CLMP157B_V1.4.PrjPcb		
Size: A4	Author: lycreturn@ALIENTEK	
Date: 2021/6/9	Version: V1.0	Sheet: 5 of 10



A

A

B

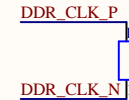
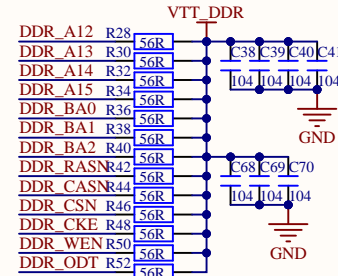
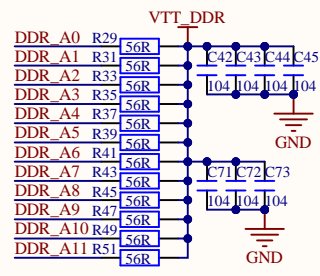
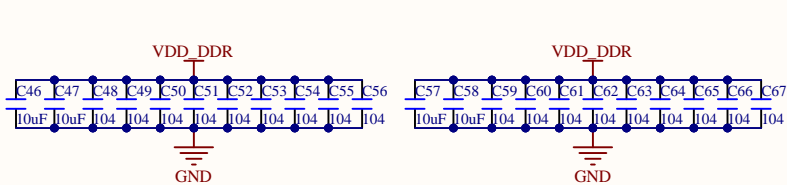
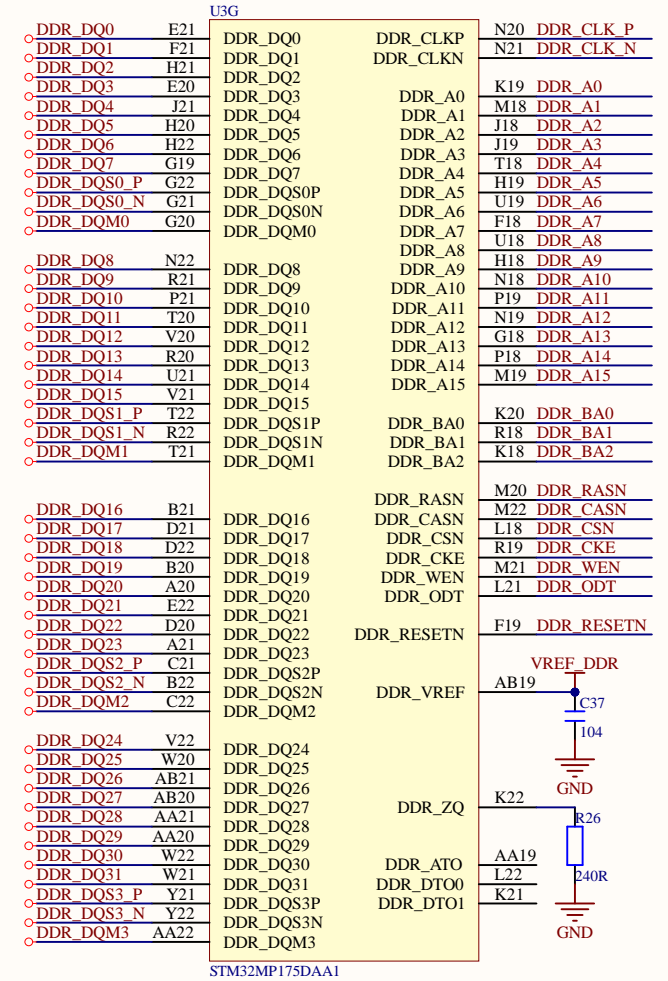
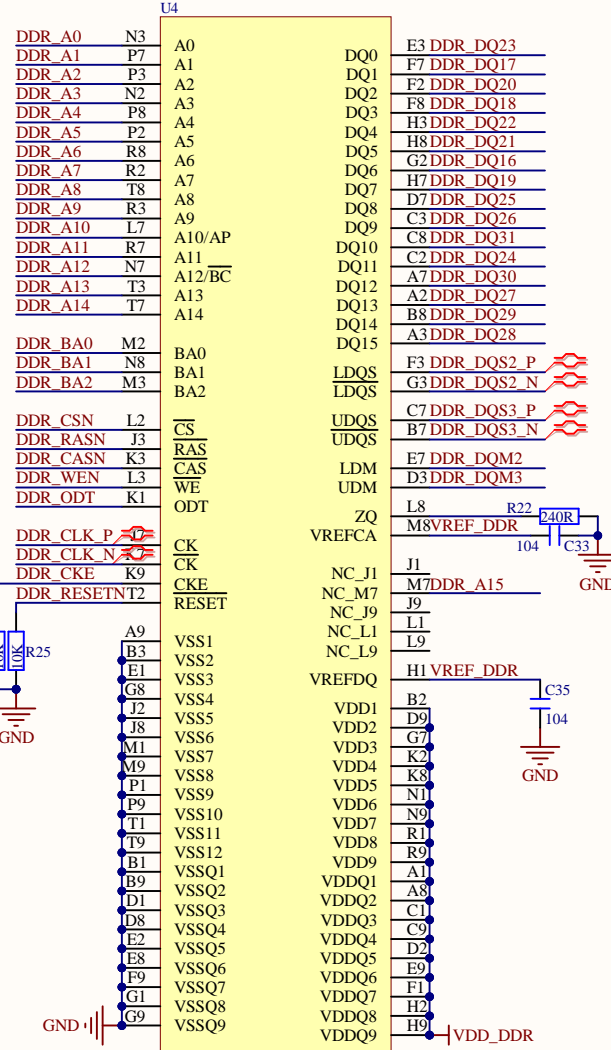
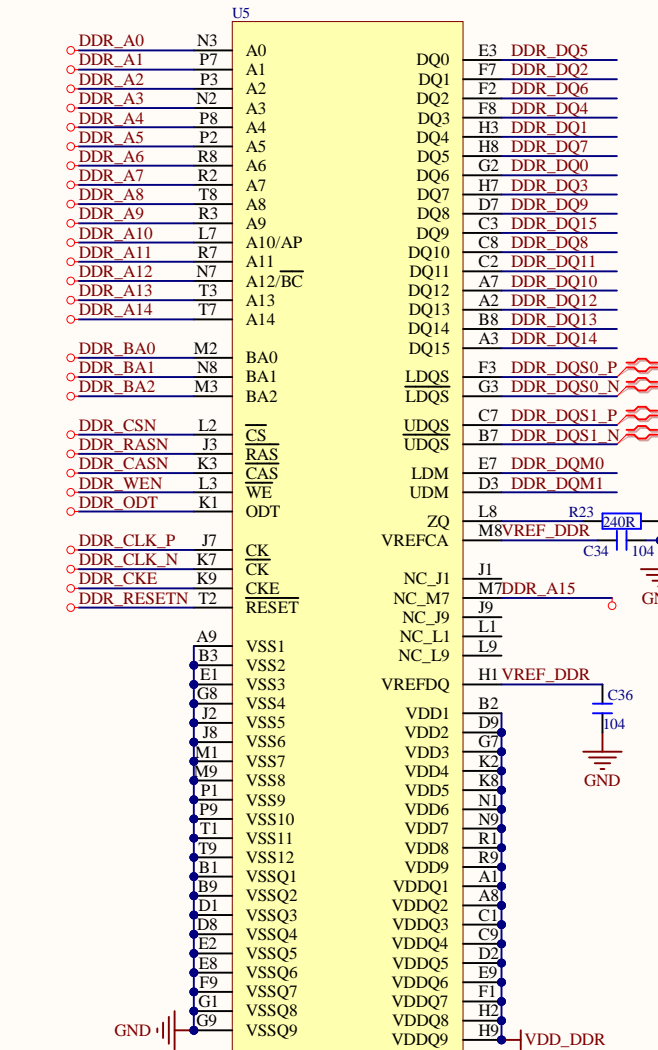
B

C

C

D

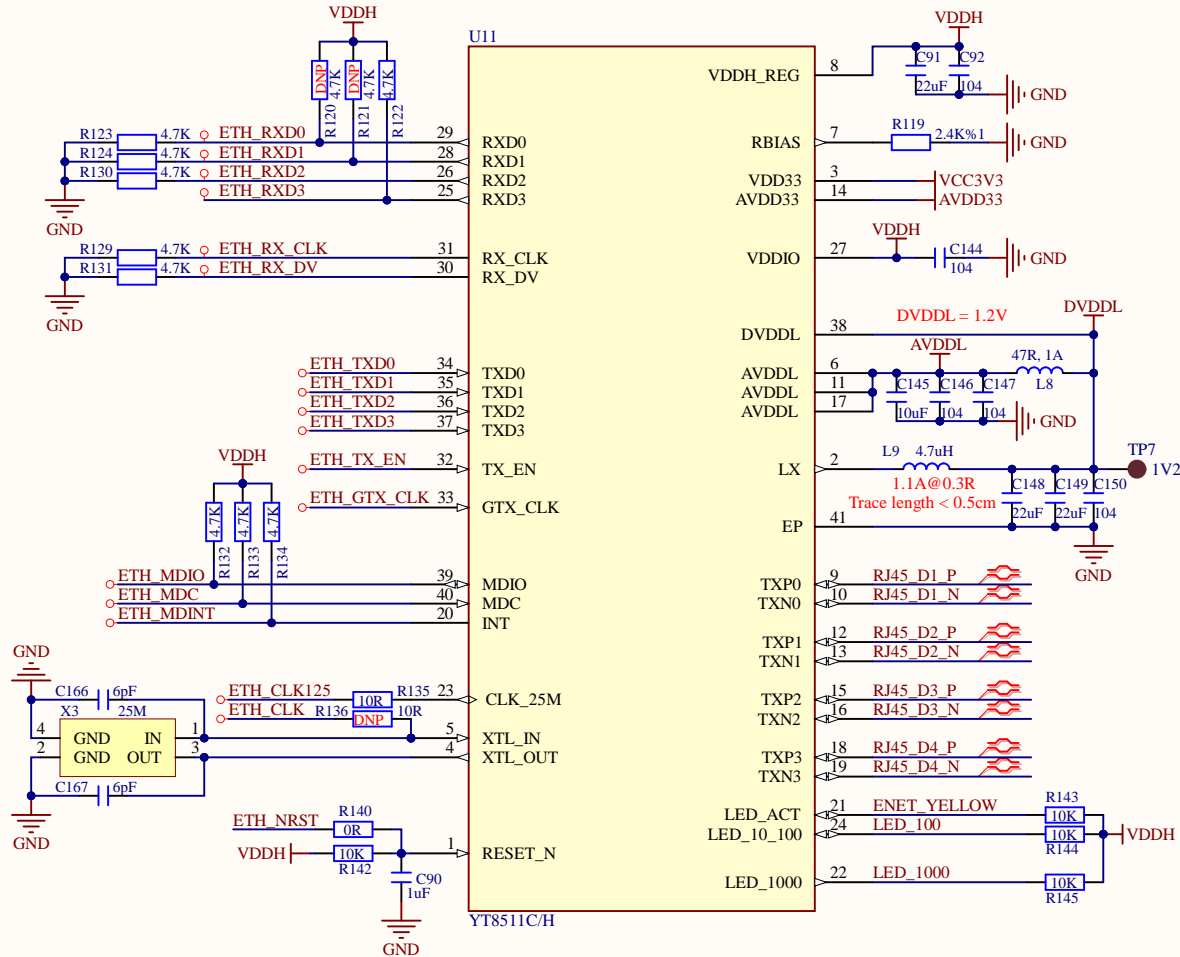
D



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Project: ATK-CLMP157B_V1.4.PjtPcb		
Size: A4	Author: lycreturn@ALIENTEK	
Date: 2021/6/9	Version: V1.0	Sheet: 6 of 10

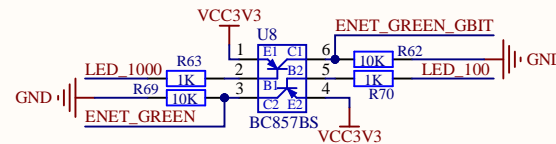


10/100/1000M NET



Power on Strapping Pins

LED_ACT(IPU)	PHY_ADDR(2)	PHY Addr=00000+PHYA(2:0)	PHY address 00100b
RXD1(IPD)	PHY_ADDR(1)		
RXD0(IPD)	PHY_ADDR(0)		
RXD3(IPD)	MODE_SEL(1)	01=force low power mode;	Normal mode
LED_1000(IPU)	MODE_SEL(0)	11=normal mode;	
RXD2(IPD)	PLLON	0=PLL OFF in hibernation; 1=PLL ON in hibernation;	PLL OFF in hibernation
RX_DV(IPD)	3.3V/2.5V_SEL	0=RGMII IO 3.3V; 1=RGMII IO 2.5V;	3.3V RGMII
LED_10_100(IPU)	RXC_delay_en	0=RXC delay disable; 1=RXC delay enable;	RXC delay enable
RX_CLK(IPD)	CLK_25M_en	0=CLK_25M enable; 1=CLK_25M disable;	CLK_25M output enable

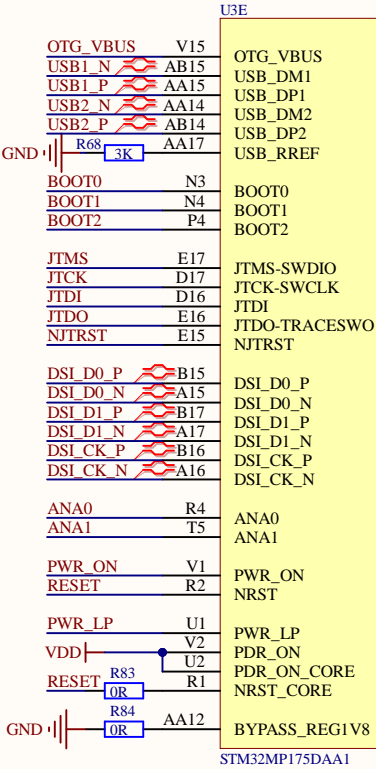


YT8511C的100M和1000M指示灯低电平有效，而底板是高电平有效，故在此对这2个信号进行反向。

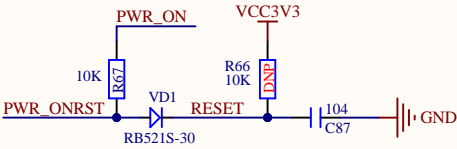
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Project: ATK-CLMP157B_V1.4.PrjPcb		
Size: A4	Author: lycreturn@ALIENTEK	
Date: 2021/6/9	Version: V1.0	Sheet: 7 of 10



USB&DSI&JTAG



RESET



LED



ESD&FKT

ESD FKT
ESD FKT
ESD FKT

Title: STM32MP157_OTHER.SchDoc		
Project: ATK-CLMP157B_V1.4.PrjPcb		
Size: A4	Author: lycreturn@ALIENTEK	
Date: 2021/6/9	Version: V1.0	Sheet: 8 of 10



