DEVICE NAME	SPEC NO	REV
MMFA60R360QTH/MMFA60R360QTP Marking Instruction	MBKB-D	0

- 1. PURPOSE (목적)
 - 1.1 To provide Marking criteria & information
- 2. SCOPE (적용 범위)
 - 2.1 MMFA60R360QTH / MMFA60R360QTP (TO220FA, 3L)
- 3. DEFINITIONS (정의)

3.1 CHARACTER TYPE : ARIAL3.2 MARKING METHOD : LASER3.3 MARKING LAYOUT & AREA



* Marking character alignment : Left alignment

* Marking position alignment : Package Center

* 1st Line: MagnaChip Logo

* 2nd Line : A60R360Q

* 3rd Line:

-Y : Year Code (1 digit)-W : Week Code (1 digit)

- Y: Year Code (1 digit)

09	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Α	В	С	D	E	F	G	Н	J	K	L	N	0	P	R	S	T	U	V	Χ	γ	Z

- W : Week Code (1 digit)

1	2	3	ţ	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	
\Box	1	2	?	3		4	ļ	5	,	6	;	7	•	8		9		ļ	١	ı	3	0	:	[)	
27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	£3	11	45	46	47	48	49	50	51	52	53
	E	ı	•	G	;	ŀ	1		1	H	(L	-	N	1	N		()	F	•	F	t	9	5	T

DEVICE NAME	SPEC NO	REV
MMFA60R360QTH/MMFA60R360QTP Marking Instruction	MBKB-D	0

Mask Type Marking ==>LLLLL : Lot Traceability Code (5 digits of wafer lot number)

Case	Wafer lot No (Example)	Lot traceability code (5 digit)	Applicable PKG	Description	Actual marking (Example of Case1)
1	FK12345□□	12345	TO220,TO220F,TO263, TO262,TO247,TO3P,	Magnachip	Magnachip
2	FK12345BB	345BB	TO220FS,TO220FT,	Device name	A60R360Q
3	FK12345L1	345L1	DFN8x8 _ discrete : Apply Mask Type Marking	YWLLLLXZ	CA12345GS

Where,

1) "□□" : Blank digit 2) "BB", "L1" : Wafer split code

Pen Type Marking ==>LLLLL : Lot Traceability Code

LLLLL is applied to 5th,6th,7th (3 digits) of wafer lot number & Ass'y No 2 digits

		Lot traceabi	lity code (5 digit)					
Case	Wafer lot No (Example)	5 th ,6 th ,7 th of wafer lot No (3 digit)	Ass'y lot tracking code (2 digit by subcon)	Applicable PKG	Description	Actual marking (Example of Case1)		
1	FK12345	345	0 A	TO220,TO220F,TO263, TO262,TO247,TO3P,	Magnachip	Magnachip		
2	FK12345BB	5BB	1B	TO220FS,TO220FT, M2PAK, TOLL	Device name	A60R360Q		
3	FK12345L1 5L1 1Z		DFN8x8 _ discrete : Apply Pen Type Marking	YWLLLLXZ	CA3450AGS			

Where,

1) "□□" : Blank digit 2) "BB", "L1" : Wafer split code

3) "0A", "1B", "1Z": Ass'y lot tracking code is assigned by subcontractor (Do not use the letter & figure to confuse and duplicate when subcontractor assigns the ass'y lot tracking code.)

** Appendix #1. Ass'y lot tracking code (for example)

Ass'y Lot	LL	Ass'y Lot	LL	Ass'y Lot	LL
1st lot	00	35th lot	10	69th lot	20
2nd lot	01	36th lot	11		
3rd lot	02	37th lot	12		
~	~	~	~	_	_
10th lot	09	44th lot	19		
11th lot	0A	45th lot	1A		
12th lot	0B	46th lot	1B		
~	~	~	~	1295th lot	ZY
34th lot	0Z	68th lot	1Z	1296th lot	ZZ

-X: Environmental Code (1 digit)

Marking code (1 digit)	Device name	Description
G	MMFA60R360QTH	Halogen Free and ROHS compliance
R	MMFA60R360QTP	ROHS compliance

-Z: Subcon Code (1 digit)

Refer to Magnachip spec "MBKB-D5380_PSD Subcontractor Code for Marking"

4. REVISION HISTORY

Rev. No	ITEM	Date
0	Initial Release	2022-Jun-01