5	Addressing leaks through networking	21
	Scenario 0: An air-gapped system (no network)	22
	Scenario 1: A closed network of trusted peers	23
	Scenario 2: Tor-ified or VPN-ed open Internet	24
	Scenario 3: Unconstrained Internet access?	25
6	(Un)trusting firmware and the host OS	27
	Firmware considerations	27
	Host OS considerations	28
	Reconsidering BIOS and ME (un)trusting?	28
7	Addressing Evil Maid Attacks	30
8	Select implementation considerations	32
	SPI Flash emulation challenges	32
	Host OS implementation considerations	33
	User partition encryption considerations	34
	Temper-resistance considerations	34
9	Alternative solutions?	36
	ARM-based platforms?	36
	FPGA-based, true open source processors and platforms?	37
10	Summary	38
Cr	Credits	
Co	Contacting the author	
References		41