

JEFF LIEVENSE

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RESEARCH

Signal Processing, Machine Learning, Error Detection and Correction.

My focus is the design and analysis of fast algorithms for dimensionality reduction and signal recovery from incomplete measurements. Ultra-low complexity, coding-theoretic algorithms for compressive sensing based on sparse-graph codes are of particular interest.

EDUCATION

Rice University, Houston, TX *09/2014 - 05/2020*
PhD candidate, Electrical and Computer Engineering *(expected)*
Advisor: Dr. Richard G. Baraniuk

University of California, Berkeley, CA *09/2010 - 05/2014*
BS, Electrical and Computer Engineering
GPA: 3.6 (major) / 3.3 (overall)

Coursework in Sparse Structure Recovery, Statistical Signal Processing, Coding Theory, Statistical Learning, Data Mining, Probability, Stochastic Processes, Algorithms, Linear Algebra, Real Analysis, Discrete Mathematics.

EMPLOYMENT

DSP Group, Rice University, Houston, TX *09/2014 - present*
Research with and course assistant for Dr. Richard G. Baraniuk.

SWARM Lab, University of California, Berkeley, CA *01/2013 - 01/2014*
Research assistant with Dr. Mekhail Anwar, Dr. Bernhard Boser.
Designed test setup for novel high resolution medical imaging device.

Texas Instruments Silicon Valley Labs, Santa Clara, CA *05/2012 - 09/2012*
Test engineering intern with Signal and Data Path Solutions team.
Designed and tested devices used to characterize PCB vias.

Amyris Inc., Emeryville, CA *05/2011 - 09/2011*
Research intern with Dr. Jeremy Agresti in Emerging Technologies.
Designed and fabricated microfluidic devices for picoscreening.

TEACHING

ELEC 301: Introduction to Signals and Systems *Rice University*
Teaching assistant for Dr. Richard G. Baraniuk. *Fall 2014*

EE 20N: Structure & Interpretation of Signals and Systems *UC Berkeley*
Lab assistant for Dr. Babak Ayazifar. *Fall 2012 - Spring 2014*