

# Timetable

Week	Date	Details
3	4th - 10th March 2025	Understand the JTAGG interface, establish a timeline
4	11th - 17th March 2025	Construct a simple IP Core and make it work on the board (Light a led depending on what is inputed)
5	18th - 24th March 2025	Construct a simple IP core and make it work on the board (Light a led depending on what is inputed)
6	25th - 31st March 2025	Make possible to write to the SRAM using the JTAG interface (how to link the IP core with the rest of the Architecture)
7	1st - 7th April 2025	Make possible to write to the SRAM using the JTAG interface (Preapartion intermediary presentation)
8	8th - 14th April 2025	Make possible to write to the SRAM using the JTAG interface (Intermediary presentation the 10th April)
9	15th - 21st April 2025	Software to write in the SRAM + Debug Module
Holiday	22nd - 28th April 2025	Holydays
10	29th April 2025 - 5th May 2025	Debug Module
11	6th - 12th May 2025	Debug Module
12	13th - 19th May 2025	Debug Module
13	20th - 26th May 2025	Debug Module
14	27th - 30th May 2025	Last week

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