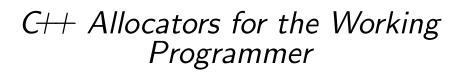


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This is John's dedication to Josh for being so great and writing this book so well.

JL

This is Josh's dedication to his wife, child, and mother-in-law for being all supportive and wonderful. And to steak. Steak is great.

 ${\rm JMB}$



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Foreword

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Author Photo here John Lakos, author of Large-Scale C++ Software Design [Pearson, 1996] and Large-Scale C++ — Volume I: Process and Architecture [Pearson, 2019], serves at Bloomberg in New York City as a senior architect and mentor for C++ software development worldwide. He is also an active voting member of the C++ Standards Committee's Evolution Working Group. From 1997 to 2001, Dr. Lakos directed the design and development of infrastructure libraries for proprietary analytic financial applications at Bear Stearns. From 1983 to 1997, Dr. Lakos was employed at Mentor Graphics, where he developed large frameworks and advanced ICCAD applications for which he holds multiple software patents. His academic credentials include a Ph.D.

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Application Developers

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- 1.1.1 Defining a PMR Allocator-Aware Type
- 1.1.2 std::pmr Collections
- 1.2 Using Allocator-Aware Types
- 1.2.1 How to use a Custom Memory Resource
- 1.2.2 How to Choose an Allocator
- 1.2.3 Testing Code that Allocates
- 1.3 Case Study 1: Unique Value Counting





Library Writers

- 2.1 Writing Allocator-Aware Types
- 2.1.1 Aggregating Other Allocator-Aware Types
- 2.1.2 Doing Allocation
- 2.1.3 Testing Allocator-Aware Types
- 2.2 Case Study 3: PMR Optional and Variant





Writing Allocators

- 3.1 Implementation
- 3.1.1 Learning from Global Alocators
- 3.1.2 Thread-Unsafe Allocators
- 3.1.3 Reuse Free Allocators
- 3.1.4 Wrapping Other Allocators for Utility
- 3.2 Benchmarking Allocators
- 3.3 Case Study 4: A Buffered Sequential Allocator





Advanced

4.1 Modern Hardware

4.2 Effective Benchmarking

Here we would be discussing the approach we have to benchmarking.

4.3 Optimizing Large Allocator-Aware Systems

4.4 Designing Effective Allocator-Aware Architectures





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Appendix A

Other Libraries

A.1 BDE

A.2 Thrust







Appendix B

Future Developments

- **B.1** More PMR Types
- **B.2** Automating Allocator Suppoer