RESUME

OUTPUT -



CODE -

```
፱ timetable 1.html ×
C: > Users > ASUS > Downloads > ■ timetable 1.html > ♦ html > ♦ head > ♦ head
 <html>
 <head>
  <title>table</title>
  <head>
  <body bgcolor="#FEF8DD">
   THEORY 
    Start
    8:00
    9:00
    10:00
    11:00
    12:00
    -
    Lunch
    14:00
    15:00
    16:00
    17:00
    18:00
    18:51
    19:01
     End
     8:50
```

```
C: > Users > ASUS > Downloads > 🧧 timetable 1.html > �� html > �� head > �� head
     9:50
     10:50
     11:50
     12:50
34
     -
     Lunch
     14:50
     15:50
     16:50
     17:50
     18:50
     19:00
     19:50
44
    LAB
     Start
     8:00
     8:51
     9:51
     10:41
     11:40
     12:31
     Lunch
54
     14:00
     14:51
     15:51
     16:41
     17:40

18:31

፱ timetable 1.html ×
C: > Users > ASUS > Downloads > ■ timetable 1.html > � html > � head > � head
    End
    8:50
    9:40
     10:40
    11:30
    12:30
    13:20
    Lunch
     14:500
    15:40
    16:40
    17:30
    18:30

19:20

    MON
    THEORY
    A1-BCSE204L-TH-PRP306-ALL
    F1
    D1-BCSE205L-TH-SJT311-ALL
    TB1-BCSE304L-TH-SJT303-ALL
    TG1-BMAT202L-TH-PRP214-ALL
    Lunch
    A2
```

```
■ timetable 1.html ×
      D2
TB2
      TG2
      V3
       LAB
       L1
       L3
       L4
       L5
       L6
       Lunch
       L32
       L33
       L34
       <\!\!td\ align="center"\ bgcolor="CCFF33">L35-BCSE203E-ELA-SJT122-ALL</\!/td>
       L36-BCSE203E-ELA-SJT122-ALL
       -
      TUE
      THEORY
      B1-BCSE304L-TH-SJT303-ALL
      G1-BMAT202L-TH-PRP214-ALL
      E1-BSTS102P-SS-PRP414-ALL
      TC1-BMAT102L-TH-SJT101-ALL
      TAA1-BCSE203E-ETH-PRP319-ALL
■ timetable 1.html ×
C: > Users > ASUS > Downloads > ■ timetable 1.html > ♦ html > ♦ head > ♦ head
```

```
-
Lunch
B2
G2
E2
TC2
TAA2
-
V4
LAB
L7
L8
L9
L10
L11
L12
Lunch
L38
L39
L40
L41
L42
-
WED
THEORY
C1-BMAT102L-TH-SJT101-ALL
```

```
፱ timetable 1.html ×
C: > Users > ASUS > Downloads > ♥ timetable 1.html > ♦ html > ♦ head > ♦ head > ♦ body > ♦ table > ♦ td
    $$ A1-BCSE204L-TH-PRP306-ALL$$ F1$$
    V1

V2

    -
    Lunch
    C2
    A2
    TD2
    TBB2
    -
    V5
     LAB
     L14
     L15
     L16
     L17
     L18
     Lunch
     L43-BMAT202P-LO-PRP119-ALL
     L44-BMAT202P-L0-PRP119-ALL
     L40
     L41
     L42
     -
C: > Users > ASUS > Downloads > ﻕ timetable 1.html > ❷ html > ❷ head > ❷ head > ❷ body > ❷ table > ❷ td
    THU
    THEOREY
    D1-BCSE205L-TH-SJT311-ALL
    B1-BCSE304L-TH-SJT303-ALL
    G1-BMAT202L-TH-PRP214-ALL
    TE1-BSTS102P-SS-PRP414-ALL
    -
    Lunch
    D2
    B2
    G2
    TE2
    TCC2
    V6
     LAB
     L19
     L20
     L21
     L22
     L23
     L24
     Lunch
     L49
     L50
     L51
```

```
፱ timetable 1.html ×
C: > Users > ASUS > Downloads > ♥ timetable 1.html > ♦ html > ♦ head > ♦ head > ♦ body > ♦ table > ♦ td
     L52
     L54
     -
    FRI
    THEOREY
    E1-BSTS102P-SS-PRP414-ALL
    C1-BMAT102L-TH-SJT101-ALL
    TA1-BCSE204L-TH-PRP306-ALL
    TF1
    TD1-BCSE205L-TH-SJT311-ALL
    -
    Lunch
    E2
    C2
    TA2
    TF2
    TDD2
    -
    V7
    LAB
     L25
     L26
     L27
     L28
     L29
     L30
C: > Users > ASUS > Downloads > ♥ timetable 1.html > ♦ html > ♦ head > ♦ head > ♦ body > ♦ table > ♦ td
     Lunch
     L55-BCSE204P-L0-PRP118-ALL
     L56-BCSE204P-L0-PRP118-ALL
     L57-BCSE203E-ELA-SJT122-ALL
     L58-BCSE203E-ELA-SJT122-ALL
     L59
     L60
     -
    SAT
    THEORY
    V8
    X12
    Y11
    Y12
    Lunch
    X21
    Z21
    Y21
    W21
    W22
    -
    V9
     LAB
     L72
```

```
C: > Users > ASUS > Downloads > ■ timetable 1.html > � html > � head > � head > � body > � table > � td
    L73
    L74
    L75
    L76
    Lunch
    L77
    L78
    L79
    L80
    L81
    L82
    -
   SUN
   THEORY
   V10
   Y11
   Y12
   X11
   X12
   -
   Lunch
   Y21
   Z21
   X21
   W21
   W22 
   -
   V11
timetable 1.html ×
C: > Users > ASUS > Downloads > ♥ timetable 1.html > ♦ html > ♦ head > ♦ head > ♦ body > ♦ table > ♦ td
    LAB
    L83
    L84
    L85
    L86
    L87
    L88
    Lunch
    L89
    L90
    L91
    L92
    L93
    L94
    -
```