Introduction to Parallel Computing, Wing MPI, OpenMP, K CUDA (CME213)

(GPU) & Goraphics perocessing Unit)

> Specialized electronic circuit designed to papidly manipulde and after mamony to accelerate the Corcation of images in a frame buffer intended for Output to a display device.

(TPU) L'Tenson processing Unit)

> AI acceleration, application-specific IC developed by Guegle specifically from neural naturals.

(MPI) [Message passing Interface]

Open MP) [Open Multi-Brocessing)

(CUDA) I Compute Unified device Architecture)

> 9t is a parallel computing platform & API model created by Midia.

L> Out allows software developers to use a cupA-enabled gup for general purpose processing.

(Gardon Moure) S1965)

The number of transistant on a chip shall double every 18-24 months.

Shand memory
Parellel Computing

Scomputer with
many cores.

Distributed

Passalld Competing

Competers many

Computers over a

fost notwork.

Deckage can at increases

- Rower consumption increases

- Meet generated increases.

make comparable to to processing speed.

Multicard

One/few but

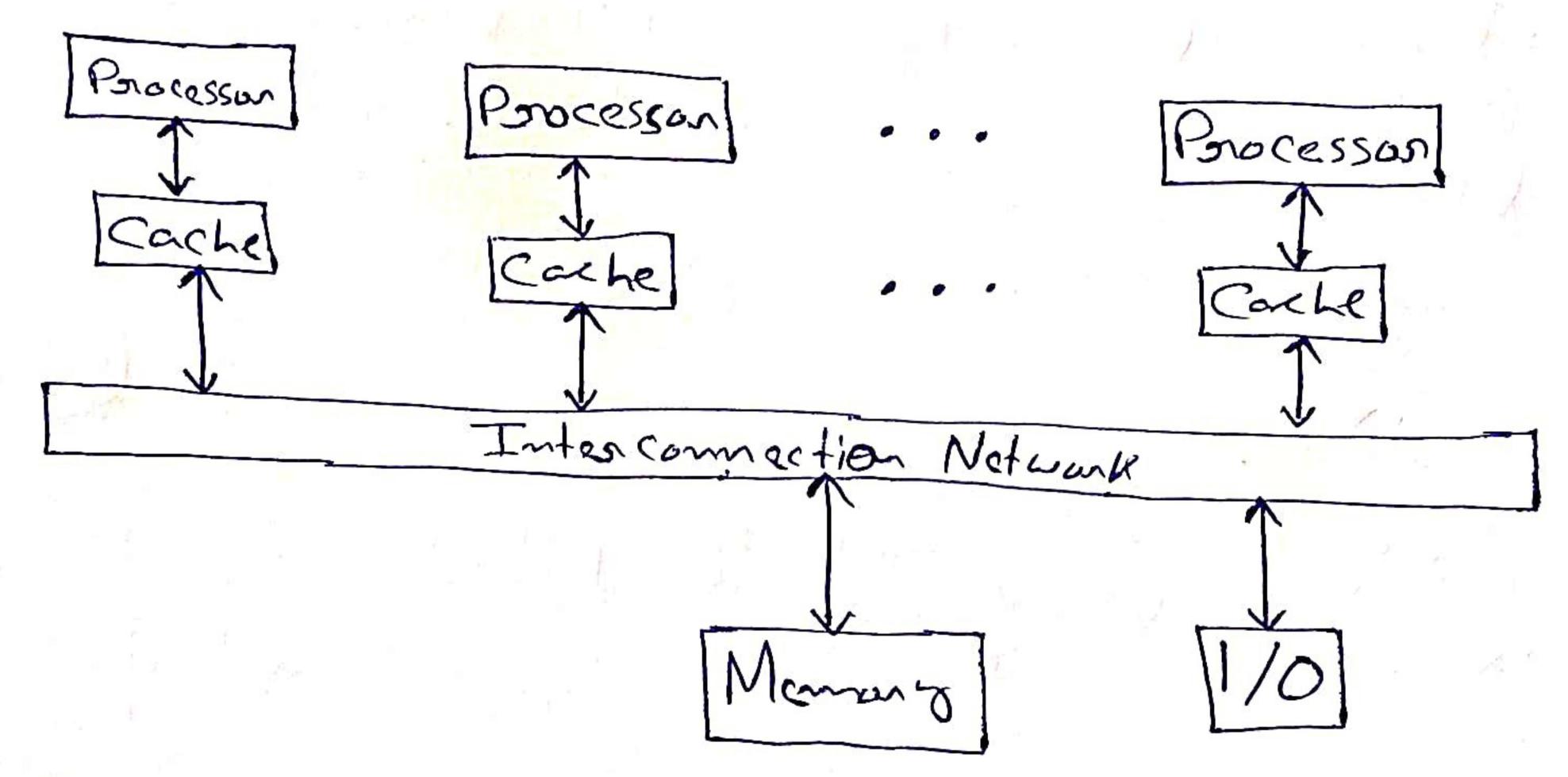
Fast

Any, but slower Cord

Capus

* Shared Memory Brucessan

- -> A number of processors on cones
- > A shared physical momenty (global moments)
- A interconnection network to Connect the Porocessoons with the momony.



-> Capu is great for:

-> Dense linear algebra

-> Finite - difference

Nemd Network.