

4.1) Introduction

=>Microcontrollers are widely used for control
applications in Vehicles, toys, appliances
k +elecommunication device.

Ly Micono controllers are also colled embedded controllers.

4.27 Numbering System

* Decemal System

> bose 10

In Numbers are exepsesanted as a combinder of any of the base to digits that are word.

763 = (7×102)+(6×101) + (3×18)

-> Convenient for Scientific metho but not Convenient in Computer System.

* Binay Sytum

to expresent number.

Computer.

 $|0|10 \Rightarrow (1 \times 2^{4}) + (0 \times 2^{3}) + (1 \times 2^{2}) + (1 \times 2^{1}) + (0 \times 2^{0})$ $= 22 \quad \text{Sim downd}$

Reportsent the decind number 55 in binery from.

2	59	
2	29	1
2	14	1
2	7	0
2	3	1
	L)

=> The Smellest unit of storage in a Computer System is the bit.

=> byte -> group of 8 bit

od.

=> when dealing with binary numbers:

-> The orightmost bit of the bines number is colled the least significant bit (LSB).

La soience it enepresents the smallest Power of 2.

The Leftmost bit is colled most significant bit (MSB)

Ly Sience it oneponesents the largest Power of 2. nitit

MSB LSB

* Hexadecimal System

=> When evaluating the Contents of a large memory location it is more convenient if we can write the values of each digit y-bit into one digit.

-> Hexadecimal on bose-16 system uses sixteen digit to exeponesent a number. -> The first 10 digits 0-9 are some as decimed to 12 13 14 15 C D E F ABCDE => A hexadecimed number is indicated by a suffix h on a pacfix ox. eg ox10 ⇒(16),0 124 => (18),0 (0110 100101010011) > 6053h 6~103+0~162+5~16+3×160 (26063), * Negative number onepresentation Ly at can be one presented by method colled 2's complement. eg 1=> 0000 0001 => -1 = 1111 1110 2 complimed)

Signed nungers that can be stoppedented by that field is from -2nd 60 2nd-1

eg n=8 → -128 €0 127

Representation of negative numbers in binary on handerind firmed is very dependent on the number of bits the are used E. sugarment the number. * Reporcionation of Real Number of There are Severed methods available to separant grad numbers; the most common is the EEE-754 flocting point mothed, which is used by all modern CPUs. of an IEEE-754 metrod: (using 32-bit field) -> 0 to 22 bits are word to suppresent montissa. 7 23 to 30 word to one priset exporent. -> MSB or bit 31 is word to significate signi (Sign × 2 exporent x montissa) Both positive (Praction) (Porition) (Negdi-1) Velvi des porent is Computed bon bit 23 \$30 by Substruction 177 Pusition 128-127 => + 2' × 1.5775 = 3.875

4.3> Microporocessons & Microcontrollers Mi cooprocessor) > borain of modern Computer eg => Cone 15 transister dements => Foor persond Computer the microprocesson is housed on the mother board of pe and wes an externed (bug to interface with morning to othe component. Set of Shared Communication) The Combination of the microprocessor and the other elements on the mother board is Called a mi Go Competer. (Micro controller) A >94 is a Single-chip device that Contains a processoon along with memory k Interfale device on the Some IC. -> Microcontroller uses on Internal bus to communicale with monory k other devices on the chip. => Mi Goprocessor oregines a peripheral chip to Interface with I/o devices.

Tob of penocessoen is to execute perogram instructions which are the low-level code. Lo That is generated by the compiler in tousiding a high-level computer Program into machine instructions, that is used by the particular processor;

y Conocesson Contains three basic units:

- O Control unit
- @ Anithmaticklogic unt
- 3 Registors.

(Control unit)

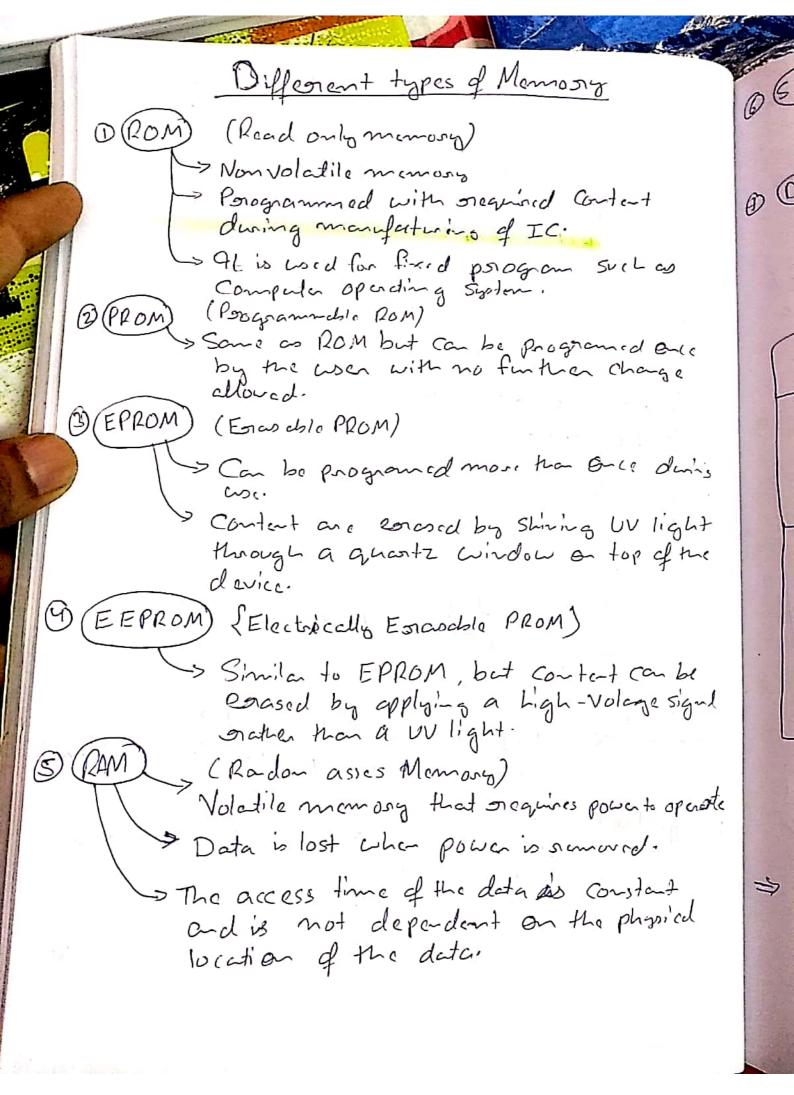
Determines timing & sequerie operation > at gene des timing signeds that are word

to Petch al program instruction from momory k executions.

Agrithmetic & Logic Unit

-> Renforms logical evaluations and actual data manipulation (such as addition)

Registers Memory locations incide CPU that holds internal data while instructions are being executed.



DERAM) (Static RAM)
Deta dose not need to be oreforeshed as lo-s as forcisally Pala ca be accessed faster the DRAM, but it is more expensive. DEAM RAM Had uses capacitoss to Store data. Data must be setroped (susition) poindically bocause of change leading .. Different type of Bus) -7 (Databus) is Used to transport data from/to the CPU K the memory on the I/o devices. > Deta length could be 4.8,16,32 os 64 bit. -7 (Address bus) La Used to Select device on the bus on Specific dotate locations within momony. -> Pach memory location has an address met must be specified before the Content of the location can be accessed. > (Control) -> wsed to Synchronize the opendion of the different elements. > It transmit groad & worth signed , System clock signeds, and other Control signed. ite > Microprocessors & microcontrollers are designed coing two design approaches. 0/7 Complex instruction Set Computer (CISC) B -> Reduced instruction Set Computer (RIST)

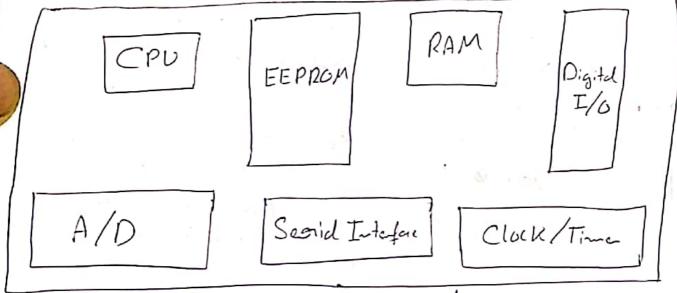
ExCISC Processor uses more complicated instrutions of that can perform more functions.

Do RISC processor uses small number of Simple instruction optimized for fost execution.

to be larger than that for a CISC Processon tends

1 but it can own fast.

4.4) PIC Microcontroller



Components of typical Microcontroller

- # EEPROM -> Used to Store Code.
- # RAM => To Store data while programs is executing
- * PIC microcontroller family
- => Many of the M(U's thave program memory.

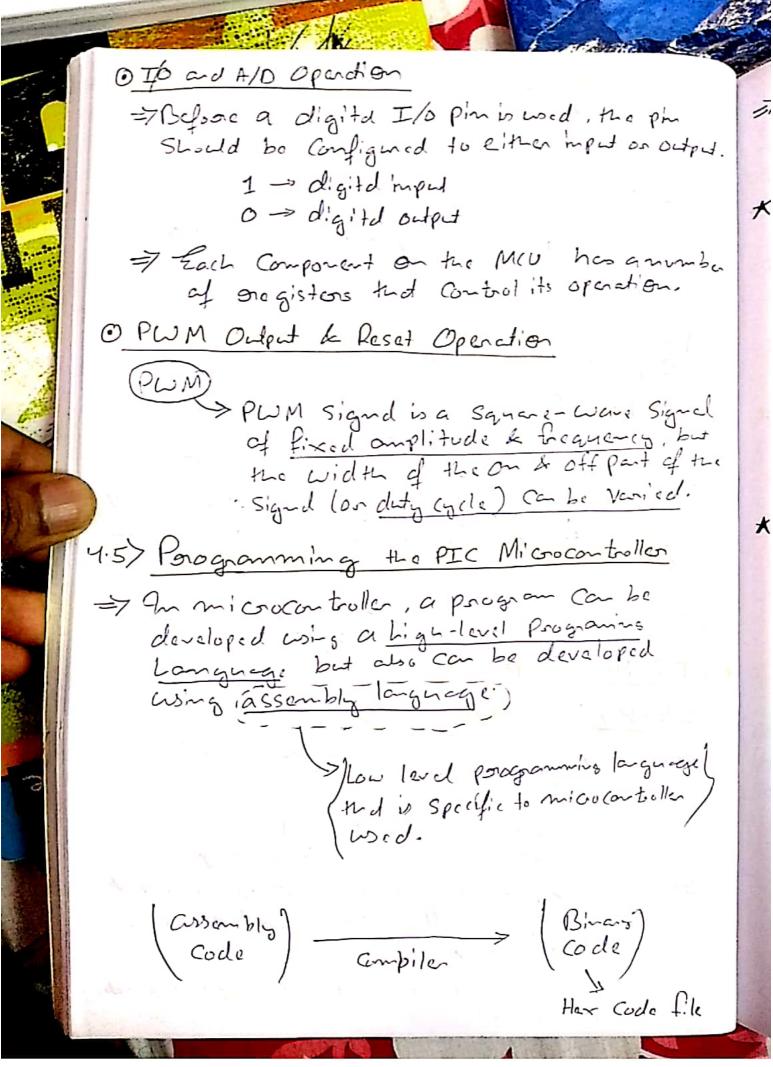
97

×

61

Splan

Flosh menos o Car be exessed & porogrammed to truction electorically, Similar to an EEPROM but without me need foor a dedicated programmer. Simple La Mourer, flash memory does not allow an individud memory location to be enased: Only a Single block of memory location - tende Car be crosed. o (assan of Porogram momoro & data momoro have Sepande buses to allow Concurrent access. * PIN Layout (SSOP) (SUIC) GOLD / Shalak smull 1 | small-outline | Porchage Odlino parkage, (integrated circuit) Used in Surface mout type Circuit * PII MCU Comporents O Clock / Oscillator Source > All CPU operations are synchonomized with -> Any device that can produce a train the Clock of pulses of fixed francicy Ji-2 > Somo chip have a built in clock source & Some seguir or dow on externed device to 25/2 Poroduce the clock pulses. eg > quantz conintal mesondar, comic mesonda 1 KC Circlet.



The binary Code (On her code file) is the downloaded to the microco-teller to be stoard in the non-Volatile program monary. * Pgrogrammers >) The process of transferring a compiled of binary (ude to the MCU is called programing) ([CSP)=> Integrated Circuit Seried programing > Two pin data pin (-> An advantage of ICSP is that the Chip does not have to be senoud from the development on taget hardware. * (Bootloaders) -> Boot loader is code that sieside on the Meu ipsugram manury; Is nowedy word for main program. -> Bootloading Coad uses an PSZZZ Scrid line to communicate with a Cosnesponding PC bootloading application. -> PC bootloading allows the user to download hax file to the PICMCU without the use of any externed programmer. -> Miconochip Technologies provides the ANI310 Softwar pakage, which is a Ligh-speed boutloader for PICIB devices.

* C Porogramming Laguage => There are Severed C-language Compiler for PICMCU. eg => PCWH Compiler