

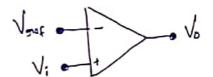
An ideal Op-orp Con be modeled as: High imput Robblace 100 output Vo = KOL × (V+-V-) open loop gain { Loudly 1.5 to 106} => Since the op-amp output is finite, but the op-ap has a very lage gain, we assume V, = V-. Bais assumptions word to andy re ideal op-ang >> V+=V->> No Current flows through input termind. Vo 1 Vsct... Vin=(V+- V-) Open-loop input/output grelationship for an

- and their bandwidth exceeds 1 MHz.
- 3 Op-curps (a perform Vanious operations Such as:
  - 1) Companison
- 40) Summation
- wil Amplification
- W) Antogration
- (11) Anversion
- WI) differentiation

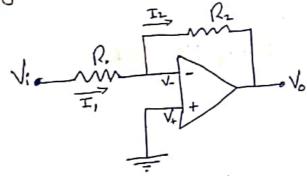
VII) filtering

## 1 Comparator Op-Amp

If Comparator is used to Compare two Voltage signals, and Switch the output to + Vset if one of the Signals is large than the other, and to -Vset Otherwise!



## 2 Inventing Op-Amp



- => Since the non-inverting imput is connected to ground V\_= V\_+ = 0
- => No Coment flows between the inventing know houther imput.

$$I_1 = I_2$$

$$T_i = \frac{\sqrt{i}}{R_i}$$

$$I_2 = \frac{-V_0}{R_L}$$

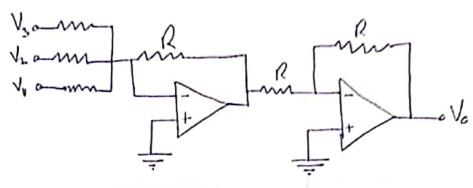
$$\Rightarrow \frac{\sqrt{1}}{R_1} = \frac{-\sqrt{1}}{R_2}$$

$$\Rightarrow \frac{\sqrt{1}}{\Omega_1} = \frac{\sqrt{2}}{\Omega_2} \qquad \Rightarrow \qquad \boxed{\sqrt{2} = -\left(\frac{\Omega_2}{\Omega_1}\right)\sqrt{2}}$$

Thus the circuit invents the input voltage & complifies it by or factor example to the sindic of the master to ance of Patali.

Application: Signal invarion of output will have

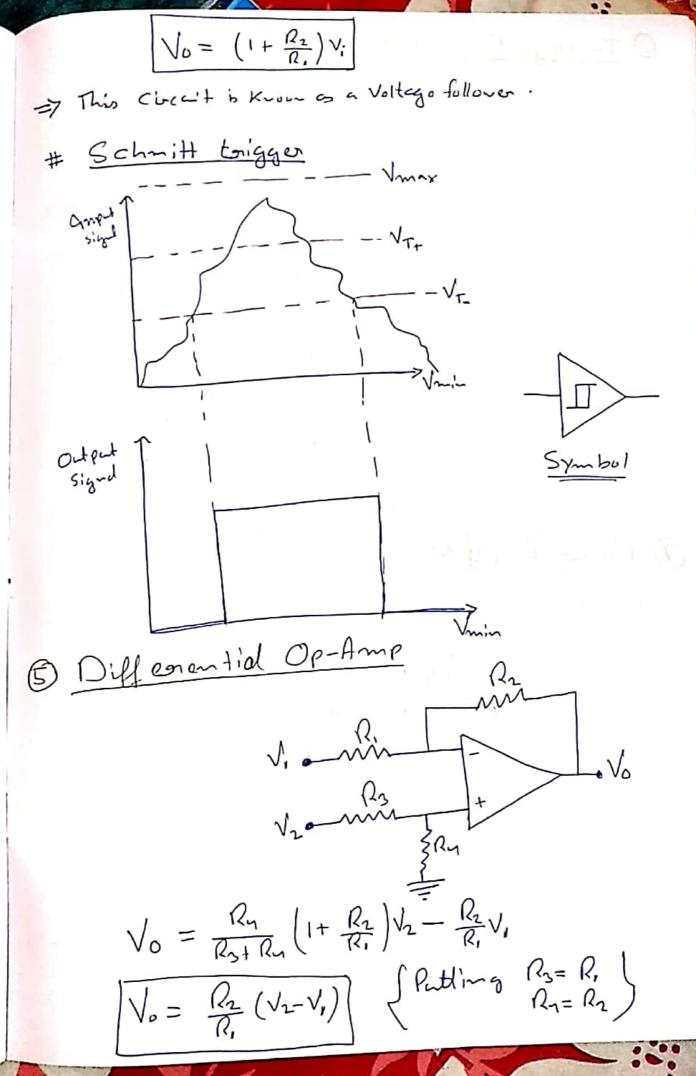
3 Summing Circuit

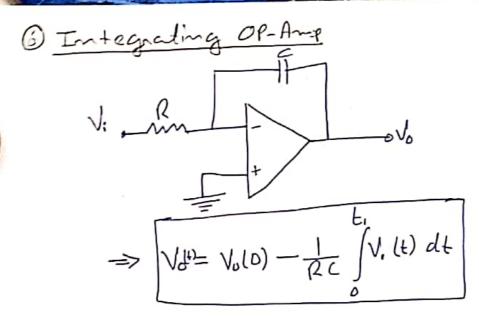


$$V_0 = V_1 + V_2 + V_3$$

1 Non inventing Op-Ame

$$I_1 = \frac{V_0 - V_{-}}{\Omega_2} - \frac{V_0 - V_i}{\Omega_2}$$





Note: If the Capacitoon and the grasistoon were interchanged in this circuit, the opening will at as differentiation of the imput signal.

$$V_0 = -R(\frac{dV_i(t)}{dt})$$

9 Power Amplifier

(Power op-a-P)

> Op-one with higher Currentle

eg = OPAS47 (by Texas Anstruments)

=> Due to their large output current, power op-one are available in parkages with a built in copper tab to allow easy mounting to a heat sink for good thank performance.

2.10) Conounding a Cound Voltage on zero Voltage is = Commany word as suchenomica. 2.11> Solemoids & Pelays \* Solemoids 79t is Commonly word for on-off applications such as to locking on briggering. eg > Electromachanical gralage, door locks ele. -7 9t is an electrically actualed mechanical dorice that has two stage: 11) Retracted 121 Extended Mureable Asmaline Stationary ason Coone 5001-9 (Solaro ida)

## \* Electromechanical Relays

- > Electorically actuated Switches that use a Solevoid to make on borock the mechanial Contat between electrical leads.
- -> One disadvatage of electromechanical onelays is their onelatively long switching time.

  Lathis is in Contonest to Solid state trasisted, which have nonosciands switching time.