Microporocessoons & Microcontrollers * Antroduction Computer Antegrated Circuit faborication Anchitacture Set of oncles and method that describe the functionality 10 organization, and Implementation of Computer Systems. # First Commercial => 4-bit 4004 by wicrobrodisoz (1971) #A by-Product of microprocessor development was the microcontroller. * Microprocessors and Microcontrollers # Mi Cro Processons => At is a general - purpose digital computer Central processing unit (CPU) L>mi Goprocesson is in no Sense a complete digital computer.

=> For flaithmetic and Logic Unit Accumulation working Register(s) Brognam Counter Stak Pointer Anterrupt Clock Clock C'oncu'ts Block diagram of a Microporacessoon => To make complete microcomputer one must add : FMemory _ RAM -> Memory docoder > Oscillation > I/o dévices [serial & parallel d'ata pont] La Special-Purpose Santersupt handless device sounters.

=> Forom micro computer to Computer. -> Mass Storage devices L) I/O Periphereb { Keyboard, CRT display} Internal Anchitecture (Mi Cooprolessos) -> Machine levil code (Use of microprocesson) -> fetch data -> Peoposon extensive Calculation -> Store those calculations in · mass starage device. => Knogram used by the microfonocesson and Stored in the mas, storego device and loaded into RAM as the user directs. => A feu microprocessor programs and Stored In ROM. La ROM board program are Smell fixed programs not opende Periphends and other fixed devices that are Connected to the System. 14 29 - 2 18 Dill grand and the second of the se 1 3 -20 1, 2724 . 5 1-271 The state of the state of the

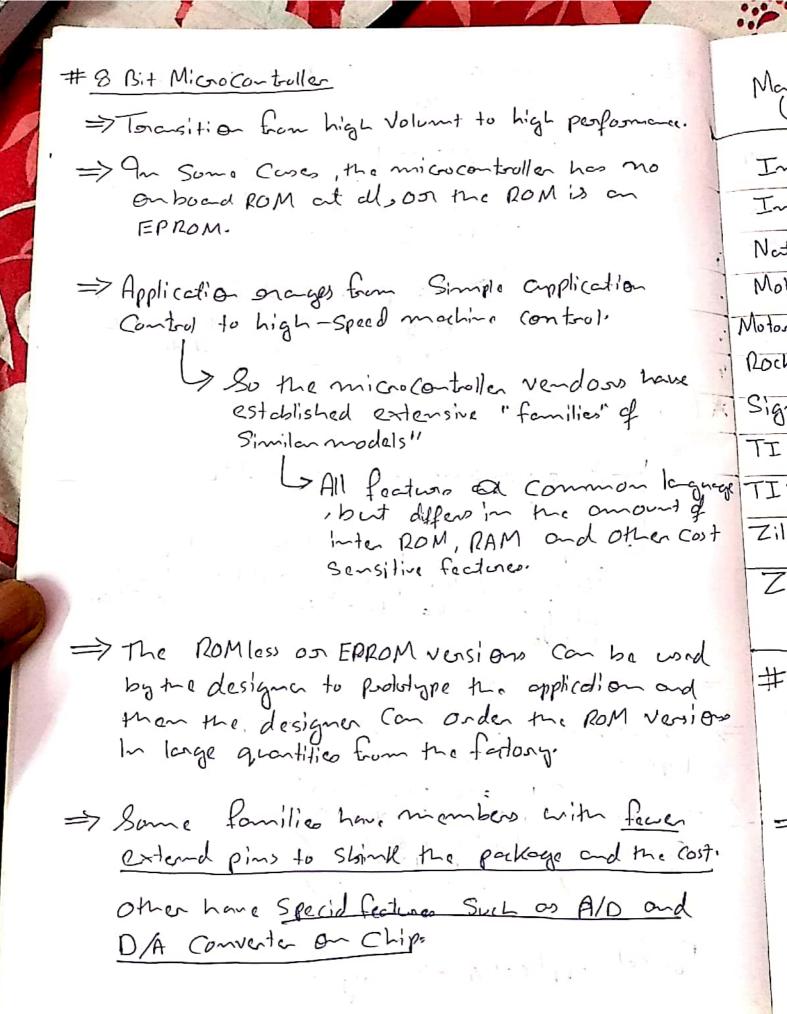
#Microcontrollers =>(P3 ALU Times/Counter I/O Posts Accumulator I/O Ports Register (>) # Con Anterrupt. antend Circuit Internal ROM RAM Clock C'oncuit Stak Pointer Program Counter Block diagram of a Microcontroller => Micro controller ane complete micro Computer. -> All the features fount in microprocesson > Plus Some additions fectures which makes # is micro Computes. => Micro controller is a general-purpose device , but one which is must to fetch date , Perform (Imited) Calculations on that data

	and Controlito convironment based on these calculations.	
	Perime use Control the operation of a machine using a fixed program Should in ROM and that does not Change over the lifetime of the System	
=	# Comparing Microprocessors and Microcontrollers (Microprocessors) (Microcontrollers)	
	Have may operational Opcodes. Codes (opcodes) for moving dada from extend momors to CPU. Share may operational Opcodes. Share may bit had lines	1
	extend moments to CPU. Some on two type of bit hadlings instructions. Summarie.	With the second
i e	Lite propid movement City or opid movement of code and desta form cutting the Compact of bits external addresses to the	
*	Chipe >8-bit Microprocesse	
	Pin Configurdion (280) (8051)	,
	Total Pins 40 Address Pins 16 cfixed) 16 Data Pins 8 (fixed) 8 Antweet Pins 2 (fixed) 2	
	Antwert Pins 2 (fixed) 2 I/o Pins 32	

		7/	::
# Anchitechen	Z80)	(G051)	#"Le
B-bit-onegister	20	-34	ef
16-bit magister	. ч = -	2	-
Stall size	64K	128	
Antend Rom	0	4Kbyte	
Antenl RAM	0	128 bytes	
Extent memors	64K	128Kbg10	
Flego	6	- 4	
Pandlels post	0	2	* A
Serid Parts	0	- U	-
		型	
#Anstruction Set			
(types /varidios)	ZBO	(805)	
External movers	4/14	2/6	
Block moves	2/4	6	
Bit-marplde			
Jump on bit	9/4	12/12	=
	\bigcirc	3/3.	
Stak	3/15	2/2	
Single byte	203	45	
Multi-byte	400		
		62	-
	**	, ,	-

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#"Lean" limstruction set in Gost in Greates programme effort to write Code. > This disadvantage can be over come Chen waiting lange programs by mount high level language Such as BASIC and C. The price paid for neducing Programmen time is the size of the program generated * A Microcomboller Survey > Microcontroller is a Commodity -> Expense is one presented more by the Voluma of the package and the number of bins it has than the amount of Silicon # Fom - Bit MicroCon trollers. => The U-bit miGo controller is today to most Popular micro made Hitachi: HMCS40) National: COP420 OKI: MSM6411 TI; TMS 1000 Excuples Toshiba: TLCS 47 Typical Application => Toys.



		Manufacture:	Quita	Counters	RAM	ROM	Other Feetures
		Manufature: (Model)			(byles)	(c + 6 cm	
	1	Intel: 8048	U0:27	1	64	IK	to BK
	,	Intel: 8051	40:32	2	128	48	to 12 8K'Soid Post Serid bit I/o
		National: cop820	28:24	1	67	IK	Serid bit
		Motusola: 6805	28:20	(64	IK	
	1	Motorola: 68MCII	52:46	2	256	8 K	Serial Ports: A10 Codel doutine
,		20ck well: \$500/1	42:32	1	1881	2K	4
	5	Signetics: 87CSSZ	68:48 .	3	256	BK	11/10/
		I:TMS7500	40:32	1	128	21	TILL A/O: Seril Pols, UOT
1	T.	I:TMS370C050	68:55	2	256	4,1	70 0 1
1		ilog: Z8	40:32	2	128	3 21	Entendamens to 124 K; Swid Port
	Z	-ilog: 28820	44:40	2	- 2=	72 8.	1.0.1
			6 34	· William			Post-

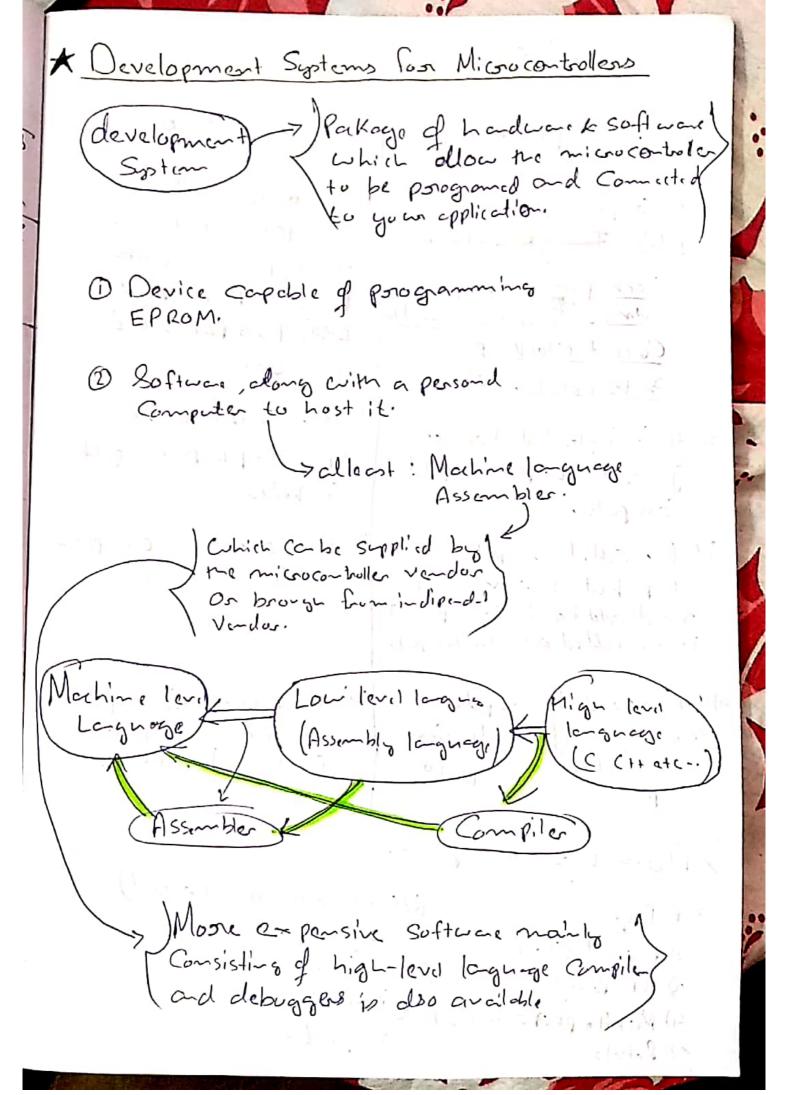
#16 Bit Mi cro controllers

Clark speed

byte - wide data

-Speed Controllers have evolved to solve high repeted Control problem of the type that might typically be confronted in the Control of Seminarion isuch as grobot arms, or for digital signal processing. (PSP) explication.

	Λ	<i>,</i>					-
	Marfailur	Pins 1/6	Conte	(mobile	(10 M (belos)	Otre feeters	-
1	Hitach:: MB/532	B& 165	5	IK	27/	Print and and the	
	Intel: 8096	68/40	2	232	gV-	Serid Post A/D UDT PWM	
The Part of the last of the la	Notion!: MPC16164.	68/52	4	512	1612	Serid Pert A/D, WOT PWM	
-	≠ 32 Bit M >32 bit do instrument , tell Com met feat on open	esign tdio munica ture op ding	tango tango y av silibus plicilio	t orice	bolice, s, ma somobile	highly intelligent iga processing and other among	
	Inten	8006	0			tuce fectures	
,	132-Pin C 20 Megal 32 bit b Floding = 512-byte Interrupt	renamiliant Construction	lock lock	, 5	Fault. Fault. Tode Clobd Clician	ent Procedure (d). -handling Capability events gregisters tinterrept vectors ile addressing	



Questions

Micro processor

Micro Controller

B)

6

(4

- 1) Microprocesson Contains ALU, Truing & Combon und
 - Obenter, Megister, Accomptos
 - / Cucuit Clock & anterrupt Circuit.
- 2) at is intended for a genera-purpose disital Computer
- 3 96 is colled computer on Chip but it is mol-9+ need additional components to be Colled a micro compete.
- 4) Htis word for rapide movement of data for extendmemory to chip.

- 1) At contains all the Components that is in microprocessor with april RAM, ROM I/o Ports and Einers-
- 2) at is intend to be Spraid-purpose digital Controller.
- 7 It is a micro compute In true Serice.
- 4) 9t is used for repide morement of bits within the chip.

2) Both has 40 pins,

- 3> 1) Toy
 - 2) Digita Cloud
 - 3 30 Prints
 - 9) Mobile phone (may bo) Mondse
 - 5) Robots
- - 10 Spadle

A CONTRACTOR OF THE PARTY OF TH
B Shoels
6 @ 37 bit 6 32 bit
Babit @ Bit
6 ubit
6 8 bit
D'Anstide of DOM, ERROM'S cased for easo
of Prototyping,
memory at 10- Cost.
@ , Ancorosa Clock Spard
2) Deste width data word
@ O Computer (Lin Scid Port)
a Accomplian on Compiler of the 1000
3 Programes (Daice Capable of Programin's EPROM)
10 64 bit Microcontroller.
>ARM. has announced 64-bit
ARMV8 Platform.