

		1	2	3	4	5
A						
B						
C						

Sheet: [LPC313x](#)

File: [LPC313x-DIP-Board-LPC313x.sch](#)

Double click inside the boxes to jump to the corresponding schematic page

Use mouse wheel to zoom in and out

Sheet: [FLASH](#)

File: [LPC313x-DIP-Board-FLASH.sch](#)

Sheet: [Connectors\\_JTAG](#)

File: [LPC313x-DIP-Board-Connectors\\_JTAG.sch](#)

Sheet: [Power](#)

File: [LPC313x-DIP-Board-Power.sch](#)

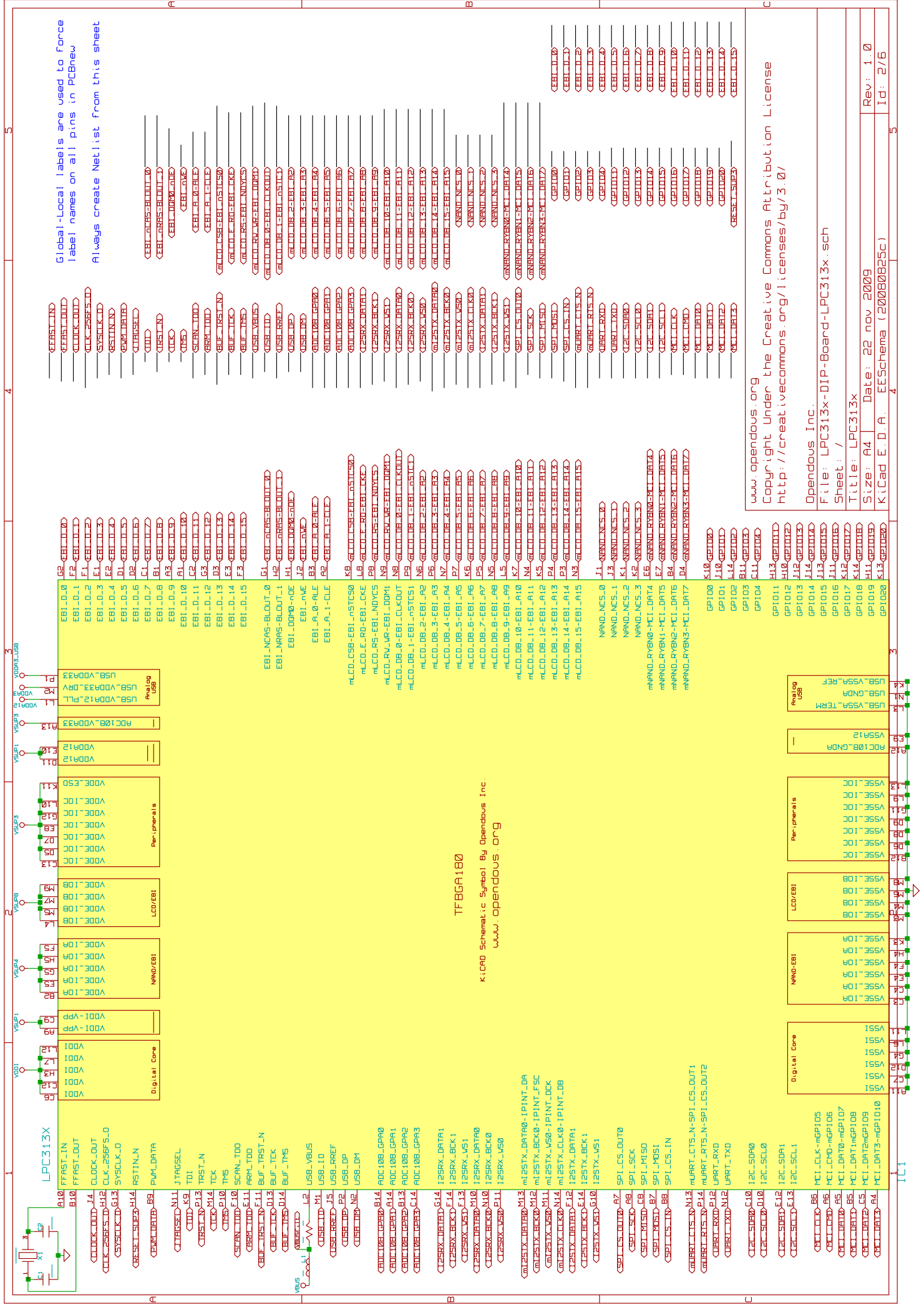
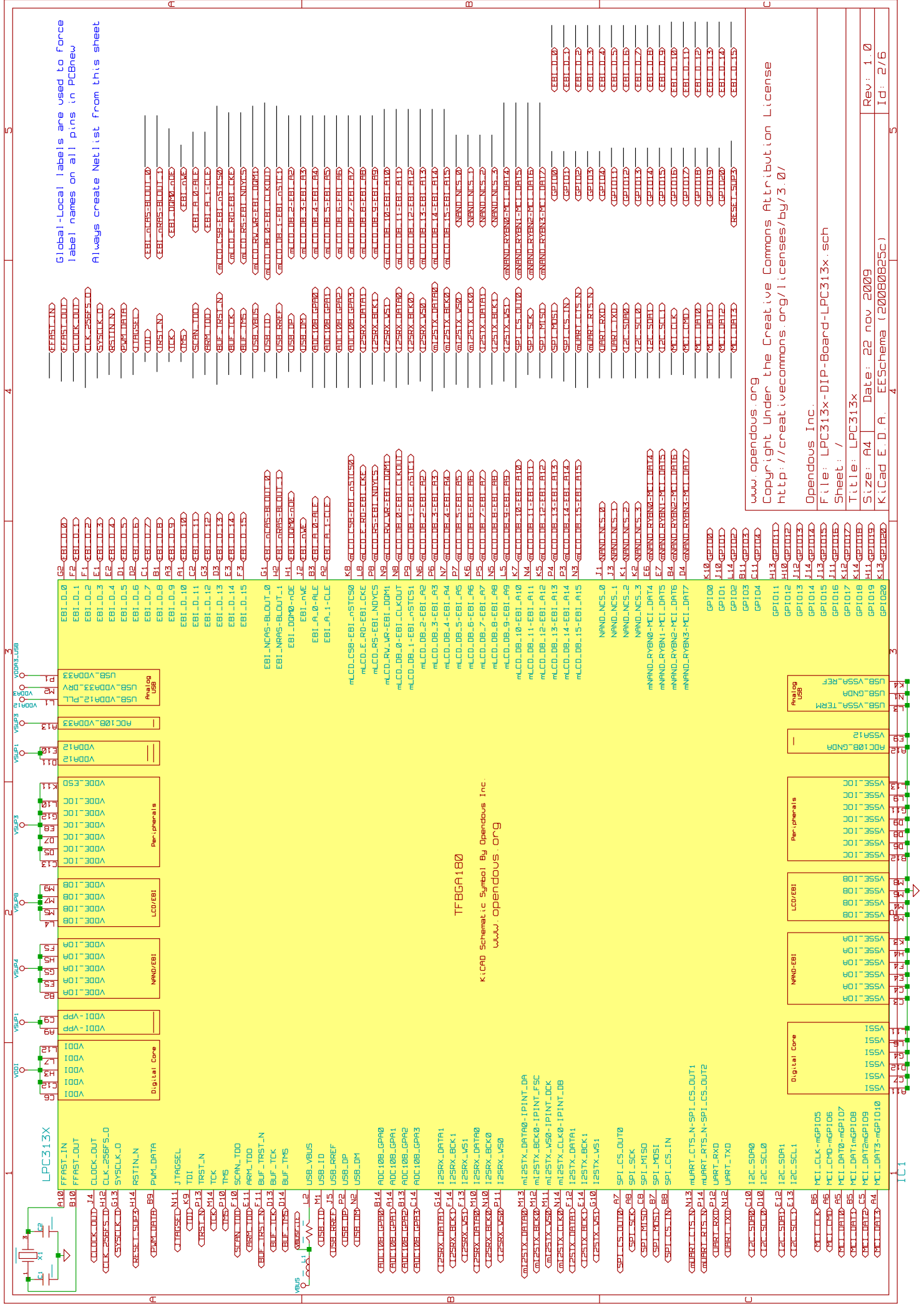
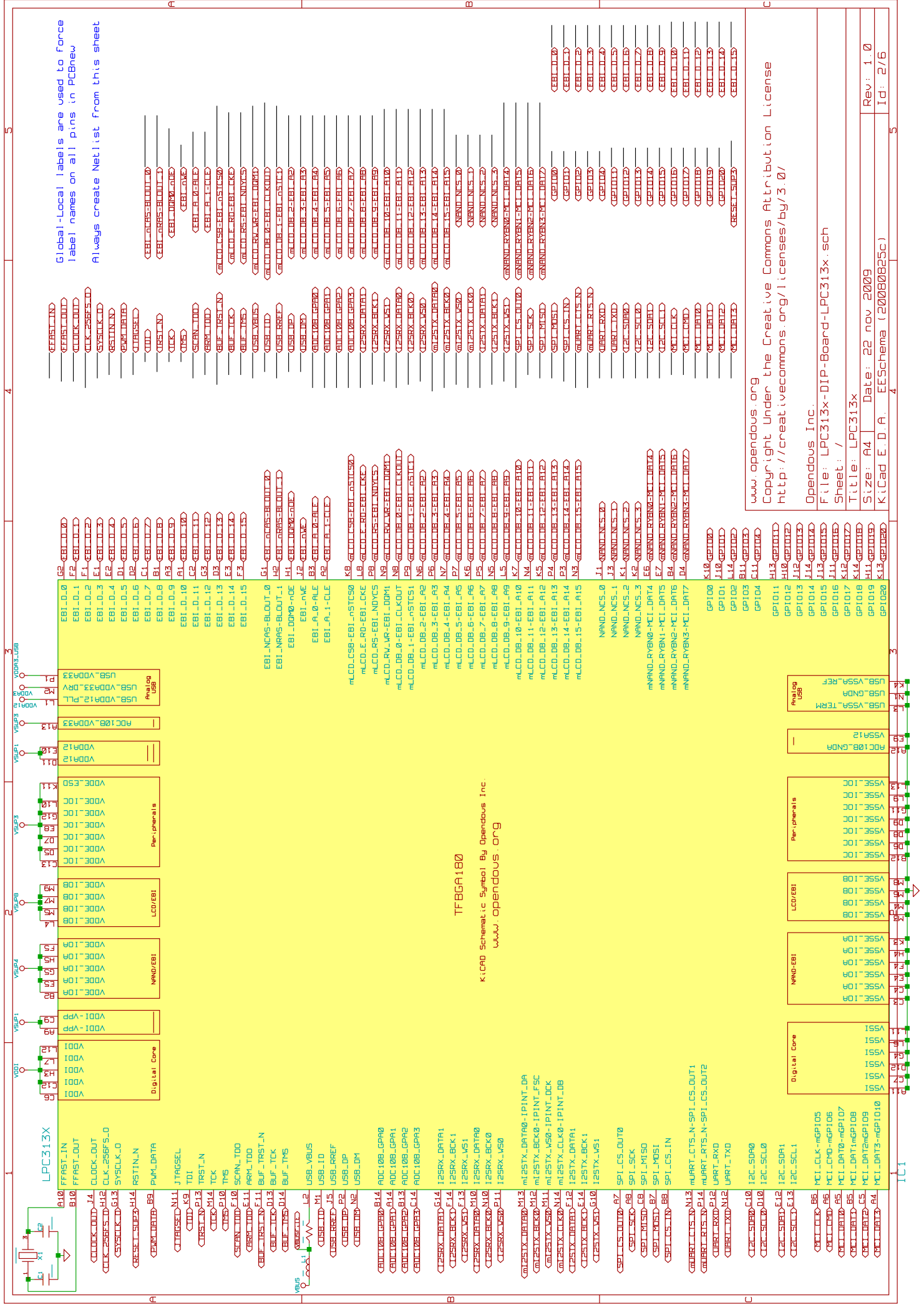
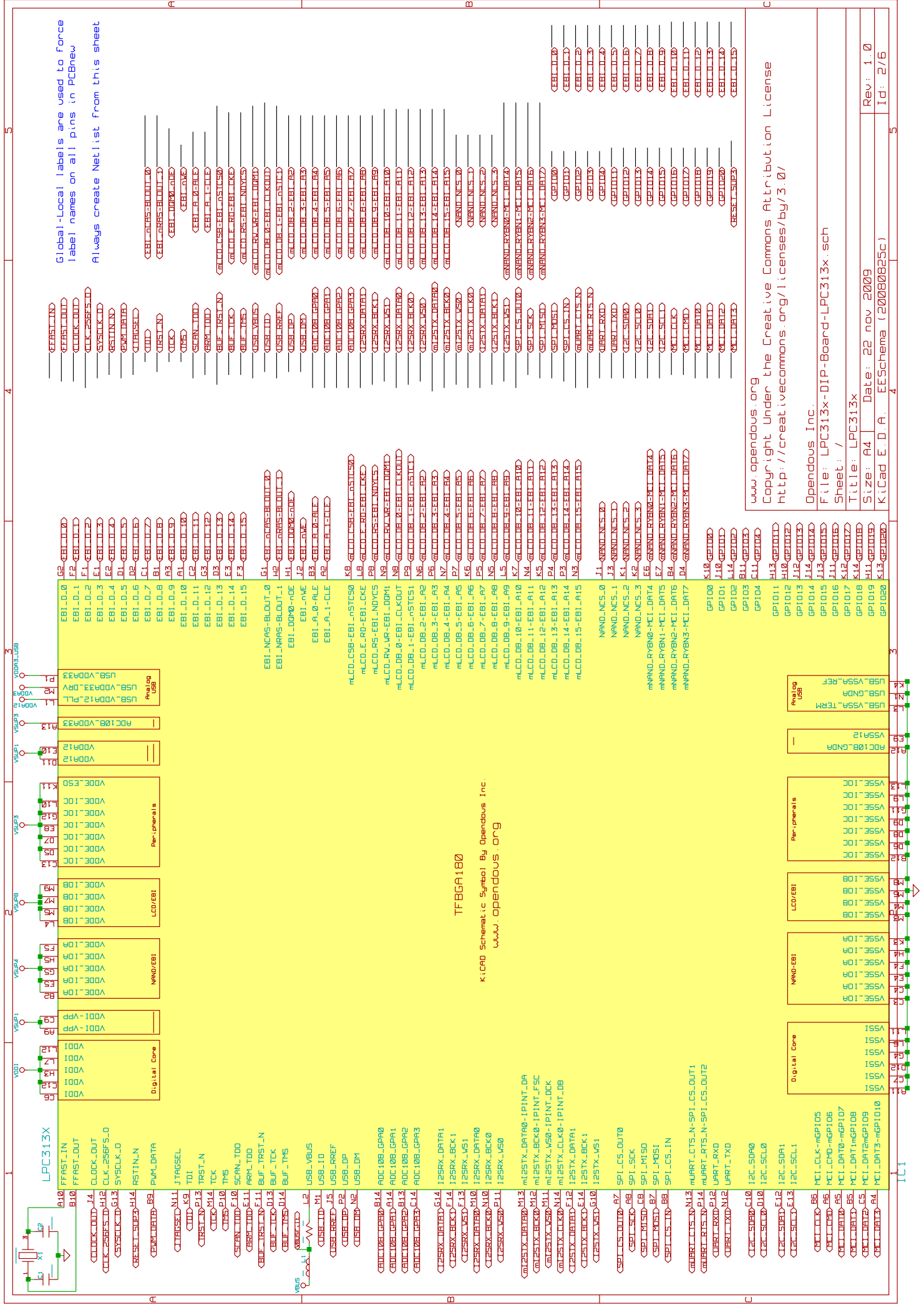
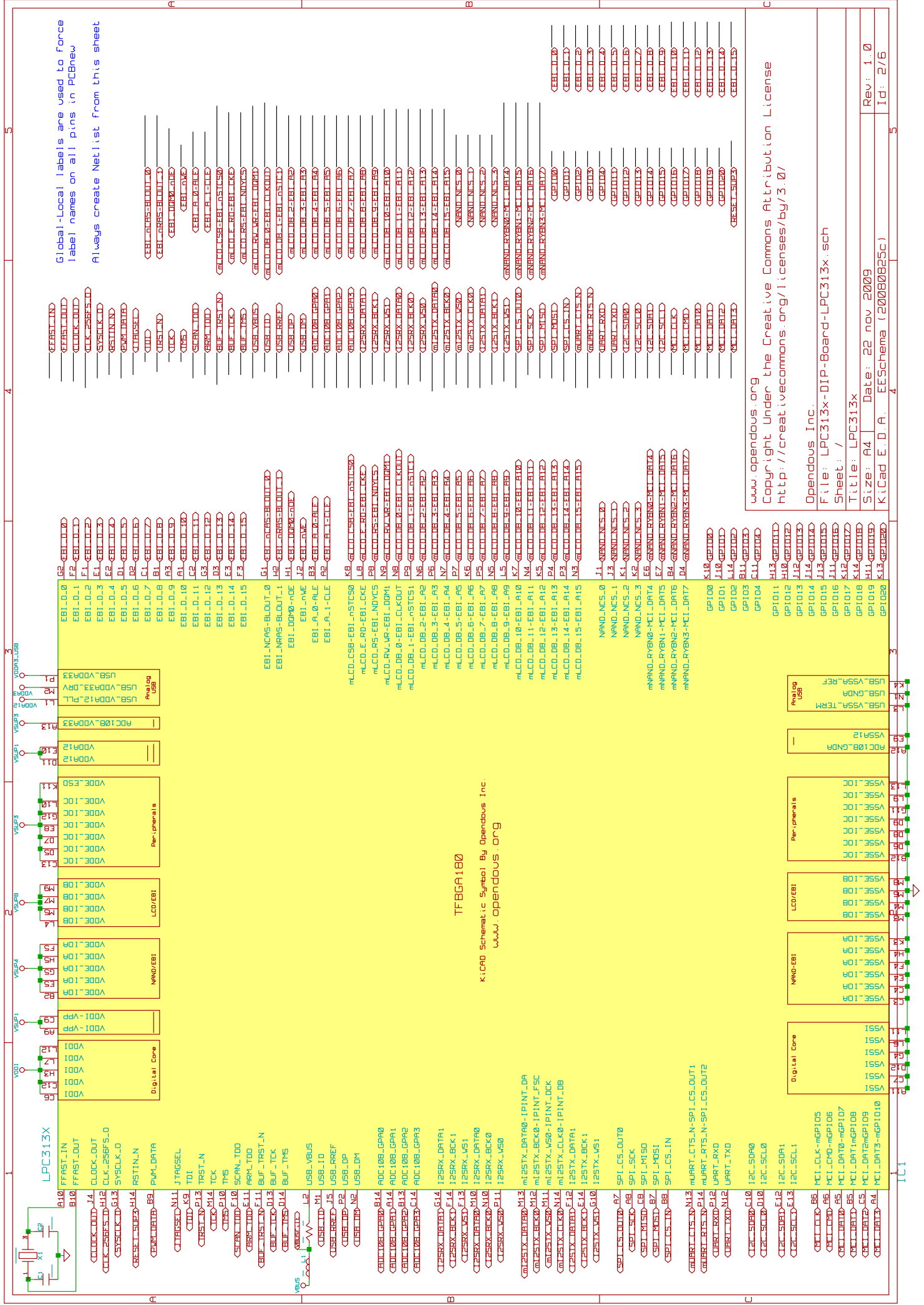
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File: [LPC313x-DIP-Board-Vias.sch](#)

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File: [LPC313x-DIP-Board.sch](#)  
Sheet: /  
Title: [LPC313x-DIP-Board](#)

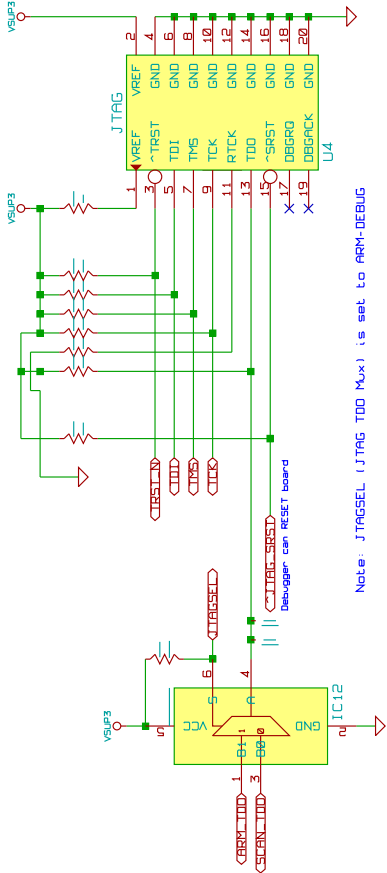
Size: [A4](#) | Date: [22 nov 2009](#) | Rev: [1.0](#)  
KiCad E.D.A. EESchema (20080825c) | Id: [1/6](#)





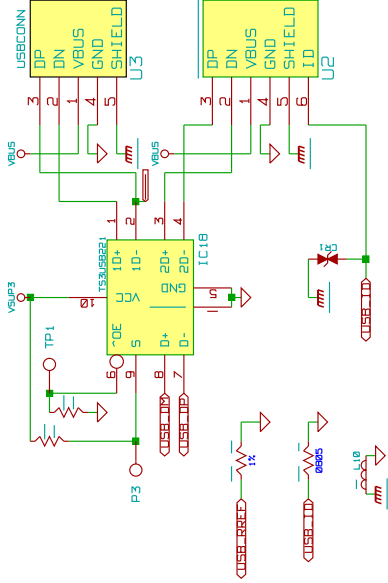
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File: LPC313x-DIP-Board-Power.sch
Sheet: /
Title: Power
Size: A4
Date: 22 nov 2009
KiCad E.D.A.
EESchema (20080825c)

JTAG



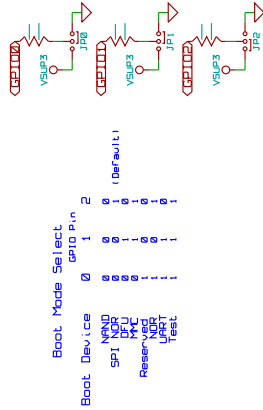
Note: JTAGSEL (JTAG TDO Mux) is set to ARM-DEBUG

USB

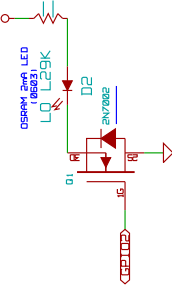


USB-ID when pulled down (R31 populated) enables Host-Only mode

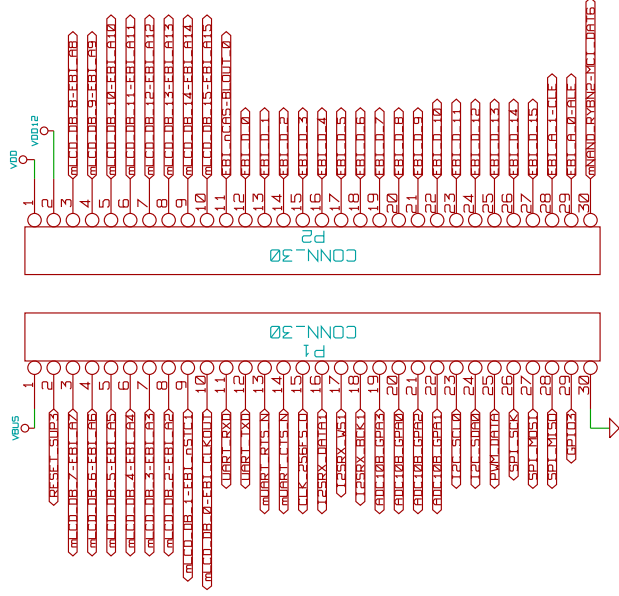
Mode Select Jumpers



Boot Status



SIL Connectors



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File: LPC313x-DIP-Board-Connectors\_JTAG.sch

Sheet: /

Title: Connectors and JTAG

Size: A4 Date: 22 nov 2009 Rev: 1.0

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