

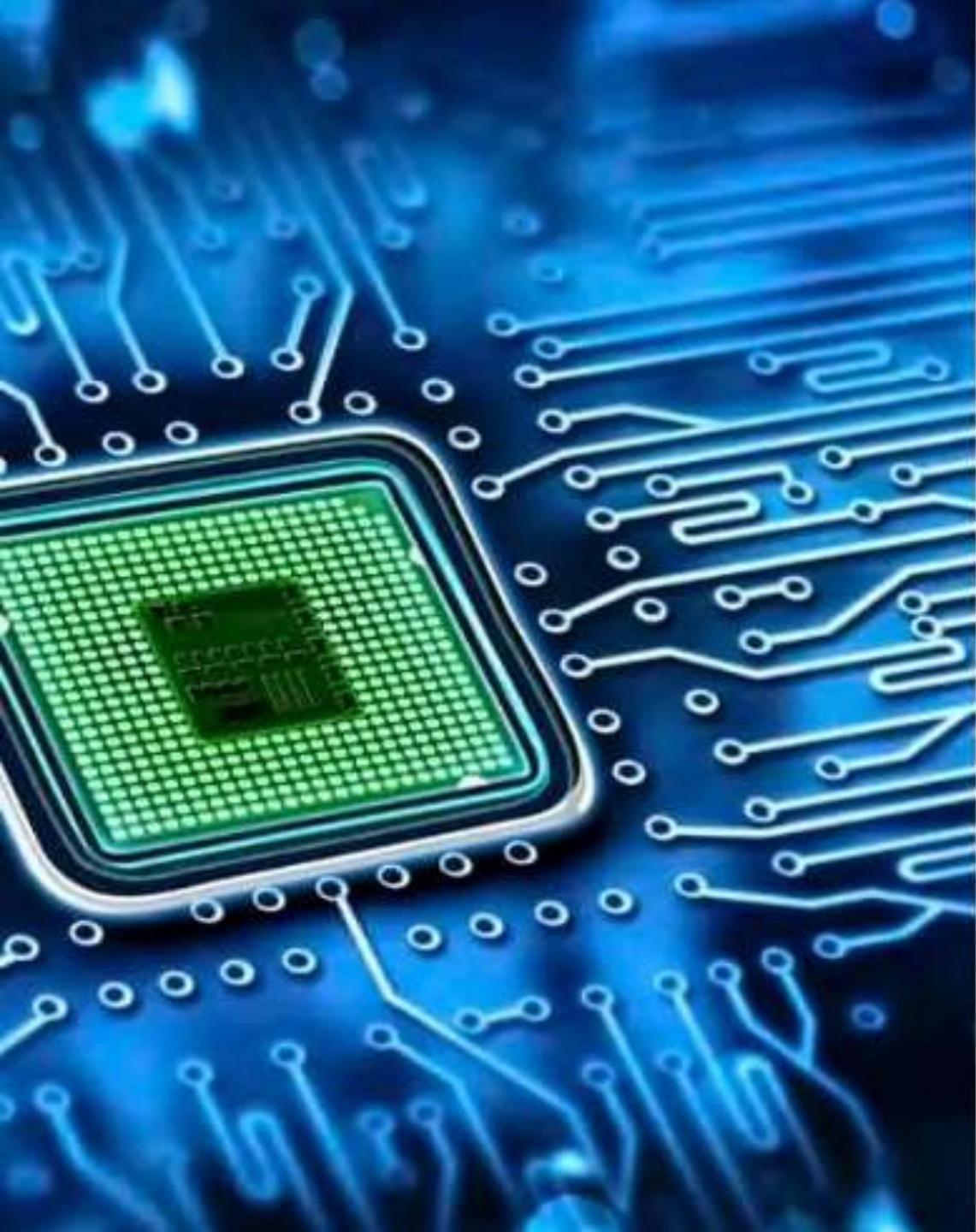
# **MICROPROCESSEUR MOTOROLA 6809**

---

RÉALISÉ PAR :

NOUHAILA CHAHOUB  
AYMEN ENNAJI  
HIBA AMROUN

ENCADRÉ PAR :  
MR. HICHAM BENALLA



# **SOMMAIRE :**

**Introduction**

**Hardware**

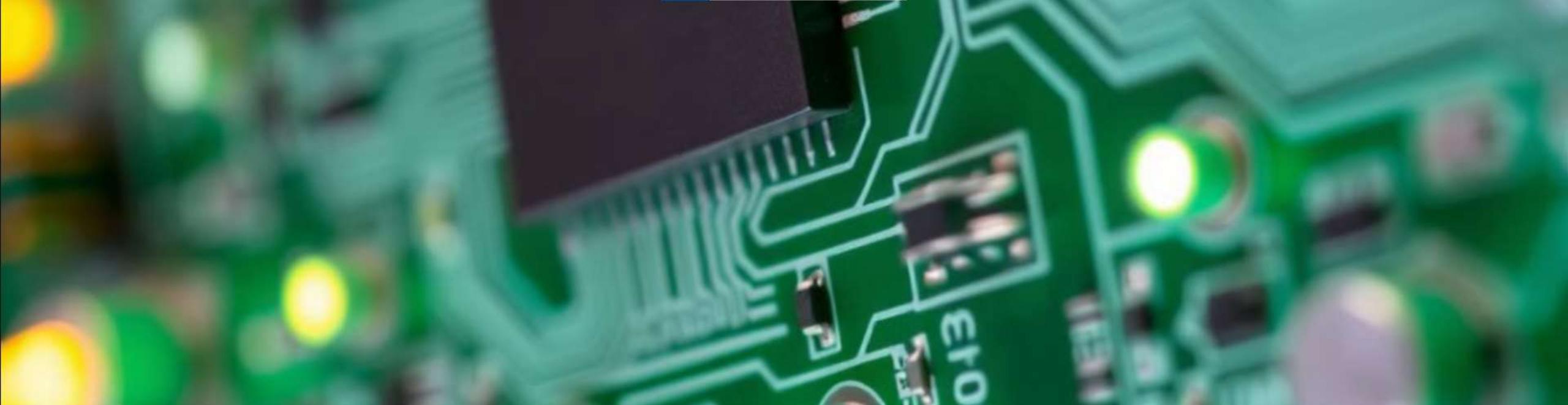
**Architecture**

**Conception UML**

**Conclusion**

# ***INTRODUCTION***

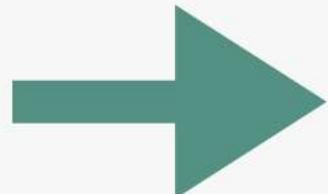
---



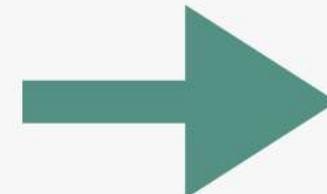
# **Hardware**

---

**Fetch**



**Decode**

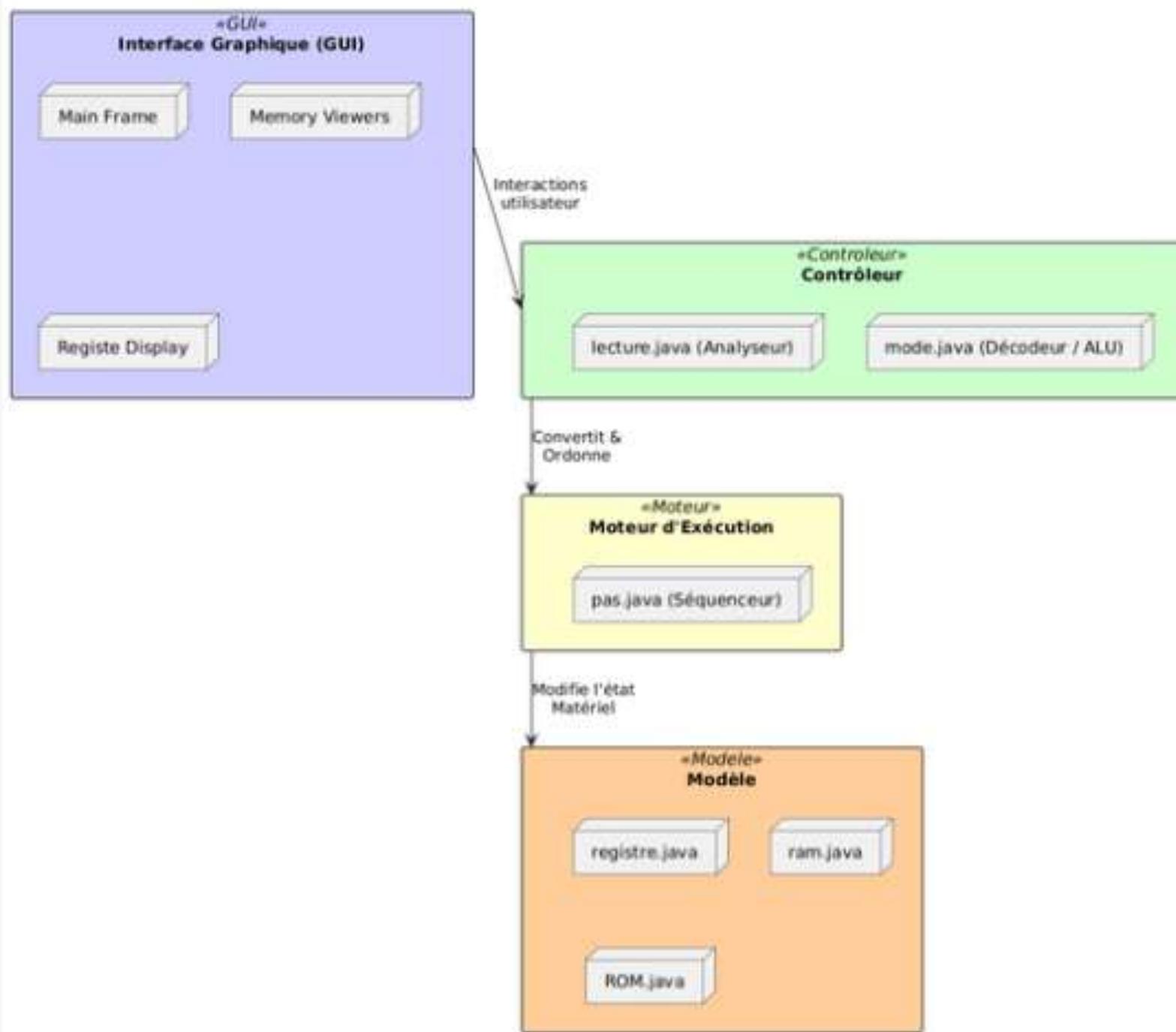


**Execute**

# **ARCHITECTURE**

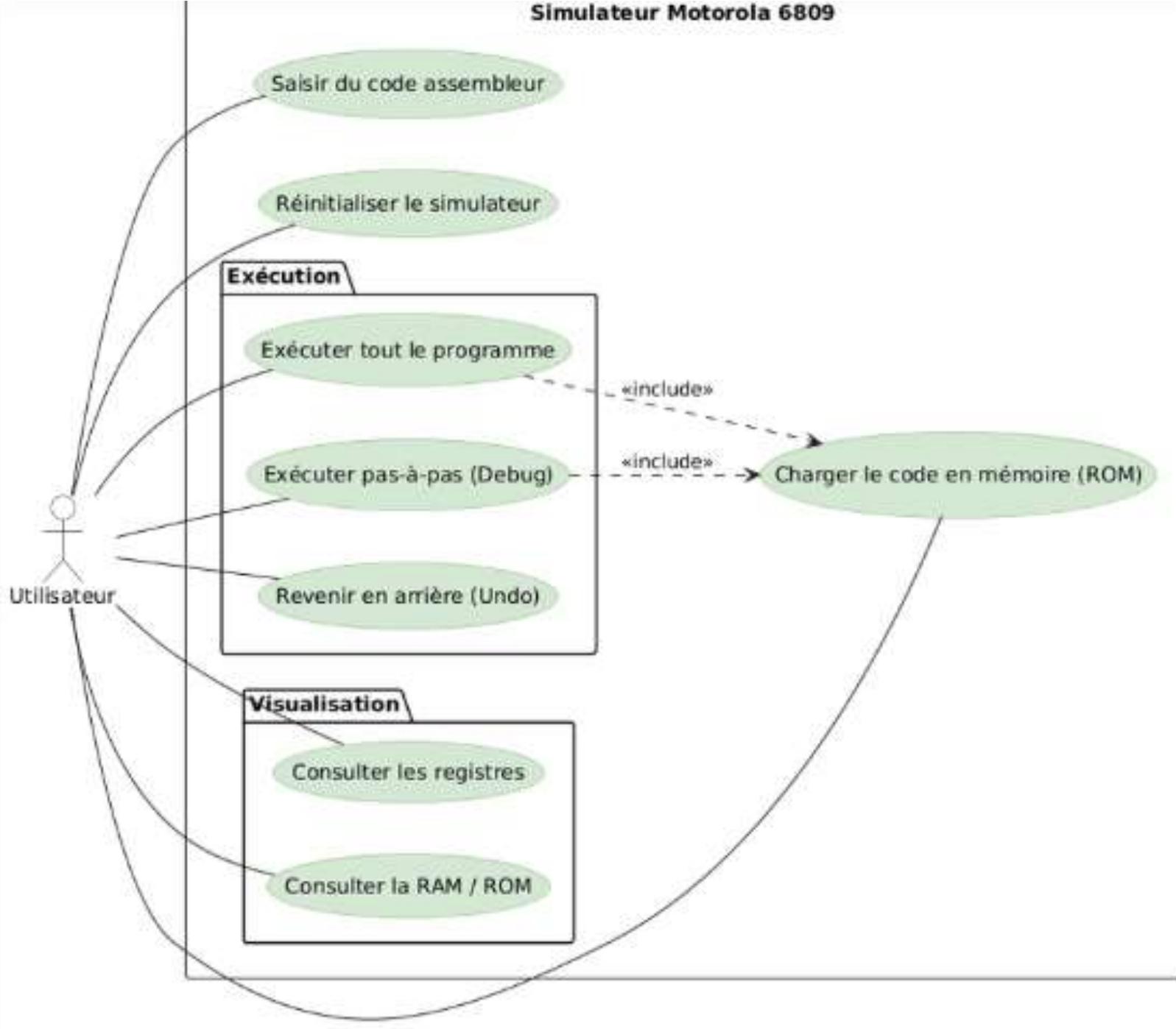
---

## Architecture en Couches du Simulateur

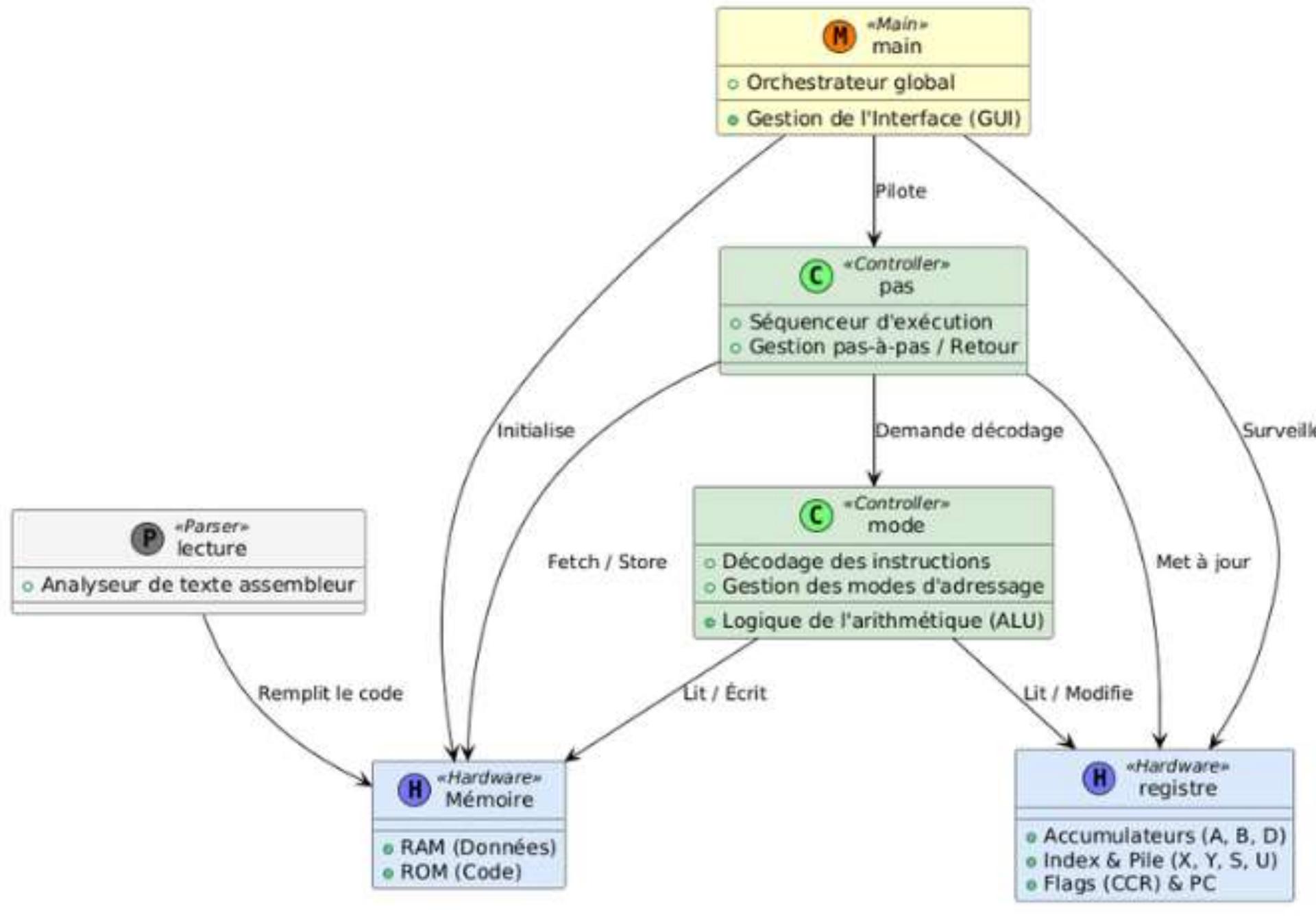


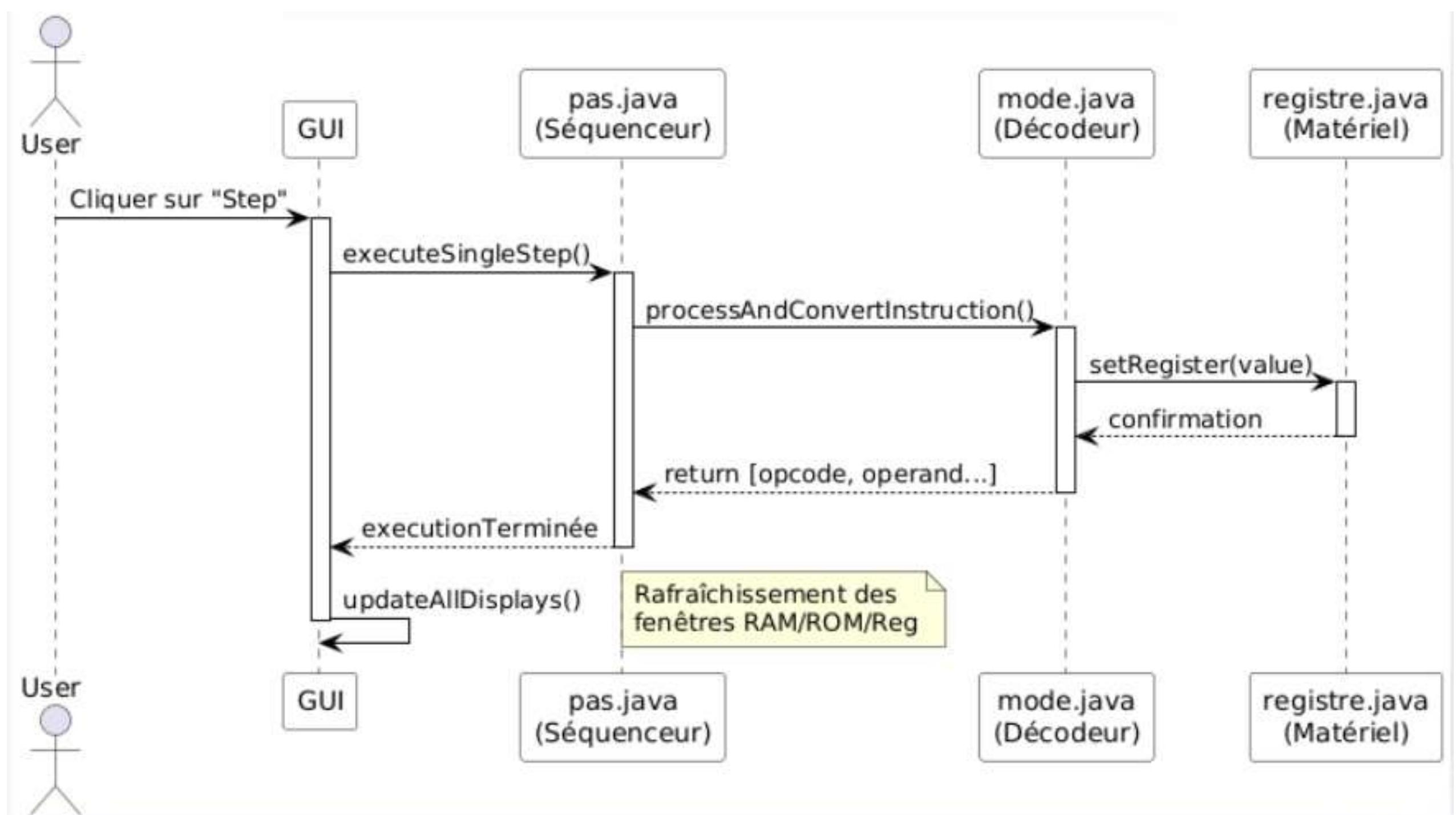
# **CONCEPTION UML**

---



## Cœur du Simulateur (Modules)





# **SIMULATION**

---

# **CONCLUSION**

---