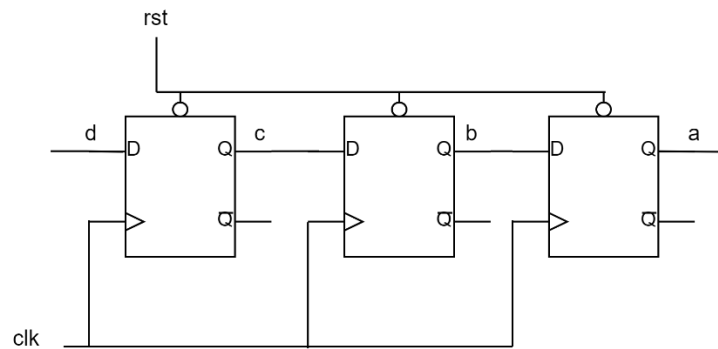


Nouman Zia (H251060)

Note: instead of 0 and 1 state ground and Vcc signal can be used.
Clock and rst wires are ommitted. Q' is left unwired

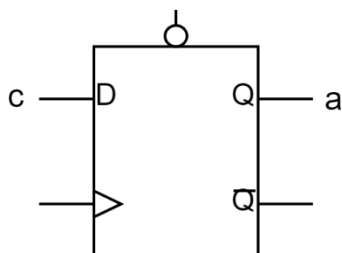
Task 1



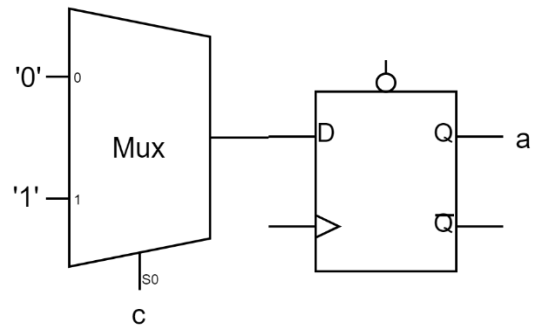
Task 2



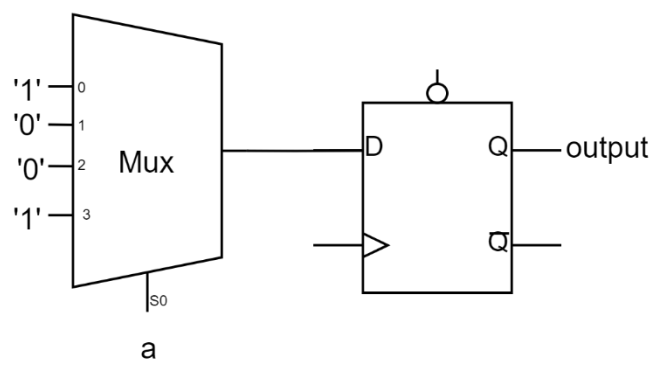
Task 3



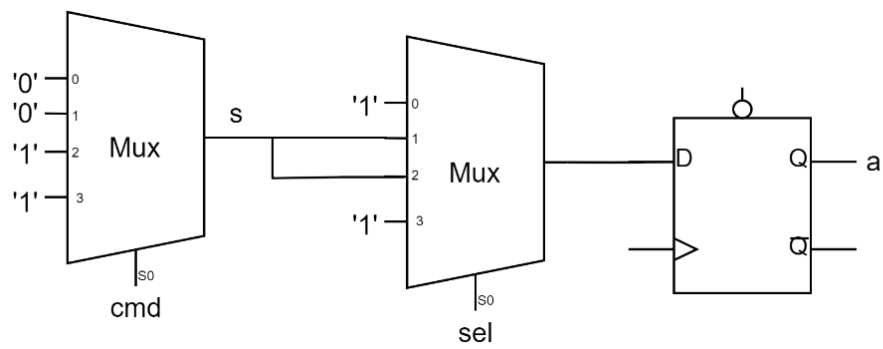
OR solution 2



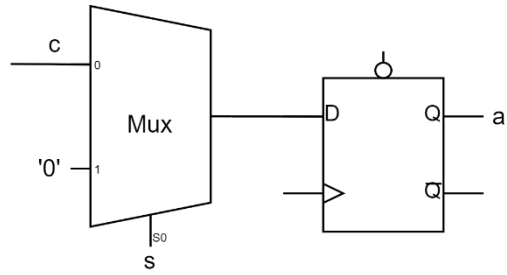
Task 4



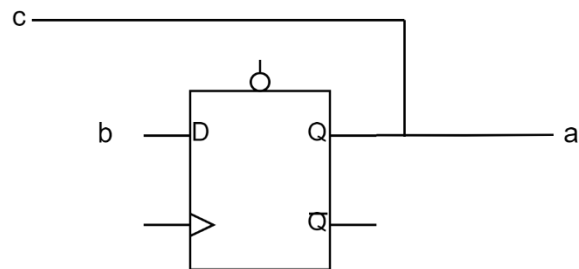
Task 5



Task 6



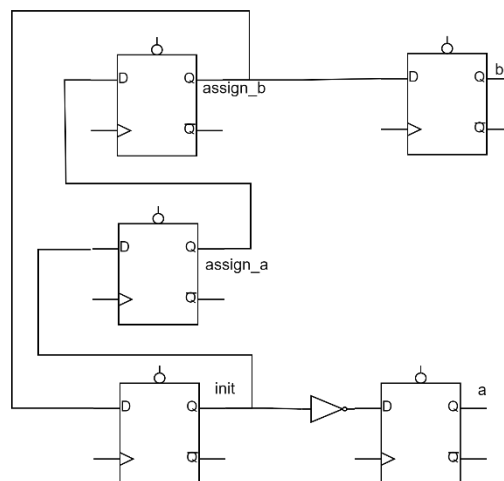
Task 7



Task 8

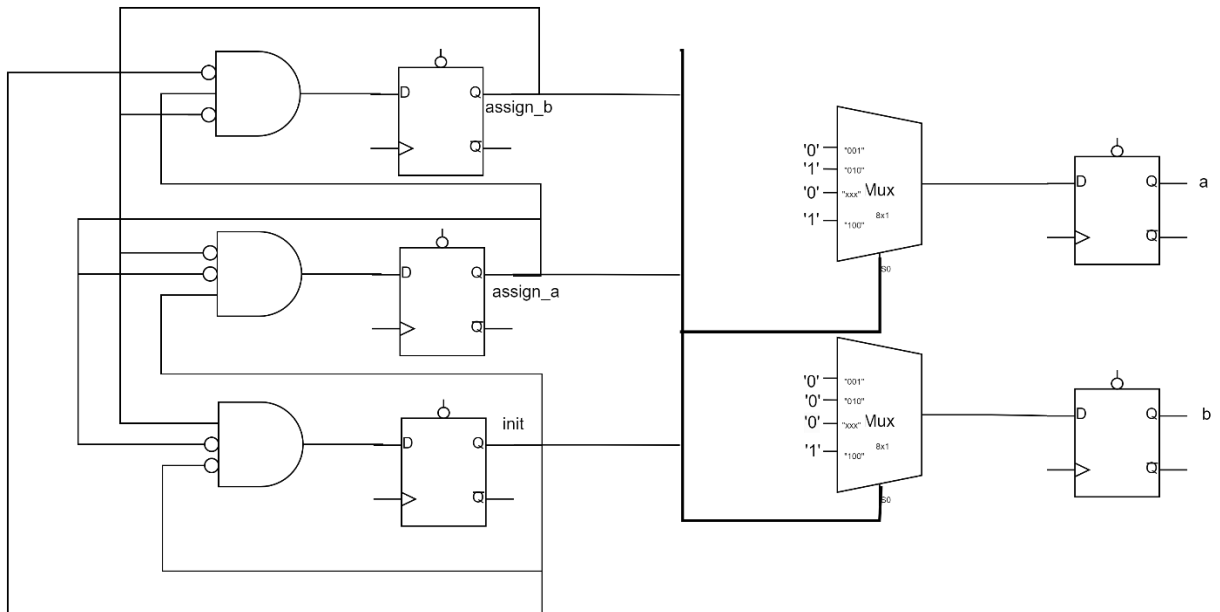
One hot encoded.

init	001
assign_a	010
assign_b	100

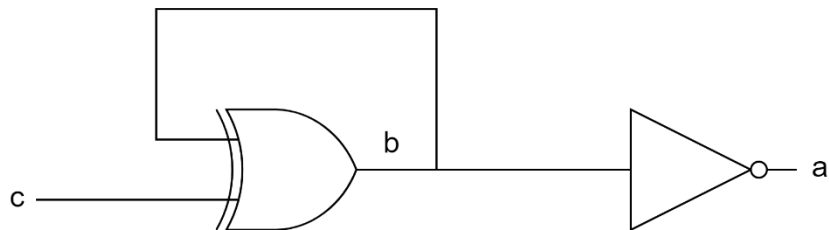


OR Solution 2

Only relevant ports of 8x1 MUX are shown.



Task 9



Task 10

