

FYP Ganttchart

- Stage 1: Basic Learning
  - Get familiar with hardware
  - Thesis proposal preparation
  - Thesis proposal and PPT
  - Thesis Proposal
- Stage 2: Main Development
  - AES development
  - Firmware verification
  - Side-channel protection
  - Interface protection
  - System integration
  - Interim report preparation
  - Interim Report
- Stage 3: Thesis Preparation
  - Interim report rectification
  - Thesis writing
  - Thesis optimization
  - Thesis defense preparation
  - Thesis Defense

