**CSCI8080 Final Exam Spring 2020**

**Problem 1 (10 points)**

Company TR’s employees are organized in a strict hierarchy with the CEO as the root of the tree. The children of a node N are all supervised by N.

Each employee E in TR is assigned a positive number, EV[E], that measures how valued he/she is, in TR. We want to find a set S of employees that with the total maximum T value with the following conditions: (i) The CEO is always in the set regardless of her value, and (ii) If an employee is in the set, then her immediate supervisor is not in the set.

Design an algorithm that computes S and T given the employee hierarchy of TR using a dynamic programming based approach.

Notes

* Probably going to start with only CEO and one other employee, build from there?
* Optimal substructure
  + Every subtree solution starting at 3rd level (which employees I take from this subtree) must also be the total max T value for that subtree. Otherwise you could sw
* If CEO has to be in it, none of their direct reports can be in it – base case?
  + Optimal substructure maybe holds starting from 3rd level? Since 3rd level can be either in or out since 2nd level is definitely out
* Algorithm
  + Start with leaf nodes (bottom employees) their values are just EV[emp]
  + Move one level up to N, calculate this value = max(EV[N], sum(EV value of children(N))
    - Is it better to take this manager N or take all of their direct reports?
  + Move one level up to N + 1, calculate this value = max(EV[N + 1], sum(EV value of children(N + 1))
    - If any of the direct reports for N + 1 are included in order to get their max value then the node N + 1 cannot be included

**Problem 2 (10 points)**

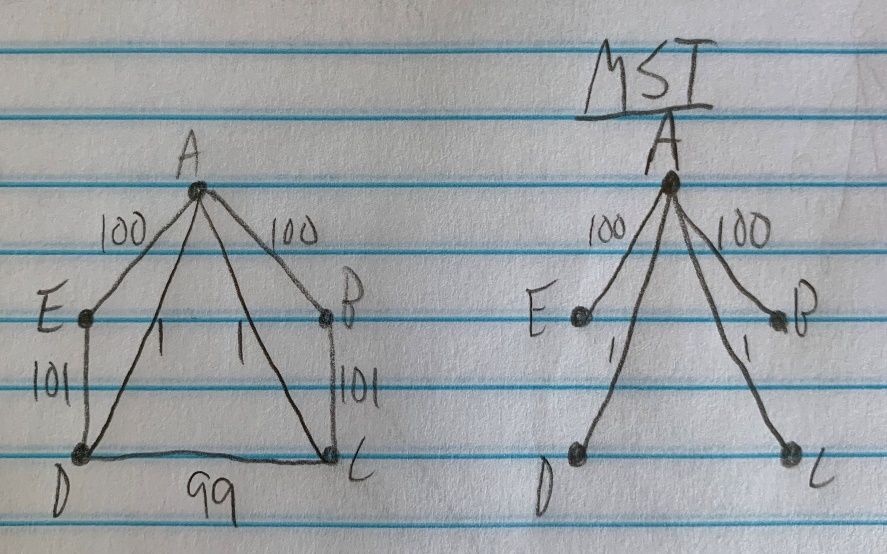
Let T be the minimum spanning tree of a graph G. Prove or disprove the following two statements.

1. T will not contain the maximum weighted edge on any cycle in G.
2. T will contain the minimum weighted edge of every cycle in G.

Notes

* MST = all vertices connected, without cycles, using minimum weight
* Cycle in G = all vertice
* A

Answers

1. True, assuming this is an undirected graph with a cycle that means that there are 2 ways (edges) to get to every node. Given that every node will be included in the minimum spanning tree in the end and there are 2 ways (edges) to get to every node the MST must always use the lighter edge, by definition. This can be proven by contradiction. Let’s assume we have a MST that includes the max weighted edge on a cycle in the graph. We use this edge to connect to some node N. If instead we were able to connect to node N using a lighter edge (the other edge that connects node N to the graph that we know is there because a cycle exists and is lighter because the other edge is the maximum weight edge in the cycle) then we would end up with a spanning tree that is lighter than the MST we assumed before. This obviously cannot happen meaning that any MST will not contain the maximum weighted edge on any cycle in G.
2. False, consider the graph and MST below where we have a cycle (A, B, C, D, E, A) with a minimum weighted edge of (D,C) = 99. This minimum weighted edge in that cycle is not in the MST for this graph.
   1. 

**Problem 3 (5 points)**

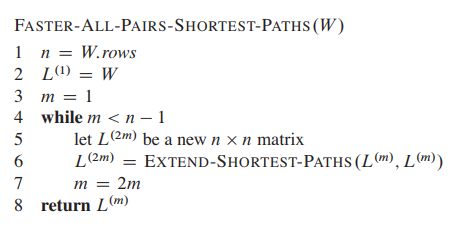
Rewrite the Faster-APSP algorithm to include the predecessor matrix computation. Explain the modifications you made.

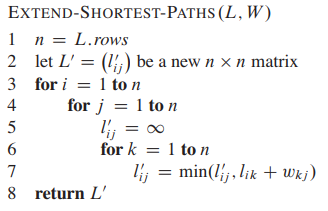
Notes

* Predecessor matrix
  + For every vertex pair a, b, mark what vertex came right before b on the shortest path from a to b
* k is predecessor in extend

Answers

Here is the Faster-APSP algorithm from the book:





In order to modify the Faster APSP algorithm to include the predecessor matrix computation you would add a variable P that is the predecessor matrix with all values initialized to *nil* sometime before line 4 of Faster-All-Pairs-Shortest-Paths. You would pass that variable P into the Extend-Shortest-Paths function. Then in the Extend-Shortest-Paths function you would rewrite the for-loop iteration on line 7 to do the following

* x = lik + wkj
* if x < l’ij then
  + // We can get a better path from i to j by going through k
  + pij = k // lower-case-p here references an entry in the predecessor matrix P
  + l’ij = x

There is no need for an else here because if it not an improvement to go through vertex k on the path from i to j then we just don’t update the value for the shortest path weight.

**Problem 4 (5 points)**

Suppose you are given a magic black box that can determine in polynomial time, given an arbitrary Boolean formula α, whether α is satisfiable.Describe and analyze a polynomial-timealgorithm that either computesa satisfying assignment for a given Boolean formula or correctly reportsthat no such assignment exists, using the magic black box as a subroutine.

Notes

* Arbitrary Boolean formula = (a and b) or (b and c)
* p-time = O(nk) for some constant k

Answers

The algorithm that can satisfy a given Boolean formula in p-time using the black box described above can use the following strategy:

* Start by checking if the given Boolean formula is satisfiable. If it is not, return/report that no such assignment exists that satisfies this formula. If it is satisfiable, move onto the next step below.
* Start with 1 of the variables in the Boolean formula, set it to true. This means replacing any reference to this variable in the formula with a ‘hard-coded’ value of true. This produces a new boolean formula, b’.
* Use the black box to determine if b’ is satisfiable.
  + If b’ is not satisfiable, that means that the variable we set to true actually has to be set to false for the satisfying assignment. So set the original variable to false, this is its value in the satisfying assignment.
  + Move onto the next variable, set it to true and repeat this process on the new b’.
* Once you have assigned all variable their values for the satisfying assignment of this Boolean formula you can return that.

With the black box all we need to do is iterate over all the variables in the original Boolean expression so we can say that our algorithm runs in O(n) where n is the number of variables in the original Boolean formula.

**Problem 5 (10 points)**

We define the 2Sol-SAT problem as follows.

Input: A, an instance of a SAT formula (A is a conjunction of disjunctive clauses).

Output: 1 if A has at least two satisfying solutions, otherwise, 0.

(a) Show that SAT ≤p 2Sol-SAT.

(b) Show that the 2Sol-SAT problem is in NP.

Notes

* A
  + Map inputs, verify outputs match
* B
  + Show it can be verified in p-time
    - Starting with an answer, in this case the 2 solutions

Answers

1. We can show that SAT reduces to 2Sol-SAT by mapping the input of SAT to that of 2Sol-SAT in polynomial time and then showing that SAT = 1 IFF 2Sol-SAT = 1 for the original SAT input and the mapped input from that to the 2Sol-SAT problem.
   1. In order to map the input, a Boolean formula B, of SAT to 2Sol-SAT we simply add another variable, v, and the following clause to the original Boolean formula B to give us: **B AND (v OR (NOT) v)**.
   2. In order to prove that SAT = 1 IFF 2Sol-SAT = 1 for the given and mapped inputs let us consider the case where SAT = 1 for B. This means there is at least 1 satisfying assignment for all the variables in B. When we map the input B as detailed above and give that to the 2Sol-SAT problem we can see that there are in fact 2 satisfying assignments for the mapped Boolean formula. One with all of the original assignments to B where v = True and one with all of the original assignments to B where v = False. We can also say that if there are no satisfying assignments to B then SAT = 0 and 2Sol-SAT = 0 because it doesn’t matter what v is, B cannot be satisfied so that means **B AND (v OR (NOT) v)** also cannot be satisfied.
2. In order to show that the 2Sol-SAT problem is in NP we need to show that it can be verified in polynomial time. The verification algorithm for the 2Sol-SAT problem would take in the 2 satisfying solutions to input A (the Boolean formula) and verify that both satisfy the Boolean formula. This algorithm runs in p-time because all it needs to do is take both solutions, one at a time, plug in all the satisfying assignments and then compute the result and validate that is the Boolean formula is satisfied. This can be done in linear time (See Lemma 34.5 that shows Circuit-SAT can be validated in linear time).

**Problem 6 (10 points)**

A Hamiltonian Cycle in a graph is a cycle that visits every vertex exactly once. DIRECTED-HAMILTONIANC problem checks to see if a directed graph contains a Hamiltonian cycle. UNDIRECTED-HAMILTONIANC problem does the same for undirected graphs.

1. Describe a polynomial-time reduction from UNDIRECTED-HAMILTONIANC to DIRECTED-HAMILTONIANC.
2. Describe a polynomial-time reduction from DIRECTED-HAMILTONIANC to UNDIRECTED-HAMILTONIAN PATH.

Notes

* Hamiltonian cycle = visits each vertex exactly once
* B
  + Take directed graph, represent it as undirected graph
  + Take directed graph, create graph for each vertex. For any particular vertex’s graph add an edge to any vertex where, in the original directed graph, you had an edge that started at this vertex and went to another. When starting at one vertex and traveling to another, this happens in the graph for the source vertex. When you want to see where you can go once you arrive at the destination vertex you look up that vertex’s graph (tree of one way edges all starting from this vertex)

Answers

1. A polynomial time reduction from UNDIRECTED-HAMILTONIANC to DIRECTED-HAMILTONIANC involves mapping the inputs of UNDIRECTED-HAMILTONIANC to the inputs of DIRECTED-HAMILTONIANC in polynomial time and then showing that the outputs match for both problems given the original input and the mapping function to the other problem.
   1. You can map the input of UNDIRECTED-HAMILTONIANC (a graph, G, with edges, E, and vertices, V) to DIRECTED-HAMILTONIANC (a graph, G’, with edges, E’, and vertices, V’) by simply taking every undirected edge (u, v) in E and creating a directed edge in both directions such that you end up with (u, v) and (v, u). The set of vertices would remain the same: G’ = {E’, V} such that E’ contains (u, v) and (v, u) for every edge (u, v) in the undirected edges in E. This can be done in polynomial time since you only need to iterate over the edges in E.
   2. DIRECTED-HAMILTONIANC will only return true given graph G’, as defined above, IFF UNDIRECTED-HAMILTONIANC will return true given graph G. This is true because the resulting directed graph, G’, is equivalent to the undirected graph G since an undirected edge can be represented as 2 directed edges, one in both directions. So saying there is a Hamiltonian cycle in an undirected graph G is the same thing as saying there is a Hamiltonian cycle in another directed graph where we represent each undirected edge as 2 directed edges, one in both directions.
2. A polynomial time reduction from DIRECTED-HAMILTONIANC to UNDIRECTED-HAMILTONIANC will involve the same steps as above but going in the opposite direction. We will need to map the inputs from DIRECTED-HAMILTONIANC (a directed graph) to UNDIRECTED-HAMILTONIANC (an undirected graph) and then show that UNDIRECTED-HAMILTONIANC returns true given a graph G’ IFF DIRECTED-HAMILTONIANC returns true given graph G.
   1. One way to map a directed graph to