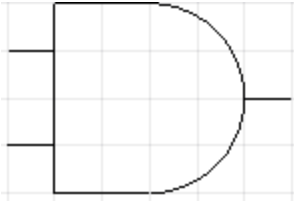


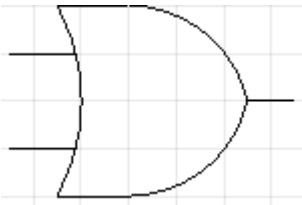
Circuitos Lógicos

AND



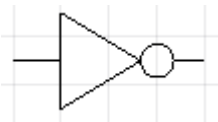
■

OR



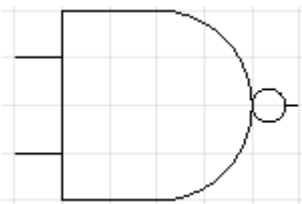
+

NOT



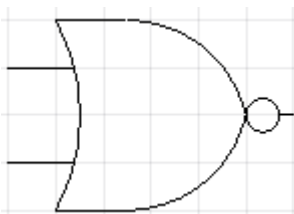
\overline{X}

NAND

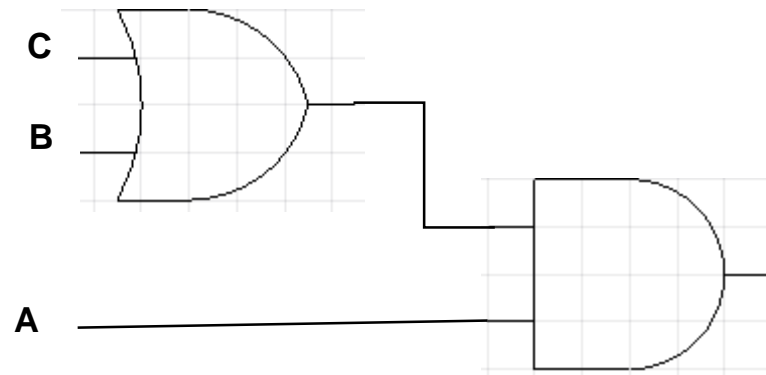


$\overline{(AB)}$

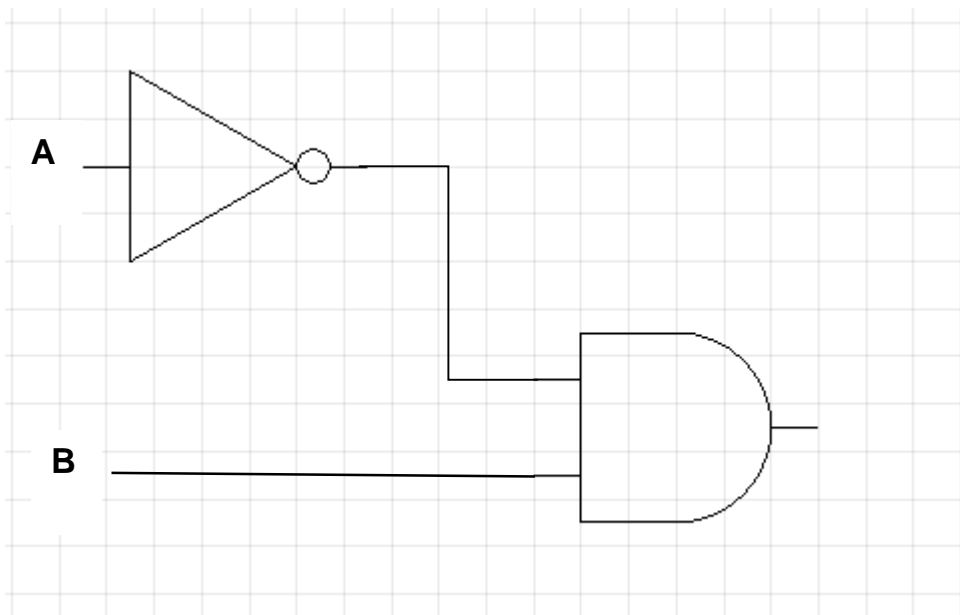
NOR



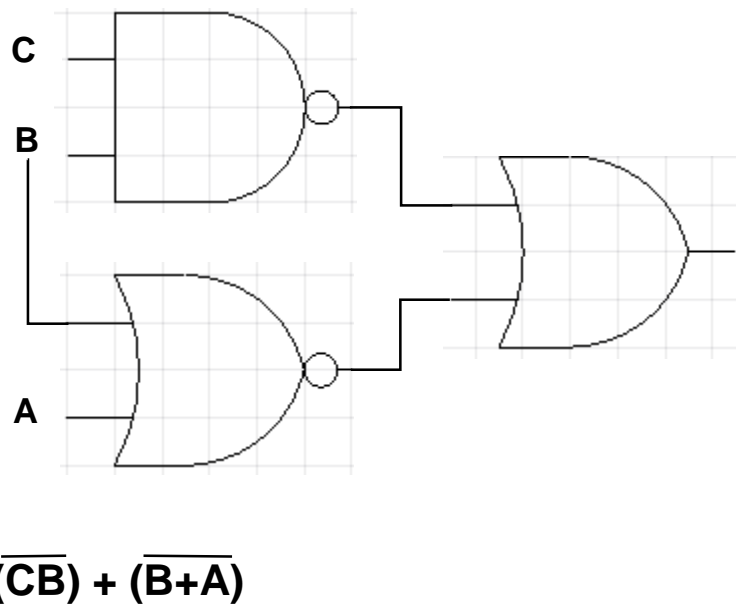
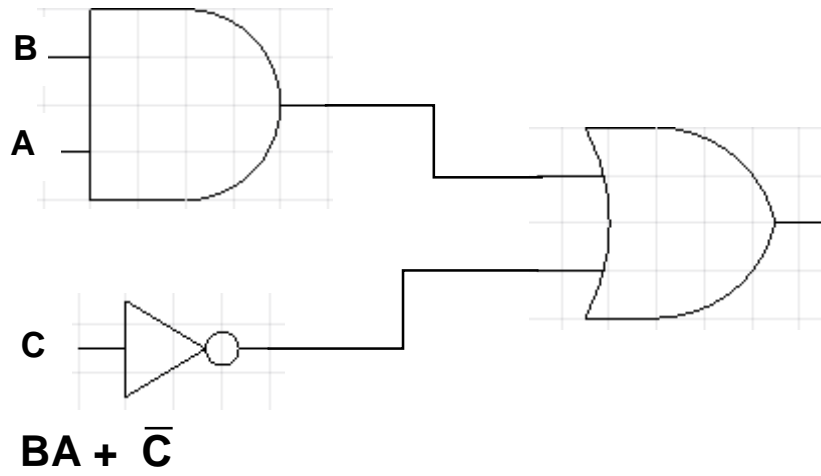
$\overline{(A+B)}$

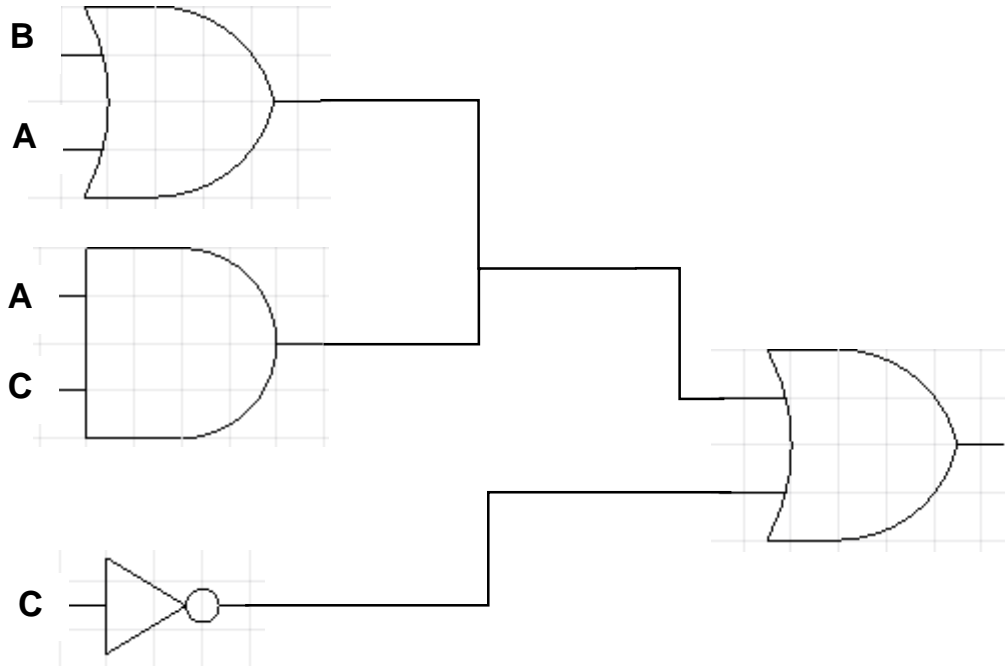


$(C+B).A$



$\bar{A}.B$





$$((B+A) + (AC)) + C$$