**Final Report**

**SOFTWARE ENGINEERING**

TOPIC: SUPPLEMENT FACTS PRODUCTS

Lecturer: **Mr. PHAM THAI KI TRING**

Students: **Ho Vinh Tuong - 520K0091**

**Nguyen Pham Phu Thinh - 520V0012**

**Final Report**

**SOFTWARE ENGINEERING**

TOPIC: SUPPLEMENT FACTS PRODUCTS

Lecturer: **Mr. PHAM THAI KI TRING**

Students: **Ho Vinh Tuong - 520K0091**

**Nguyen Pham Phu Thinh - 520V0012**

Course: **24**

# ACKNOWLEDGEMENT

We send our most sincere thanks to Mr. Pham Thai Ki Trung for always supporting and accompanying us to complete the mid-term report of Software Engineering in the best way. We always hope that you will always be the teacher that accompanies and gives our students the best knowledge so that they can go out and have the most solid knowledge. Once again, thank you very much for your time.

The team would also like to thank the Faculty of Information Technology and the school for creating conditions both physically and mentally so that the group could complete the mid-term report in the best way.

We would like to thank all the teachers who have supported us in the best way. We sincerely thank you!

# THE REPORT IS COMPLETED AT TDT UNIVERSITY

I hereby declare that this is the product of our own project and under the guidance of Mr. Pham Thai Ki Trung. The research contents and results in this topic are honest and have not been published in any form before. The data in the tables for analysis, comments and evaluation are collected by the author himself from different sources, clearly stated in the reference section.

In addition, the project also uses a number of comments, assessments as well as data from other authors, other agencies and organizations, with citations and source annotations.

If I find any fraud, I will take full responsibility for the content of my project. Ton Duc Thang University is not related to copyright and copyright violations caused by me during the implementation process (if any).

*HCMC, December 3rd 2022*

*Authors*

*Ho Vinh Tuong*

*Nguyen Pham Phu Thinh*

# CONFIRMATION AND ASSESSMENT SECTION

**The evaluation of lecturer**

HCMC, December 3rd, 2022

**The evaluation of examiner**

HCMC, December 3rd, 2022

Contents

[ACKNOWLEDGEMENT 1](#_Toc121662543)

[THE REPORT IS COMPLETED AT TDT UNIVERSITY 2](#_Toc121662544)

[CONFIRMATION AND ASSESSMENT SECTION 3](#_Toc121662545)

[A. INTRODUCTION 37](#_Toc121662546)

[1. Purpose and Scope 37](#_Toc121662547)

[2. Product Overview (including capabilities, scenarios for using the product, etc.) 37](#_Toc121662548)

[3. Structure of the Document 37](#_Toc121662549)

[4. Terms, Acronyms, and Abbreviations 37](#_Toc121662550)

[B. PROJECT MANAGEMENT PLAN 37](#_Toc121662551)

[1. Project Organization 37](#_Toc121662552)

[2. Lifecycle Model Used 37](#_Toc121662553)

[3. Risk Analysis 37](#_Toc121662554)

[4. Hardware and Software Resource Requirements 37](#_Toc121662555)

[5. Deliverables and Schedule 37](#_Toc121662556)

[6. Monitoring, Reporting, and Controlling Mechanisms 37](#_Toc121662557)

[7. Professional Standards 37](#_Toc121662558)

[8. Evidence all the artifacts have been placed under configuration management 37](#_Toc121662559)

[9. Impact of the project on individuals and organizations 37](#_Toc121662560)

[C. REQUIREMENT SPECIFICATIONS 37](#_Toc121662561)

[1. Stakeholders for the system 37](#_Toc121662562)

[2. Use case model 37](#_Toc121662563)

[2.1. Graphical use case model 37](#_Toc121662564)

[2.2. Textual Description for each use case 37](#_Toc121662565)

[3. Functional requirements 37](#_Toc121662566)

[4. Non-functional requirements 37](#_Toc121662567)

[D. ARCHITECTURE 37](#_Toc121662568)

[1. Architectural style(s) used 37](#_Toc121662569)

[2. Architectural model 37](#_Toc121662570)

[3. Technology, software, and hardware used 37](#_Toc121662571)

[4. Rationale for your architectural style and model 37](#_Toc121662572)

[E. DESIGN 37](#_Toc121662573)

[1. Database design 37](#_Toc121662574)

[2. Static model – class diagrams 37](#_Toc121662575)

[3. Dynamic model – sequence diagrams 37](#_Toc121662576)

[4. Rationale for your detailed design model 38](#_Toc121662577)

[5. Traceability from requirements to detailed design model 38](#_Toc121662578)

[F. TEST PLAN 38](#_Toc121662579)

[1. Requirements/specifications-based system level test cases 38](#_Toc121662580)

[2. Traceability of test cases to use cases 38](#_Toc121662581)

[3. Techniques used for test generation 38](#_Toc121662582)

[4. Assessment of the goodness of your test suite 38](#_Toc121662583)

[G. DEMO 38](#_Toc121662584)

[1. Database 38](#_Toc121662585)

[2. Source code 38](#_Toc121662586)

[3. Testing 38](#_Toc121662587)

[H. REFERENCES 38](#_Toc121662588)

# INTRODUCTION

## Purpose and Scope

## Product Overview (including capabilities, scenarios for using the product, etc.)

## Structure of the Document

## Terms, Acronyms, and Abbreviations

# PROJECT MANAGEMENT PLAN

## Project Organization

## Lifecycle Model Used

## Risk Analysis

## Hardware and Software Resource Requirements

## Deliverables and Schedule

## Monitoring, Reporting, and Controlling Mechanisms

## Professional Standards

## Evidence all the artifacts have been placed under configuration management

## Impact of the project on individuals and organizations

# REQUIREMENT SPECIFICATIONS

## Stakeholders for the system

## Use case model

## Graphical use case model

## Textual Description for each use case

## Functional requirements

## Non-functional requirements

# ARCHITECTURE

## Architectural style(s) used

## Architectural model

## Technology, software, and hardware used

## Rationale for your architectural style and model

# DESIGN

## Database design

## Static model – class diagrams

## Dynamic model – sequence diagrams

## Rationale for your detailed design model

## Traceability from requirements to detailed design model

# TEST PLAN

## Requirements/specifications-based system level test cases

## Traceability of test cases to use cases

## Techniques used for test generation

## Assessment of the goodness of your test suite

# DEMO

## Database

## Source code

## Testing

# REFERENCES