## Computer Science

1. (10 pts.) The following 8 bits 10101011, are to be sent over a channel along with the cyclic redundancy checks (CRC). If the codeword has n = 11 bits and the divisor is 1011, how many CRC bits will need to be added to the 8 bit message? Show the computing of the CRC.

(10 pts.) Given a 7-bit Hamming codeword 1110111 with odd parity, extract the data bits, determine bit error if any, and if so, correct it. Show your calculation.

3. (4 pts.) Convert the following hexadecimal numbers to decimal. a. F0016 b. 101016 c. 1016 d. 123.4516

C. F00161 = 111000000000 = 3840

C. F00161 = 00010000 = 1111 d. 123.4516

b. F010(6) = 00010000 00010000 0 = 4111 d. 123.4516

4 (4 pts.) Change the following decimal numbers to 8-bit two's complement integers. a. -128 b. -127 c. -1 d. 128

d- 138 = 10000000 6 127 = 01111111 C/ 1 = 2000 0 0 0 1 100 4 4 4 4 4 4.0121111 141

 (10 pts.) The bit pattern 01000011\_11100000\_00000000\_000000002 is stored in memory in 32-bit IEEE format. What is the value of the number in decimal notation. Show your calculation. Hint. 32-bit IEEE format

exponent size is 8 bits

mantissa size is 23 bits

bias is 127

6. (20 pts.) Convert the following numbers in 32-bit IEEE format. Show your calculation.

a. -0.71875 b. 0.1

12. (4 pts.) Write an UNIX command to make all files in the directory "all" experitable by owner.

13. (4 pts.) Write an UNIX command to rename a file named "tmp.cpp" in directory "/home/stu/" to "a.cpp".

(10 pts.) Prove the following two theorems: (1) (x + y) = x ⋅ y (2) (x ⋅ y) = x + y.

16. (10 pts.) The full-adder has three inputs: an X bit, a Y bit, and C<sub>i</sub> bit. The result of the addition of these three bits produces two bits: a sum bit S, and a carry bit C<sub>o</sub>. Design a logic circuit that will perform the full-adder. Construct the truth table, the sum-of-products expression for the S and C<sub>o</sub>, and the circuit.

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