# EES216: Circuit Analysis Midterm Mock Exam

#### curated by The Peanuts

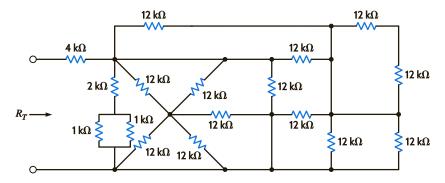
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Conditions: Semi-Closed Book

#### **Directions:**

- 1. This exam has 15 pages (including this page).
- 2. Calculators are permitted (You may bring 100 of them. Haha)
- 3. Write your name clearly at the top of each page.
- 4. Reading the problem is optional but highly recommended.
- 5. You may bring one A3 sheet of note, which will magically become illegible the moment the exam begins.
- 6. Tears shed on your answer sheet may cause short circuits. Please cry responsibly.

Determine the total resistance,  $R_T$ , in the circuit

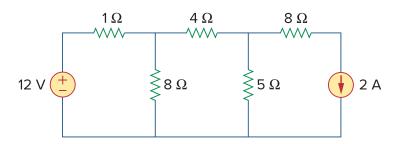


For an ideal operational amplifier (op-amp), state the values of the following characteristics and briefly explain their significance:

- 1. Open-loop voltage gain (A)
- 2. Input resistance  $(R_i)$
- 3. Output resistance  $(R_o)$

Let a be the number of branches in the circuit, b be the number of nodes, and c be the number of meshes. Then, compute the value of:

$$a^2 + b^2 - c$$



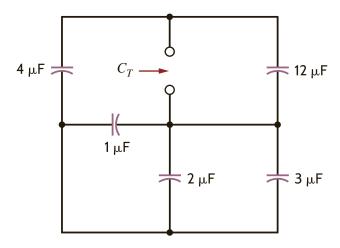
Take the last digit of your student ID and compute:

(last digit) mod 3

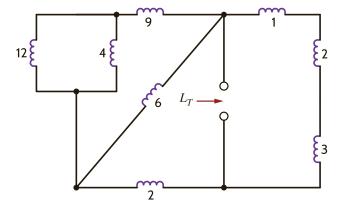
Based on the result:

- If the result is 0, find a limitation of **Mesh Analysis**.
- If the result is 1, find a limitation of **Node Analysis**.
- If the result is 2, find a limitation of **Superposition Theorem**.

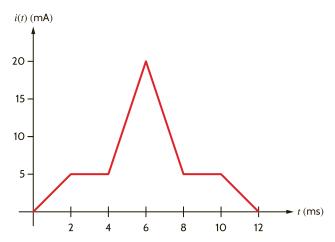
Determine the value of  $\mathcal{C}_T$  in the circuit



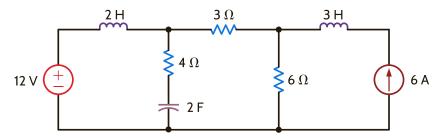
Find the total inductance  $\mathcal{L}_T$  in the circuit, All inductors are in millihenrys.



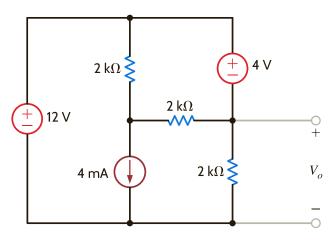
The current waveform in a 40-mH inductor is shown below. Derive the waveform for the inductor voltage.



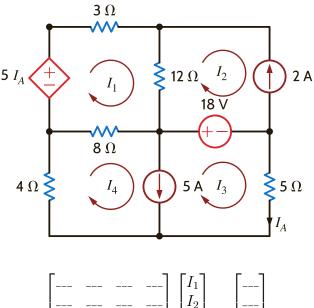
Given the circuit, find the power dissipated in the 3- $\Omega$  resistor and the energy stored in the capacitor.



Find  $V_o$  in the circuit.

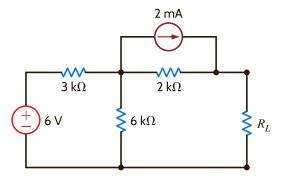


Determine the loop currents  $I_1, I_2, I_3, I_4$  in the given electrical circuit and express the solution in matrix form.

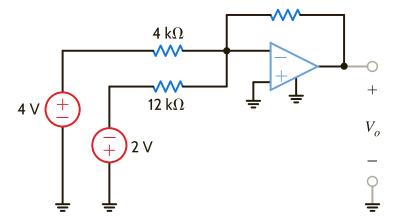


$$\begin{bmatrix} --- & --- & --- & --- \\ --- & --- & --- & --- \\ --- & --- & --- & --- \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} --- \\ --- \\ --- \\ --- \end{bmatrix}$$

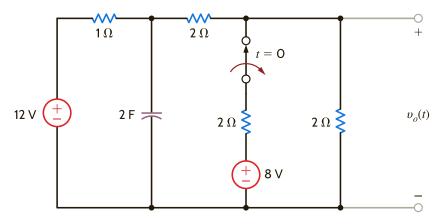
Find  $R_L$  for maximum power transfer and the maximum power that can be transferred to the load. Additionally, draw the Norton equivalent circuit.



Given the summing amplifier shown below, find the values of  $R_2$  that will produce an output voltage of -3 V.



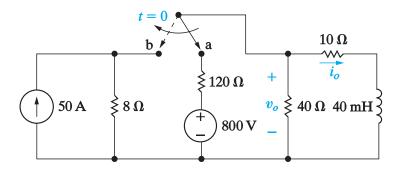
Consider the circuit. The switch opens at t = 0. Find  $v_o(t)$  for t > 0.



Assume that the circuit reaches steady state after a duration equal to five times the time constant. Calculate the exact time at which the circuit reaches steady state.

The switch in the circuit shown below has been in position a for a long time. At t=0, the switch moves instantaneously to position b.

- Find the numerical expression for  $i_o(t)$  when  $t \ge 0$ .
- Find the numerical expression for  $v_o(t)$  when  $t \ge 0^+$ .



#### **Bonus Problem**

The circuit shown below is used by a biology student to study "frog kick." She noticed that the frog kicked a little when the switch was closed but kicked violently for 5 s when the switch was opened. Model the frog as a resistor and calculate its resistance. Assume that it takes 10 mA for the frog to kick violently.

