MSI PC/104 Embedded PC Series

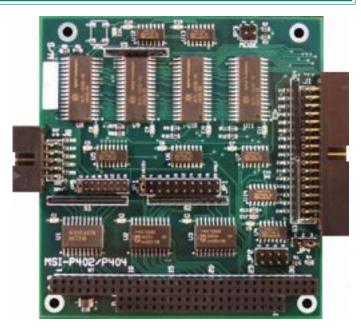
MSI-P404 Quadrature Decoder/Counter Card with Index Inputs

FEATURES

- 2, 4, 6 or 8 channels of quadrature encoder inputs and channel index inputs.
- 32-bit binary up/down counter with selectable 1X, 2X or 4X decoding using a Agilent HCTL-2032 decoder IC.
- Ideal for monitoring shaft positions and rotations in machinery and robotic applications.
- Individual software reset for each channels.
- Selectable interrupts IRQ3 thru IRQ9 for processing roll-overs at ±2,147,483,647 counts.
- Single +5V operation.
- 8-bit stackthrough PC/104 with I/O mapped 16-bit addressing.
- Jumper selectable card addresses.
- Operating temperature range -40°C to 85°C.
- Two-year warranty from date of shipment.

DESCRIPTION

The MSI-P404 is a 32-Bit quadrature decoder/counter PC/104 card designed for monitoring up to 8 quadrature encoder inputs with index inputs used in monitoring shaft positions and rotations. Each channel provides a 32-bit binary up/down counter with selectable 1X, 2X or 4X decoding using an Agilent HCTL-2032 decoder



IC. This device provides a digital noise filter network, decoding logic, a 32-bit counter, and a 32-bit latched output for two quadrature decoder channels. A block diagram of the card is shown below. Inputs from quadrature encoders are applied via an input connector (J1) that requires a frequency and a reference signal input for each channel in use. Index inputs are applied by a separate connector (J2) and can be individually disabled. Inputs signals are TTL levels. The clock employed for processing the input signals is selectable from SYSCLK (6 to 8.33 MHz,

QUADRATURE HP HCTL-2032 PC/104 8-BIT STACKTHROUGH A6-A15 ENCODER INPUTS DECODER **JUMPERS** (CH 1 and 2) (CH 1-8) HP HCTL-2032 CONNECTOR MSI-P404 **DECODER** PC/104 (CH 3 and 4) **BUS INTERFACE INDEX** HP HCTL-2032 NETWORK **INPUTS** DECODER (CH 5 and 6) (CH 1-8) IRQ3-IRQ9 HP HCTL-2032 **JUMPERS** DECODER (CH 7 and 8) **INDEX DISABLE** JUMPERS (CH 1-8) **BLOCK DIAGRAM**

depending on the processor card used, or OSC at 14.318 MHz) of the PC/104 bus. As an option, clock oscillators from 2 to 33 MHz are available. In addition to the frequency and reference quadrature inputs, +5V and GND connections are provided on the input connector (J1) for supplying power to the encoder of each channel. Surge protectors are provided on each all quadrature and index inputs to protect against damage due to voltage surges in noises environments.

The maximum frequency that can be applied to either a frequency or reference input is

$$f_{max} = CLK/7$$

Therefore, for CLK = 8.33 MHz, f_{max} = 1.19 MHz and for CLK = 14.318 MHz, f_{max} = 2.045 MHz.

Each channel has a 32-bit up/down counter and an output latch. When the frequency input leads the reference input (nominally by 90 degrees), the counter counts up. Conversely, when the reference input leads the frequency input (nominally by 90 degrees), the counter counts down. Counts range from 0 to FFFFFFFF hexadecimal (0 to 4,294,967,295 decimal). Data reads require four Byte I/O reads to acquire the 32-bit count of each channel. Roll-over occurs for 0-to-FFFFFFFF and FFFFFFFF-to-0 count transitions. These transitions are OR'ed together for use with interrupts IRQ3 thru IRQ7 and IRQ9 if required. The monitoring software must account for the roll-over events.

A software reset is provided for each channel that sets the count to 0. The card is an 8-bit stackthrough unit that requires +5V from the PC/104 bus.

SPECIFICATIONS

PC/104

8-bit, stackthrough

Quadrature Encoder & Index Inputs

No. of Channels 2, 4, 6 or 8

Low level 1.5V Max.* High Level 3.5V Min.* Input Impedance 10 K Ω *

Surge Supression Varistors on each input

* Note: 10K pull-up on all inputs.

Input Frequency

 $f_{max} = SYSCLK/7$

 $f_{max} = 1.19 \text{ MHz for SYSCLK} = 8.33 \text{ MHz}$

 $f_{max} = 2.045 \text{ MHz}$ for SYSCLK = 14.318 MHz

Input Connector (J1)

Quadrature Inputs, +5V and GND

One (1) 3M 303XX-5002 or equivalent.

XX = 10, 2 channels.

XX = 16, 4 channels.

XX = 26, 6 channels.

XX = 34, 8 channels.

Input Connector (J2)

Index Inputs

One (1) 3M 30310-5002 or equivalent.

Base Address, Interrupt, Count Mode, and Clock Selection Jumpers (JP1-JP4)

0.025" square posts, 0.1" grid

Index Input Disable Jumpers (JP5)

0.020" square posts, 2 mm grid

Electrical & Environmental

+5V @ 2 mA/Channel typical -40° to 85° C

Models

MSI-P404 - X, X = no. of channels (2, 4, 6, 8).



MICROCOMPUTER SYSTEMS, INC.

1814 Ryder Drive • Baton Rouge, LA 70808 • Phone (225) 769-2154 • Fax (225) 769-2155 Email: staff@microcomputersystems.com http://www.microcomputersystems.com