Niranjan Ramadas

Engineer

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Jan 2013 – Mar 2013

App Academy Projects

Quick Chess | quickchess.herokuapp.com | github.com/nramadas/WebChess

- Built an anonymous chess game. Backend in Ruby and Rails, frontend in JavaScript.
- Created custom chess game logic in Ruby. Design was test-driven and highly object oriented.
- Added ability to play across multiple devices, using pull requests to update the game board in real time.

Simple Pub | simplepub.herokuapp.com | github.com/nramadas/EbookReader

- Built an online ePub reader. Used Ruby and Rails with extensive usage of JavaScript to render book pages.
- Added de-duplication of eBooks, omniauth login for Google and Dropbox, multiple file upload with drag and drop, and background jobs for scanning Dropbox folders.

Aug 2010 – Dec 2012

Capgemini North America

Project Analyst

- Worked with Excel over massive data sets to compile and monitor raw data into usable sets. Reports generated were viewed by several key decision-makers, including the CIO.
- Coordinated problem-incident ticket efforts, helping to ensure that service level agreements were met.
- Verified invoices and contracts

SAP CRM Software Consultant

- Worked with developers, clients, and backend teams to maintain service and update functionality of the client's ERP software
- Coordinated efforts amongst a team spanning three countries (USA, Romania, and India) to successfully complete multiple regression tests of the system.

Sept 2009 – Dec 2009

Senior Design Project 1 – VLSI

RISC Microprocessor

- Created a RISC microprocessor that utilized the RAZOR concept. RAZOR allowed the microprocessor to be clocked to the average instruction completion time as opposed to the maximum. Use of RAZOR improved clock speed by roughly 30%.
- Utilized both manual CAD layouts and computer generated designs to optimize for space and power consumption.

Sept 2008 – Dec 2008

Senior Design Project 2 – Computer Architecture

Out-of-order Microprocessor | github.com/nramadas/Senior-Design-1-Architecture

- Designed a dual fetch, dual commit out-of-order processor using Verilog code.
- Created features that made best use of manpower (3 person group) and time (70 days). Final feature list included a pre-fetching instruction cache, non-blocking data cache, branch predictor, and load store queue.

Jan 2009 – Apr 2009

Project – Embedded Systems

Autonomous Motion-Sensing Robot | github.com/nramadas/Embedded-Systems-Project

- Designed a motion sensing robot that avoids objects that move towards it by moving away.
- Used ultrasound sensors to detect motion, infrared sensors to avoid obstacles, and servos to move the device. Used extensive debugging to determine best sensor usage to eliminate noise.
- Applied use of intelligent interrupt routines to make the algorithm resource-efficient.

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Sept 2006 – Apr 2010

University of Michigan Ann Arbor, College of Engineering

B.S.E. Computer Engineering

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Technologies

Ruby, Rails, Python, JavaScript, CoffeeScript, jQuery, C, C++, HTML, CSS, Git