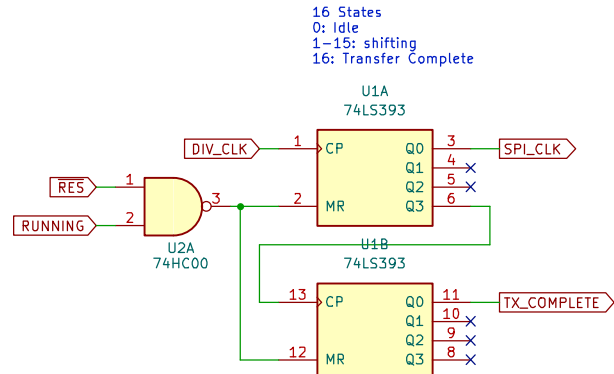
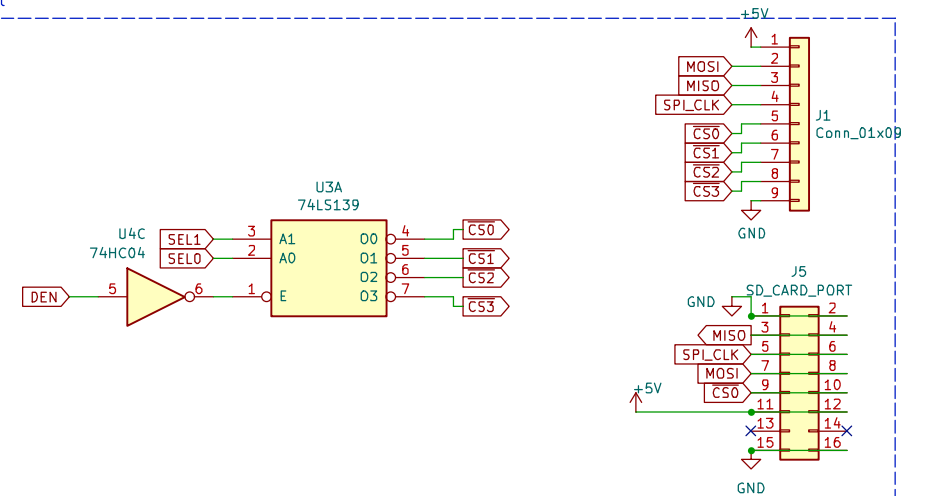


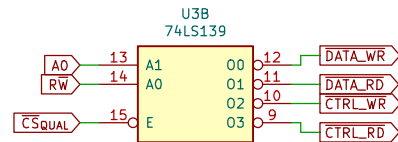
Finite State Machine



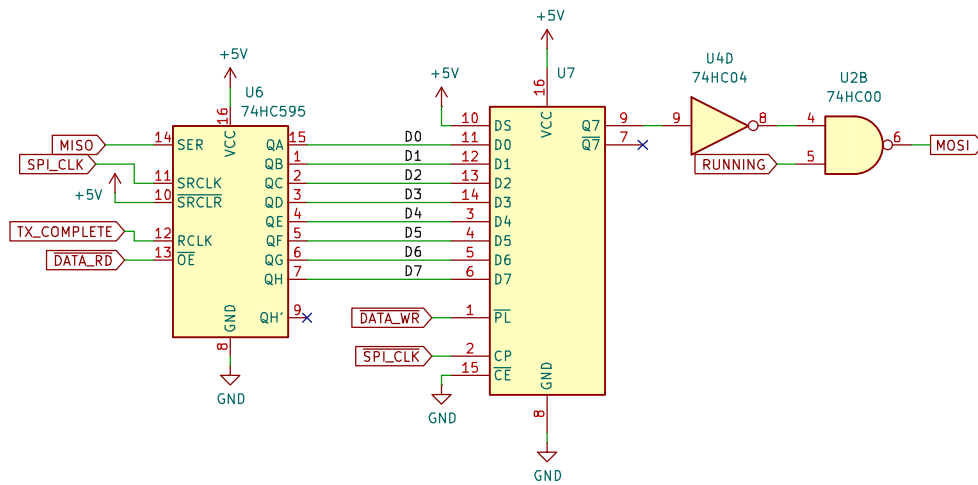
Device Select



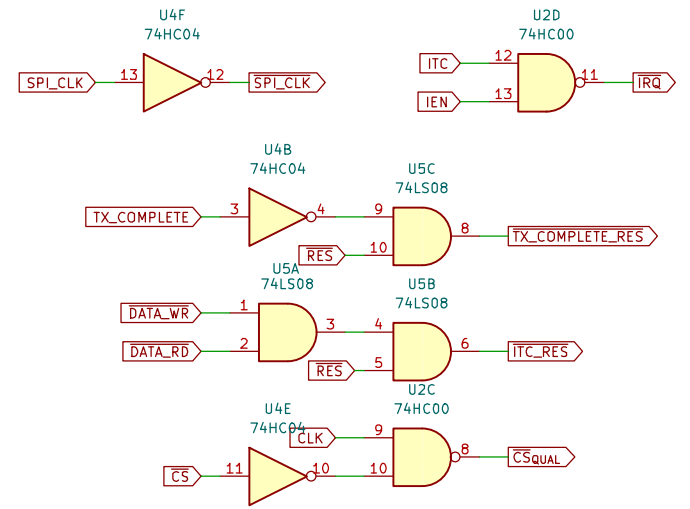
Register Decoding



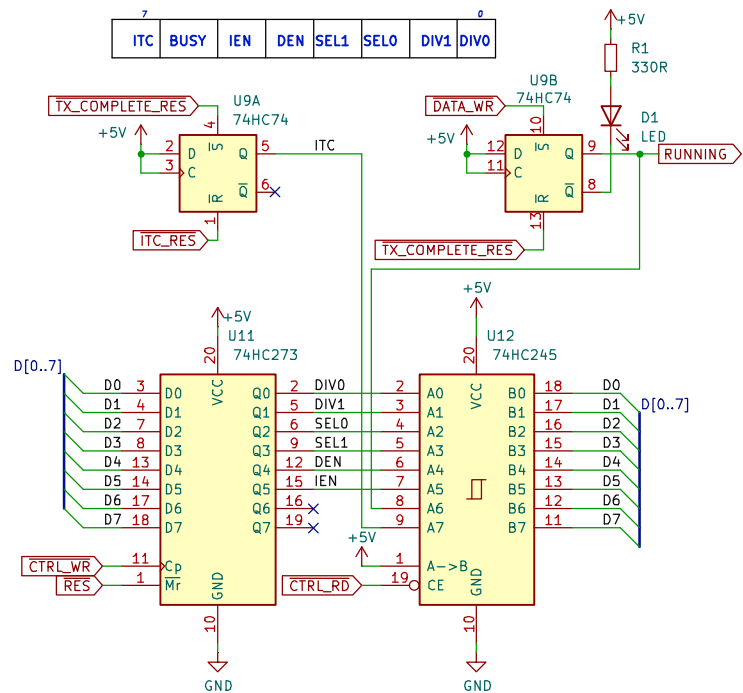
Data (Shift) Registers



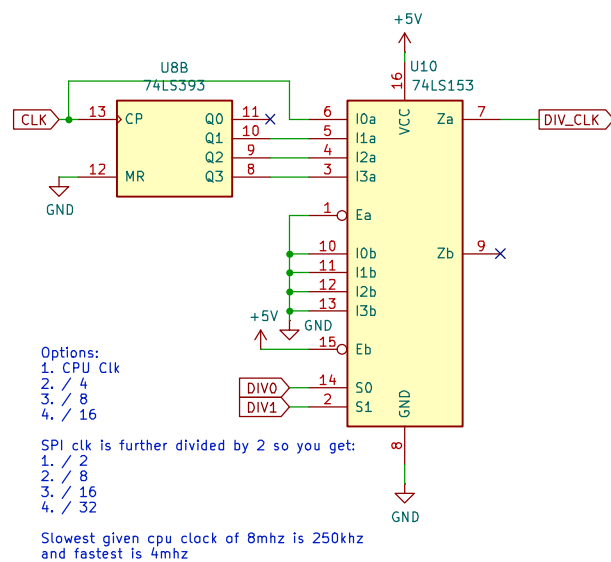
Other Signal Generation



Control Register



Clock Select



Power and Decoupling

