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Revisions			
Rev	Description	Date	Approved
X1	Original Release	DEC-17-2014	O. Romero
X2	Second Release	JAN-30-2015	O. Romero
A	Production Release	FEB-03-2015	O. Romero
B	2nd Production Release *J1-9 Connected to 3V instead of 5V_VDDA_pwr *J1-3 Connect to VDD instead of 3V3 *Added U107 *Replaced R70L *Placed C48 and C47 after D9 and D13 *Replaced C18 with 10 uF cap. *Placed U14 *Changed net name RS-HV to V18 *Relocate PTMG channels *Relocate signals on C202 to C212 *Place R11 and CMP R94 *Changed C84 and tie header with right angle headers *Place R105 by default and leave R104 DNP *V11 pin J connected to VBAT *Replaced R80 with two 2K resistors in parallel *Removed L3 *Removed C14 *Placed D84 button, R118, R116 and C46 *Placed U18 *Button resistors changed to pull-down. *Changed J1 voltage regulator *Changed Q1 translator *Replaced D16 D13 and D6 *Replaced C15 and C18	JUL-06-2015	J. Romero J. Sanchez

FREEDOM+ KEA



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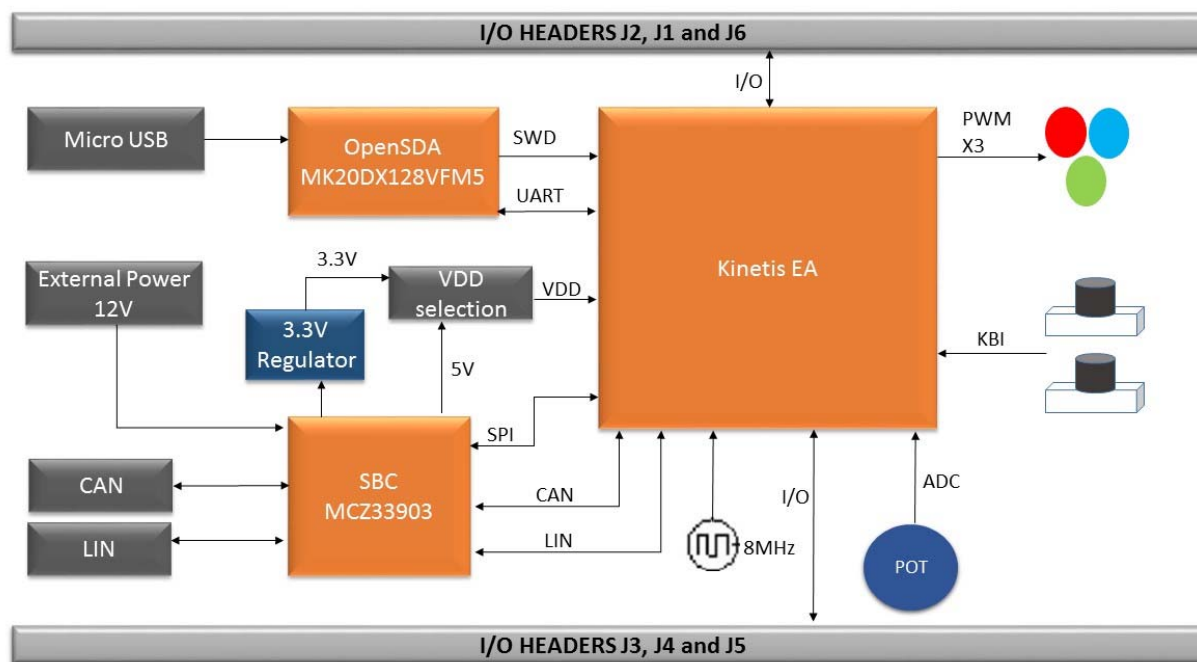
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- Unless Otherwise Specified:  
All resistors are in ohms, 1% and 5 %  
All capacitors are in uF, 10% , 20 % and 5%  
All voltages are DC  
All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- Special signal usage:  
\_B Denotes - Active-Low Signal  
<> or [] Denotes - Vectored Signals
- Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



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# OpenSDA Interface

