5	4		3	2	1			
Table of Contents					Revisions			
2 Notes					Rev	Description	Date	Approved
3 KEA MCU	1				l I	Original Release	DEC-17-2014	
4 OpenSDA					X2	Second Release	JAN-30-2015	O. Romero
5 Power Supply/CAN/LIN	7						FEB-03-2015	O. Romero
6 I/O Headers	7				В	2nd Production Release *J3-9 Connected to 5V instead of P5V_SDA_PSW *J3-3 Connect to VDD instead of 3V3	JUL-06-2015	J.Romero J.Sanchez
7 Headers Multiple Functions						*Added J107 *Replaced XTAL *Placed C48 and C47 after D9 and D13		J. Sanchez
	_					*Replace C48 with 10 uF cap.		
						*Changed net name P5-9V to Vin *Relocate FTM2 channels *Relocate signals on J202 to J212		
						*Place R11 and DNP R94 *Changed CAN and LIN header with right angle headers *Place R105 by default and leave R104 DNP		D
						*J11 pin 2 connected to VBAT *Replaced R80 with two 2K resistors in		
						parallel. *Removed L3 *Removed C34		
						*Placed SW4 button, R118, R116 and C46 *Placed J14 *Button resistors changed to pulldown		
						*Changed 3.3 voltage regulator *Changed 01 transistor		
						*Replaced D9, D13 and D6 *Replaced C35 and C38		
								Ц
								c

FREEDOM+ KEA

Automotive Processing Product Line 6501 William Cannon Drive West Austin, TX 78735-8598

ICAP Classification: FCP:

FRDM+KEA
TITLE PAGE

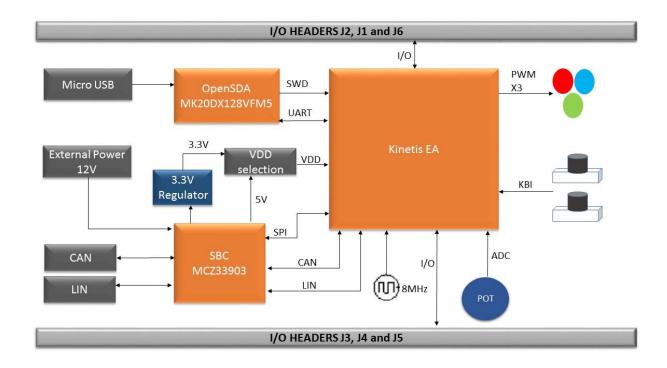
NO

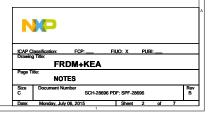
Designer: Osvaldo Romero 1. Unless Otherwise Specified:

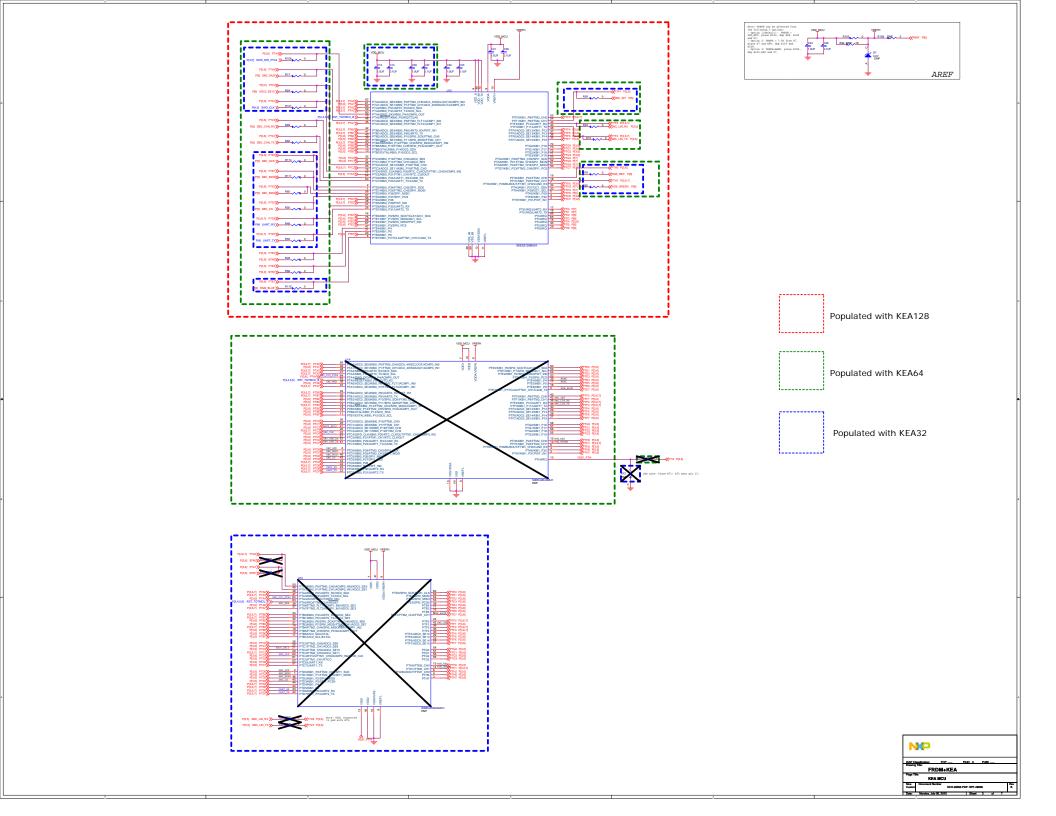
All resistors are in ohms, 1% and 5 % All capacitors are in uF, 10%, 20 % and 5% All voltages are DC

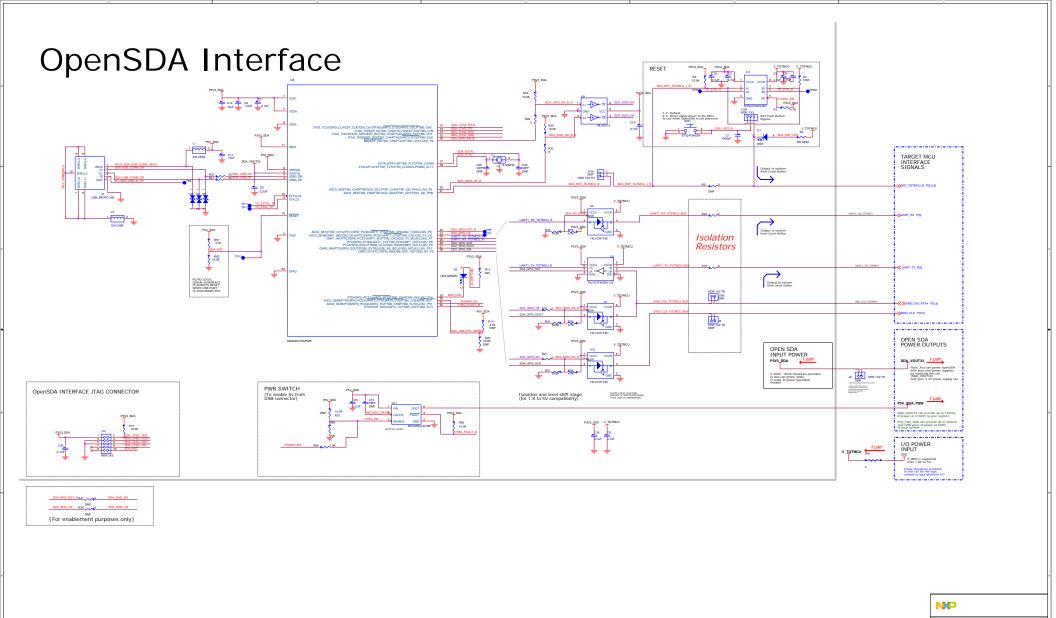
All polarized capacitors are aluminum electrolytic

- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes Active-Low Signal <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.









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